Conference on Computing in High Energy and Nuclear Physics



Contribution ID: 453

Type: Talk

The new hardware trigger processor at NA62 experiment: Status of the System and First Results

Monday 21 October 2024 14:24 (18 minutes)

The NA62 experiment is designed to study kaon's rare decays using a decay-in-flight technique. Its Trigger and Data Acquisition (TDAQ) system is multi-level, making it critically dependent on the performance of the inter-level network.

To manage the enormous amount of data produced by the detectors, three levels of triggers are used. The first level L0TP, implemented using an FPGA device, has been in operation since the start of data taking in 2016.

To increase the efficiency of the system and implement additional algorithms, an upgraded system (L0TP+) was developed starting in 2018. This upgrade utilizes a high-end FPGA available on the market, offering more computing power, larger local memory, and higher transmission bandwidth.

We have planned tests for a new trigger algorithm that implements quadrant-based logic for the veto systems. This new approach is expected to improve the main trigger efficiency by several percent.

Extensive tests were conducted using a parasitic setup that included a set of Network TAPs and a commodity server, allowing for proficient comparison of trigger decisions on an event-by-event basis. The experience gained from this parasitic mode operation can be leveraged for the next data-taking period as a development setup to implement additional features, thereby accelerating the TDAQ upgrade.

After the testing period, the new system has been adopted as the online processor since 2023. Preliminary results on the efficiency of the new system will be reported. Integration with the new AI-based FPGA-RICH system, which performs online partial particle identification, will also be discussed.

Authors: LONARDO, Alessandro (INFN, Roma I (IT)); BIAGIONI, Andrea (INFN, Roma I (IT)); CHIARINI, Carlotta (Sapienza Universita e INFN, Roma I (IT)); ROSSI, Cristian (Sapienza Universita e INFN, Roma I (IT)); LO CICERO, Francesca (INFN, Roma I (IT)); Dr SIMULA, Francesco (INFN, Roma I (IT)); PONTISSO, Luca (INFN, Roma I (IT)); TURISINI, Matteo (Cineca); RAGGI, Mauro (CERN and Sapienza); MARTINELLI, Michele (INFN, Roma I (IT)); FREZZA, Ottorino (Sapienza Universita e INFN, Roma I (IT)); FREZZA, Ottorino (Sapienza Universita e INFN, Roma I (IT)); FREZZA, Ottorino (INFN, Roma I (IT)); CRE-TARO, Paolo; VICINI, Piero (INFN Rome Section); PERTICAROLI, Pierpaolo (INFN, Roma I (IT)); AMMENDOLA, Roberto (INFN Roma Tor Vergata (IT)); PIANDANI, Roberto (Univ. Autonoma de San Luis Potosi)

Presenters: FREZZA, Ottorino (Sapienza Universita e INFN, Roma I (IT)); FREZZA, Ottorino (INFN, Roma I (IT))

Session Classification: Parallel (Track 2)

Track Classification: Track 2 - Online and real-time computing