# **Evaluating a File-based Event Builder to enhance the Data Acquisition in the CMS Experiment**



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# **CMS Data Acquisition System**

### Architecture for the LHC RUN 3 (2022-2026)



## **Alternative File-Based Event Builder**

In the LHC Phase-2, **filter farm** processes need to **assemble events** from the given orbit for analysis. Why not **build** the **event** or **orbit** directly within the **filter farm**? By doing so, the step of moving data between super-fragments and the event or orbit building process can be eliminated.

- **Benefits** of building events or orbits in the filter farm process:
- No need for the Event Backbone network resulting in cost savings on network line cards and NICs;
- Simplifying the event builder code reduces its task to only building super-fragments;
- 3. Building events or orbits entirely in RAM disk

Super-fragment Builder (SFB)	The SFB constructs multiple super-fragments based on the grouping of FEDs and stores them in local RAM disks.
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The event builder in the CMS DAQ is responsible for assembling events or orbits. Both the current and future DAQ architectures make use of state-of-the-art network technologies, employing Ethernet switches that support RoCE protocols. The DAQ frontend hardware is custom-designed, using a reduced TCP/IP protocol implemented in FPGA for reliable data transport between custom electronics and commercial computing hardware.

The files created by the event builder, each containing around 100 events or one orbit, are distributed over NFS among filter farm processes using RAM disks.

#### Architecture for the LHC Phase-2 (starting in 2030)



on filter farm PCs **reduces** the overall number of **memory copies** in the data flow, which would not be the case if the BU applications were moved to the filter farm PCs.



## **Super-fragment Builder**

A super-fragment consists of the data read by one or more Front-Ends and corresponding to the same L1 accept or orbit, and the SFB constructs multiple super-fragments corresponding to the number of Read-Unit (RU) machines in the DAQ system, storing them in local RAM disks. In the LHC Phase-2, the DAQ custom-designed hardware will identify potential issues during data taking. In contrast, in the LHC Run 3, this task is handled by the EVB, which needs to be integrated with the SFB.

Preliminary **performance** tests of the SFB using DAQ 3 hardware were conducted the at beginning of the year with the **DAQ 3 Emulator**. By dropping data in the **RAM disk** (without HLT), the SFB was able to reach the nominal 115 kHz L1 rate for the LHC Run 3.





build
2) Asynchronously Event fragments from FEROL to EVM & RUs
3) EVM schedule & Control:

A. EVM receives enough event fragment from TCDS FED to satisfy one request from N SFWs
B. EVM sends a message to all RUs with L1T numbers of the events together with the SFW ID and all IDs of RU contributing with super-fragment

4) EVM and RUs build the super-fragments of the requested events and send them to their SFW

1) SFW requests to EVM N resource to

- 5) The SFW after receiving a superfragments writes it to the RAM disk
  A) SFWa act the number of superts for a
- A) SFWs get the number of events for a given lumisection from EVM

RU machine



All to all HLT input over NFS test (62 RU x 196 FU) with Run 3 DAQ 8000 7000

## **Builder Filter Farm File Based (B3F)**



This plot shows the B3F performance using DAQ 3 RUBU nodes of **all-to-all** HLT NFS using TCP/IP operation with a script running in each node to



6000

Run 3.

#### generate RAM disk files using real event sample:

- Total throughput Limited by top of the rack switch uplinks 40 x 100 GbE (8 links per rack);
- Better result with larger files (nearly 90% of the line speed);
- Next step to explore NFS over RDMA.

#### **Future Work**

2.7 MB per input

In the LHC Run 3, the main goal is to develop and commission the File-based Event Builder during periods when data is not being taken. The target for the File-based Event Builder is to go operational for data taking during the final years of the LHC Run 3, the DAQ architecture will need to be updated and take advantage of new technologies as part of a redesigned system.



**"Track 2 - Online and real-time computing**" as Poster 14 in session "Poster" on Tuesday, the 22<sup>nd</sup> of October 2024

**RUBU** machine

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