

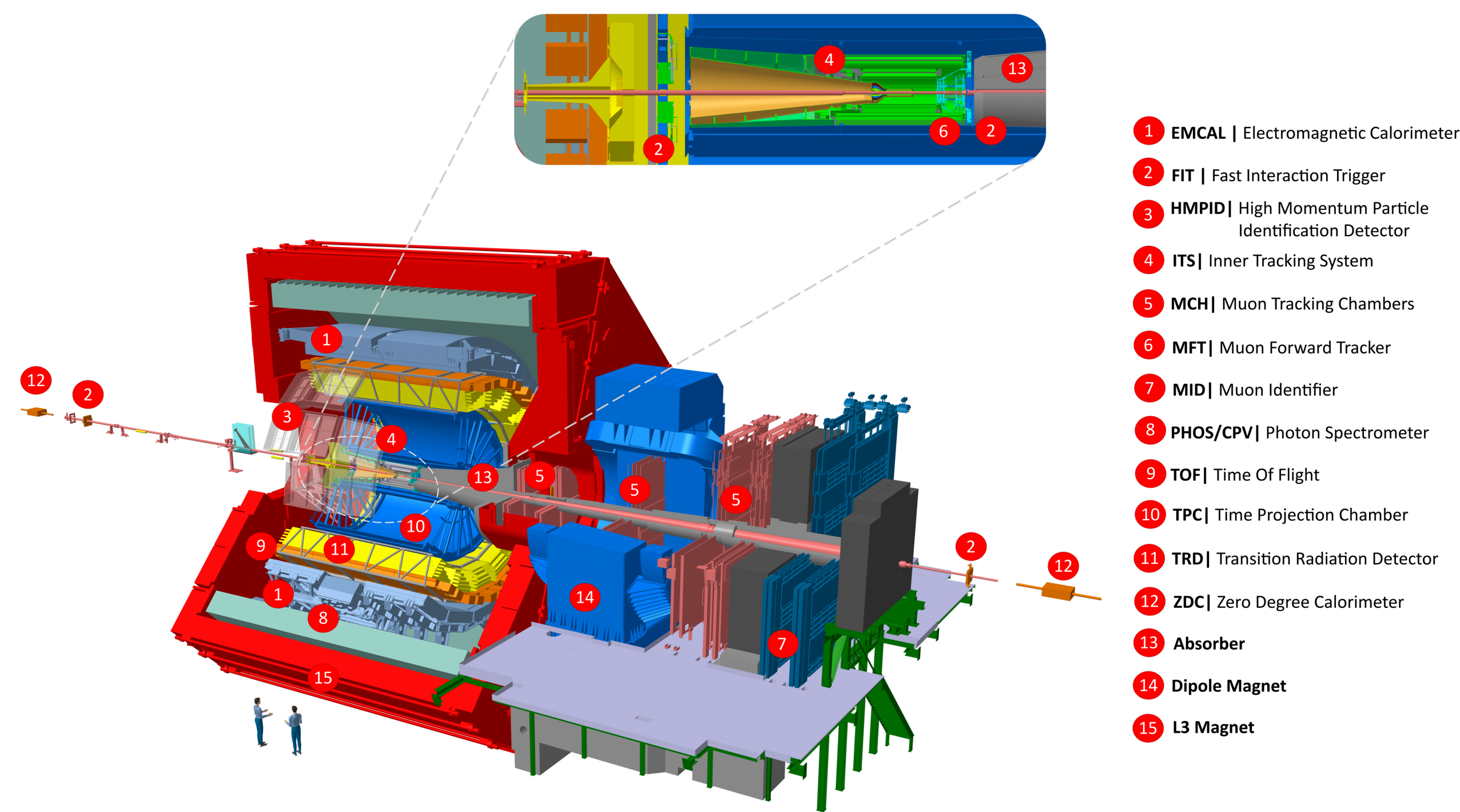


INTEGRATING IPBUS ALFRED INTO THE ALICE-FIT SETUP



Krystian Roslon on behalf of the ALICE Collaboration
 Warsaw University of Technology, Poland, krystian.roslon@pw.edu.pl

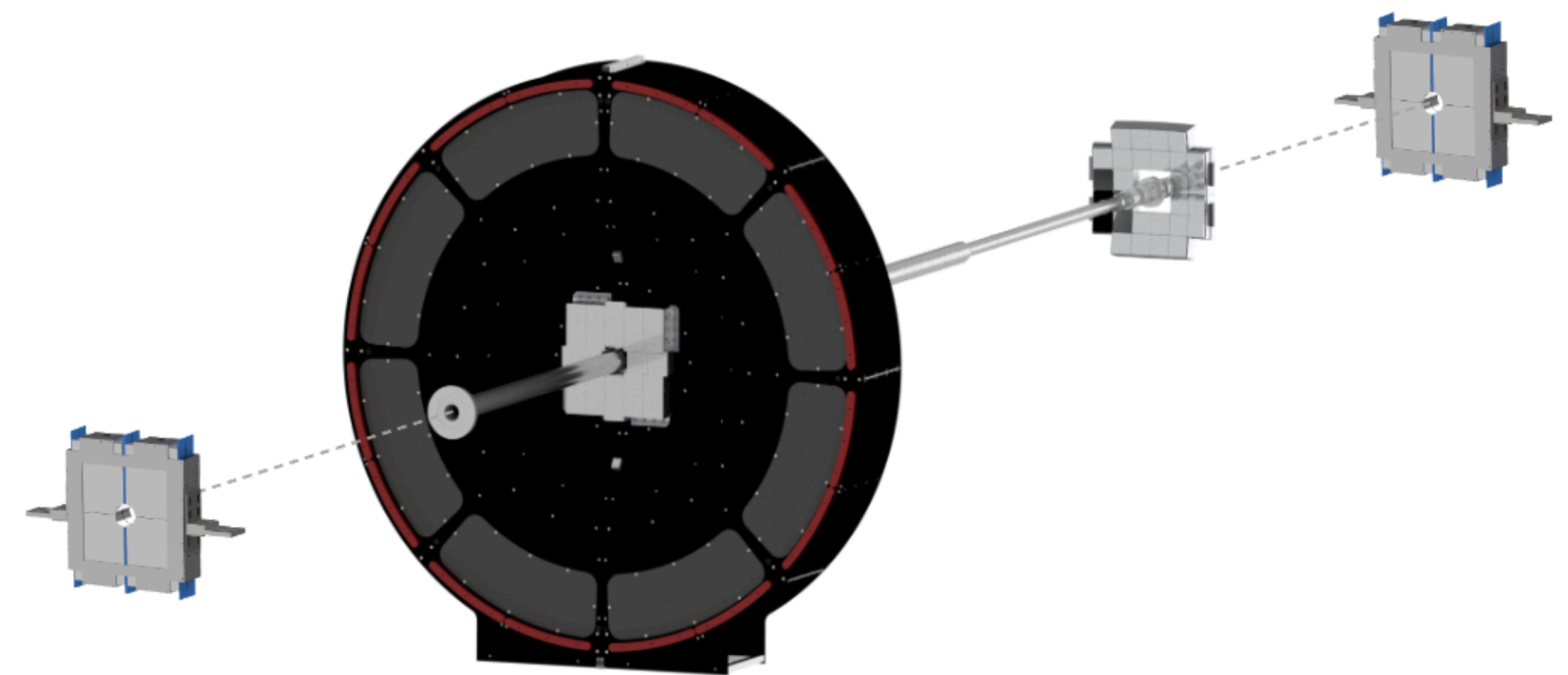
A LARGE ION COLLIDER EXPERIMENT (ALICE)



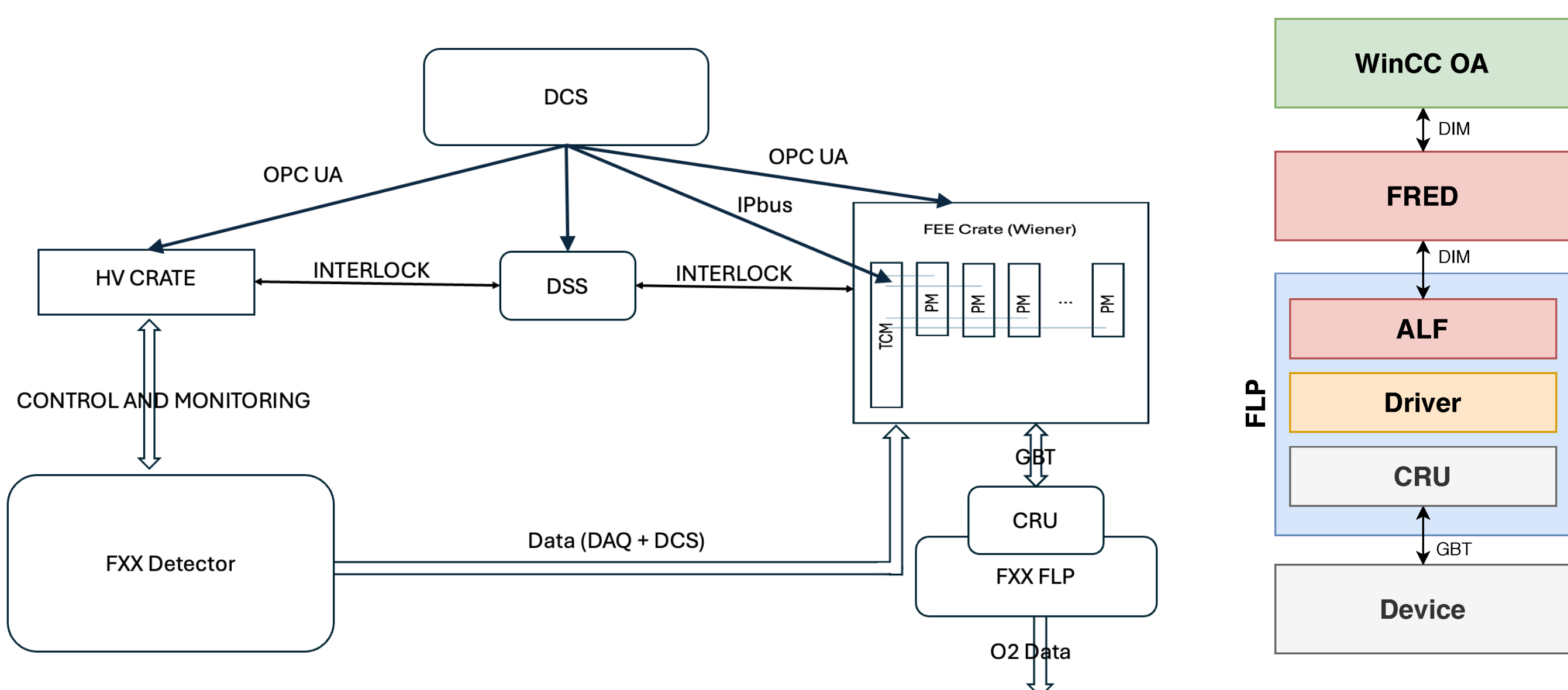
ALICE - FAST INTERACTION TRIGGER (FIT)

Delivered Functionality

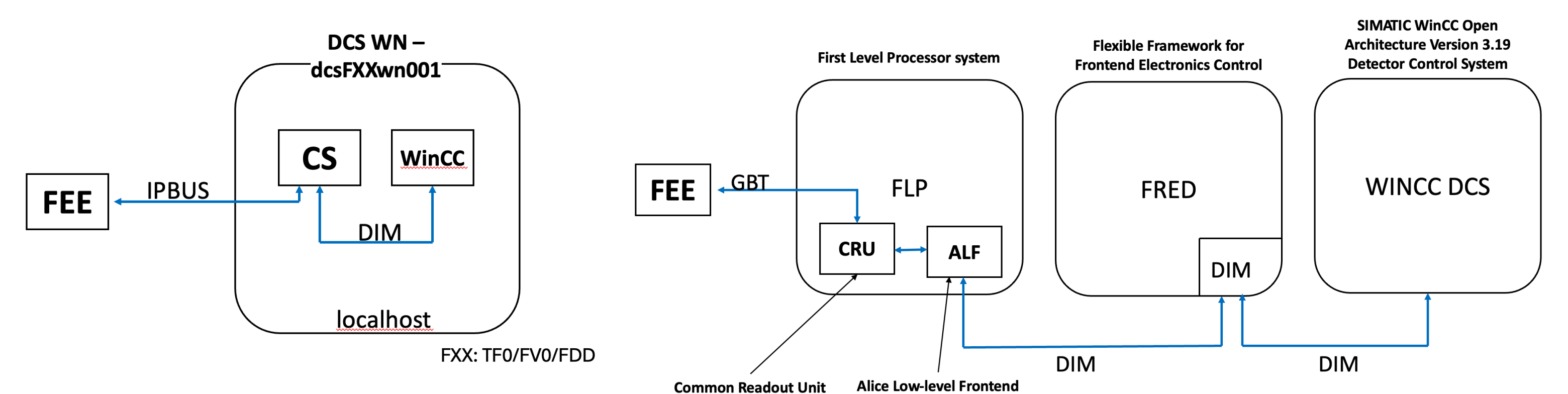
- Fast min. bias collision trigger with latency < 425 ns;
- Time resolution: 5 ps in Pb-Pb and 18 ps in pp collisions;
- Particle Identification signal for TOF;
- Luminosity and background monitor;
- Centrality and multiplicity measurement;
- Event plane determination;
- Diffraction physics measurements;
- Veto trigger for UltraPeripheral Collision;



FIT DETECTOR CONTROL SYSTEM (DCS)



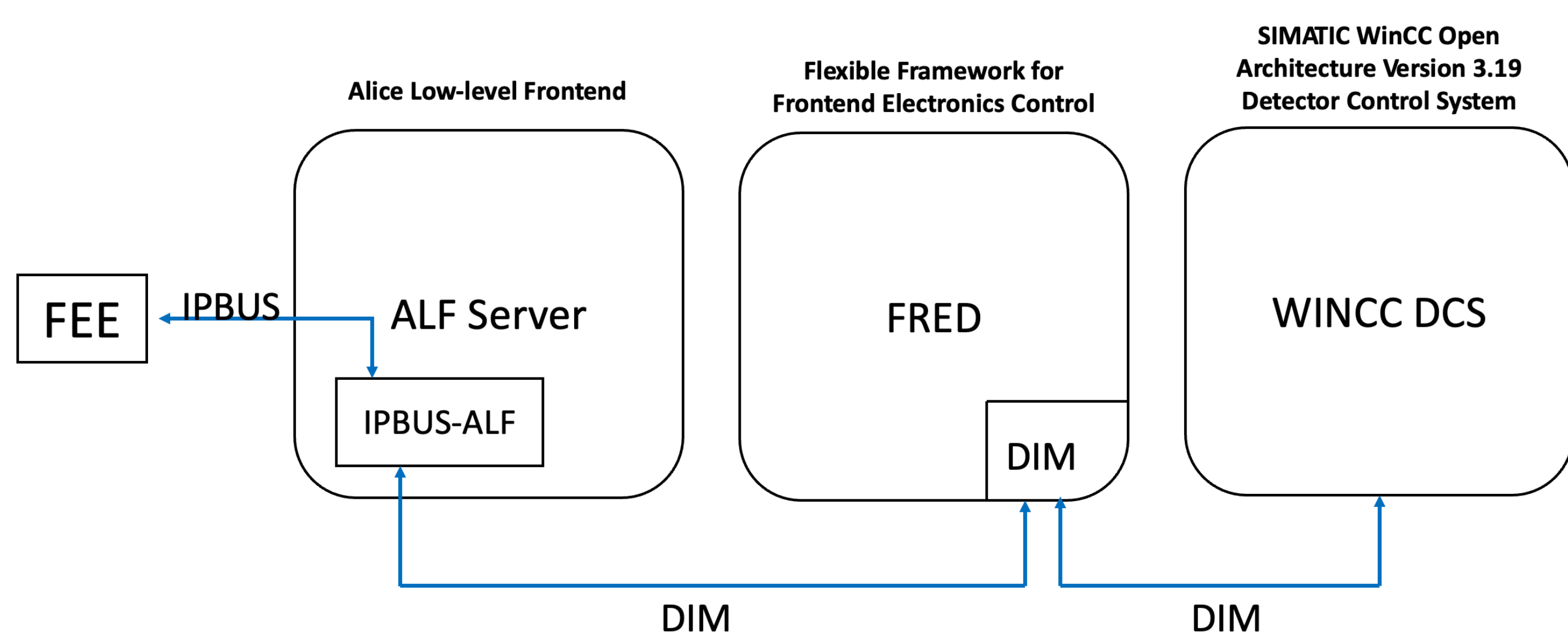
ACTUAL AND FINAL DCS IMPLEMENTATION



ACTUAL IMPLEMENTATION

FINAL IMPLEMENTATION (LHC RUN4)

IPBUS INTEGRATION IN ALFRED (LHC RUN3)



CONCLUSION AND FURTHER WORKS

- A COMMUNICATION ARCHITECTURE FOR THE DETECTOR CONTROL SYSTEM TAILORED FOR THE FAST INTERACTION TRIGGER WAS PROPOSED.
- THE IPBUS PROTOCOL WAS EXPLORED FOR COMMUNICATION WITH THE FRONT-END ELECTRONICS (FEE), APPLICABLE TO VARIOUS DETECTORS.
- THE ARCHITECTURE INTEGRATES DISTINCT COMPONENTS: WINCC OA, FRED, ALF, DIM, AND IPBUS.
- FUTURE WORK WILL FOCUS ON REPLACING THE IPBUS-ALF STRUCTURE WITH A SYSTEM THAT TRANSLATES THE SWT PROTOCOL INTO IPBUS.
- THE FINAL GOAL IS TO DEVELOP A PLUG-AND-PLAY SYSTEM FOR SEAMLESS COMMUNICATION WITH THE CONTROL READOUT UNIT AND TESTING ARCHITECTURE BANDWIDTH.

