

# CALICE, a legacy



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\* with many slides adapted from R. Pöschl, F. Simon, O. Wataru, I. Laktineh,, K. Krüger and others.



**NUCLÉAIRE  
& PARTICULES**

# Introduction : Calorimetry and Particle Flow

## Classical Calorimetry in HEP :

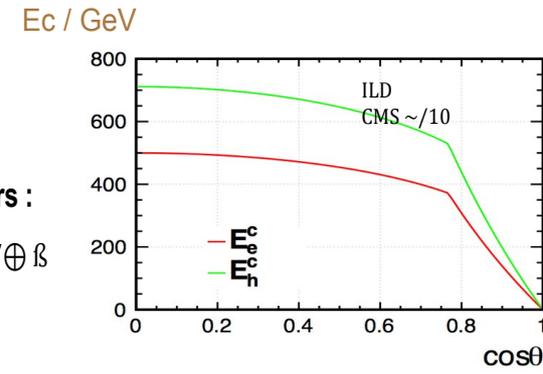
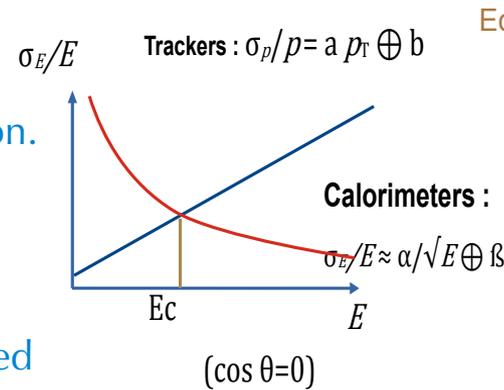
- Precise measurement of particle energies and identification.

## Particle Flow Approach for Jets :

- Calorimeter as a part of a detector system
- Charged particles are most of the time way better measured in tracking than in calorimeters
- Combine tracks and calorimeters clusters *topologically*, to avoid “bad” calorimetric resolutions

## CALICE :

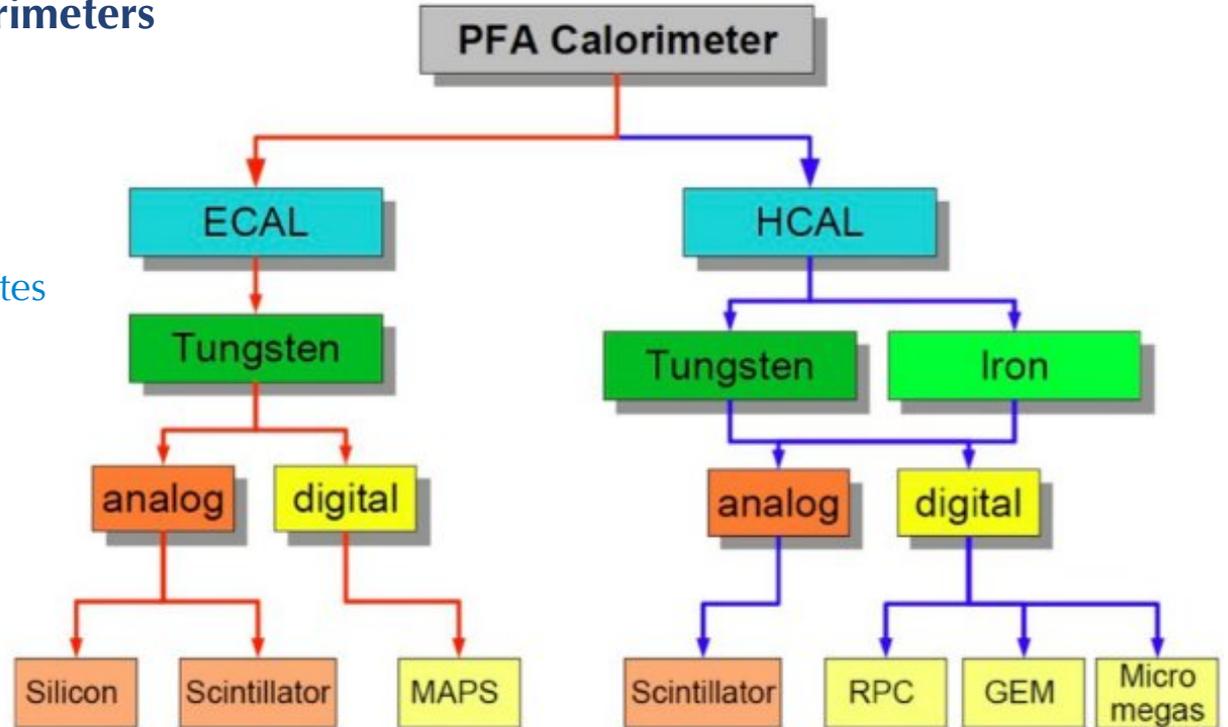
- Calorimeter for the Linear Collider Experiment, founded in 2004 with a specific focus on developing **highly-granular calorimeters** suitable for particle flow reconstruction techniques.
  - Deemed impossible to design calorimeters system with 10–100’s of Millions of channel, although electronics allows
  - **CALICE as a challenge**



# The CALICE Collaboration

## Calorimeter R&D for large imaging calorimeters

- MOU 2005
  - first Spokesperson: Jean-Claude Brient
  - current (& last? ) : Roman Poeschl
- ~270 physicists/engineers from 62 institutes and 18 countries from 4 continents
- Integrated R&D effort
- Acceleration of detector development due to coordinated approach

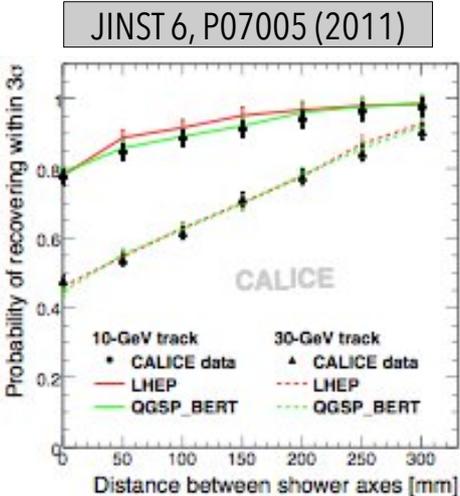


Here,  
we explore CALICE's journey in advancing calorimetry for future collider experiments.

# Steps of R&D

## Physics Prototypes

2003 - 2012



- Proof of principle of granular calorimeters
- Large scale combined beam tests
- Inspiration for CMS HGCAL

## Technological Prototypes

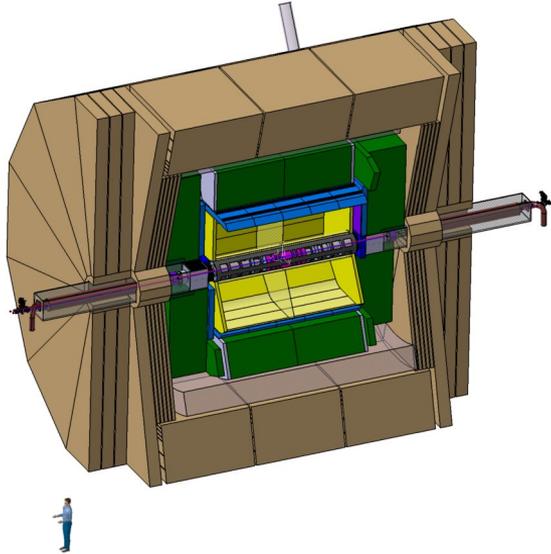
2010 - ...



- Engineering challenges
- Higher granularity
- Better sensitivity (lower noise)
- $\mathcal{O}(10^4-10^6)$  cells

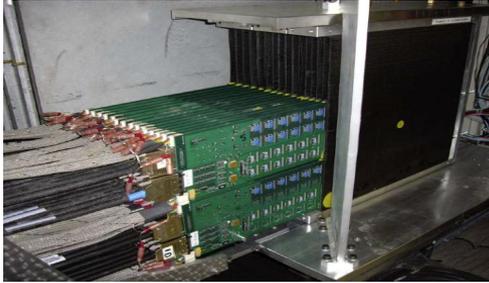
## Current period

## Higgs Factory Detector



- **The goal**
  - Typically  $10^8$  calorimeter cells
- **Compare:**
  - ATLAS  $\ell$ Ar  $\sim 10^5$  cells
  - CMS HGCAL  $\sim 10^7$  cells

# Physical Prototypes (2005–2012<sup>++</sup>)



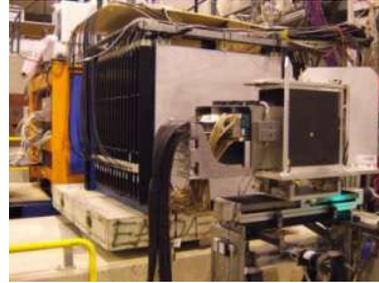
## SiW-ECAL

- Silicon-sensor
- 10,000 cells of  $1 \times 1 \text{ cm}^2$
- **Analogue** readout
- **Tungsten** absorber
- 30 layers ( $24X_0$ ,  $1\lambda$ )



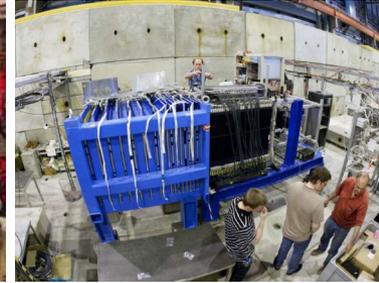
## ScW-ECAL

- Scintillator strips
- 2160 cells of  $1 \times 4.5 \text{ cm}^2$  each
- **Analogue** readout
- **Tungsten** absorber
- 30 layers ( $24X_0$ ,  $1\lambda$ )



## AHCAL

- Scintillator tiles :
- 7608 cells of  $3 \times 3$  (cent),  $6 \times 6$ ,  $12 \times 12 \text{ cm}^2$
- **Analogue** readout
- **Steel** or **Tungsten** absorber
- 38 layers ( $5.3\lambda$ )



## DHCAL\*

- GRPC
- up to 500,000 cells,  $1 \times 1 \text{ cm}^2$  each
- Readout :
  - **Digital** (1 bit)
  - **semi-digital** (2 bits, 3 thr.)
- **Steel** or **Tungsten** absorber
- Up to 48 layers ( $\sim 6\lambda$ )
- different readout ASICs



## SDHCAL\*

- GRPC
- up to 500,000 cells,  $1 \times 1 \text{ cm}^2$  each
- Readout :
  - **Digital** (1 bit)
  - **semi-digital** (2 bits, 3 thr.)
- **Steel** or **Tungsten** absorber
- Up to 48 layers ( $\sim 6\lambda$ )
- different readout ASICs

## Full-layer test beam prototypes for proof-of-principle of high-granularity calorimeter concept

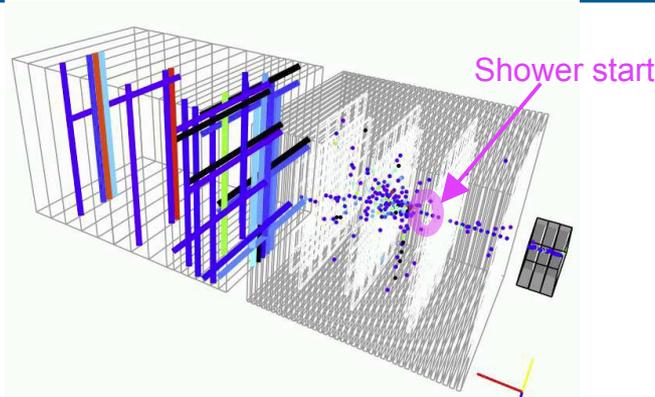
Some / Many technological criteria set aside : uniformity, embedded electronics, pulsed mode, mechanics...

\* Except for (S)DHCALs

# Hadronic Shower Studies : pion vs proton, shower start

## Hadronic shower studies by AHCAL

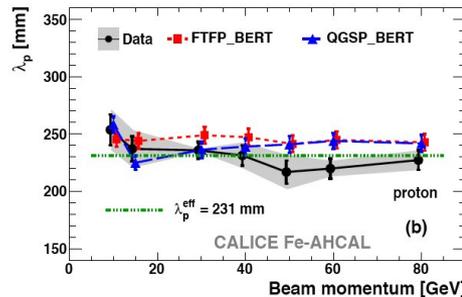
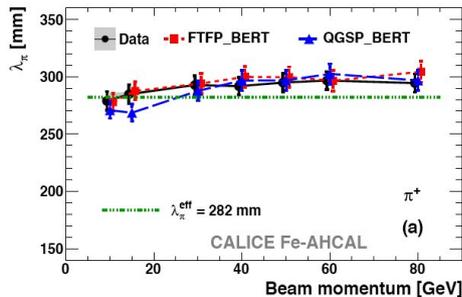
- Test beam data:  $\pi^+$ , p 10–80GeV@CERN and FNAL
- Simulation
  - GEANT4 ver9.6
  - Physics lists: FTP\_BERT, QGP\_BERT



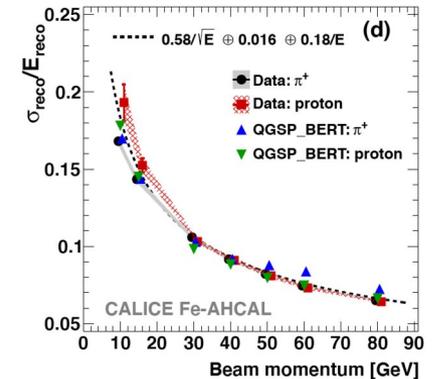
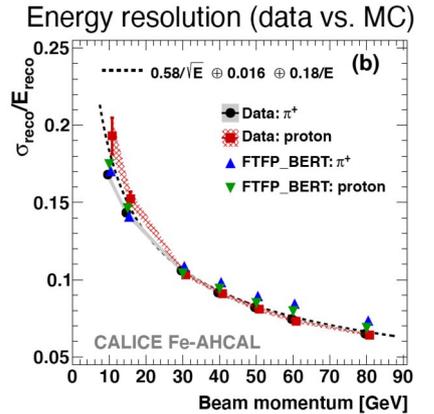
## Pion vs Proton-induced hadronic showers

- Longitudinal segmentation allows to measure shower start on event-by-event basis
- Interaction length extracted from distribution of shower starts
- Good agreement as calculated from detector compounds

### Nuclear interaction lengths for $\pi^+$ and p



JINST 10 (2015) P04014





# Hadronic Shower Studies : low energy in ECAL

NIM A794 (2015) 240-254

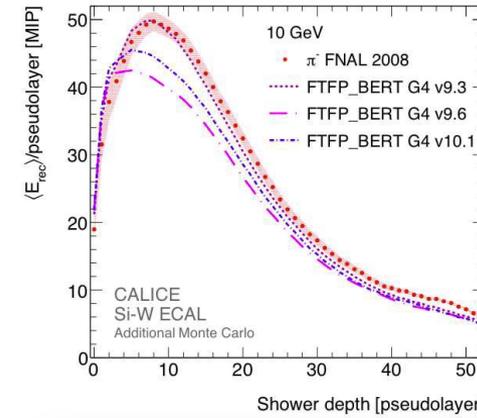
## Shower studies with low energy hadrons using SiW-ECAL

- Test beam:  $\pi^-$  2-10GeV @FNAL

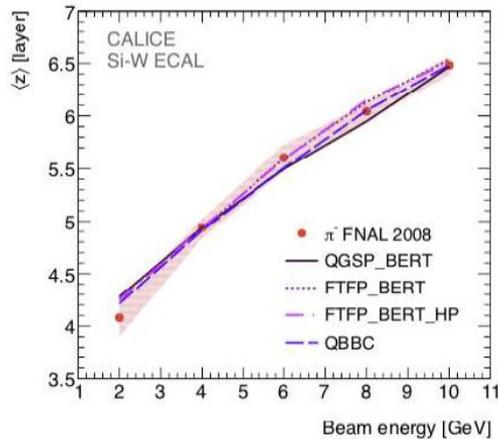
## Comparison with simulation

- Agreement to within 20% (much closer for most observables)
- Longitudinal hit distributions well described
- Largest discrepancies in longitudinal and radial profile of reconstructed energy

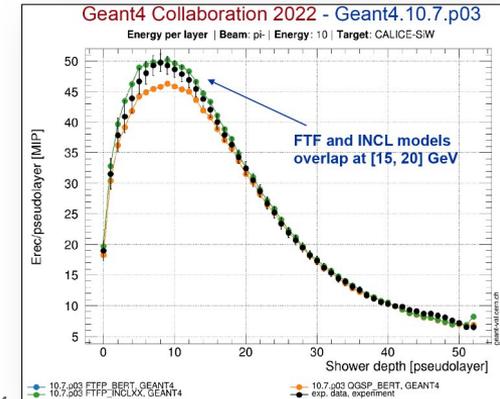
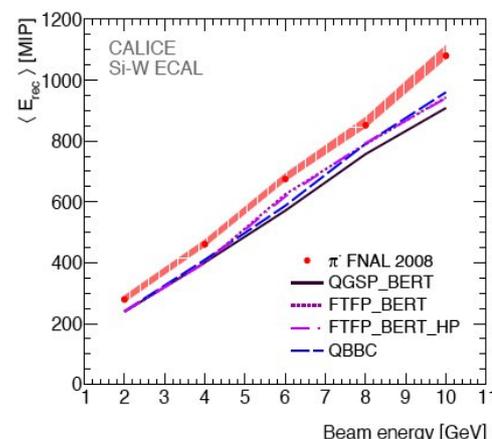
## Longitudinal energy distribution (10 GeV $\pi^-$ )



Mean of longitudinal hit distribution



Mean of longitudinal energy distribution



# GEANT4 Validation samples

L. Pezzotti, A. Ribon and D. Konstantinov,  
CALICE Meeting, Valencia, 2022

## SiW-ECAL

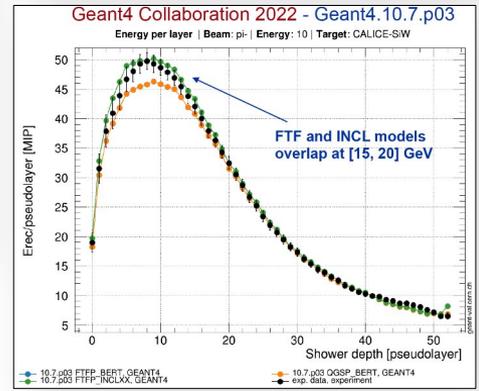
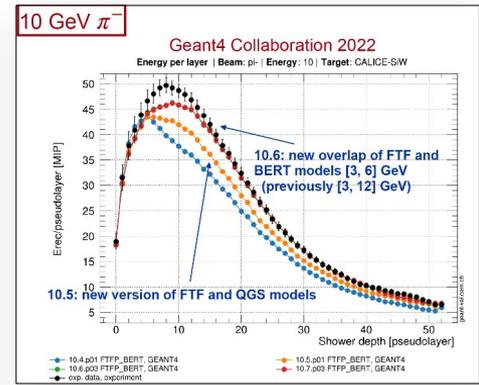
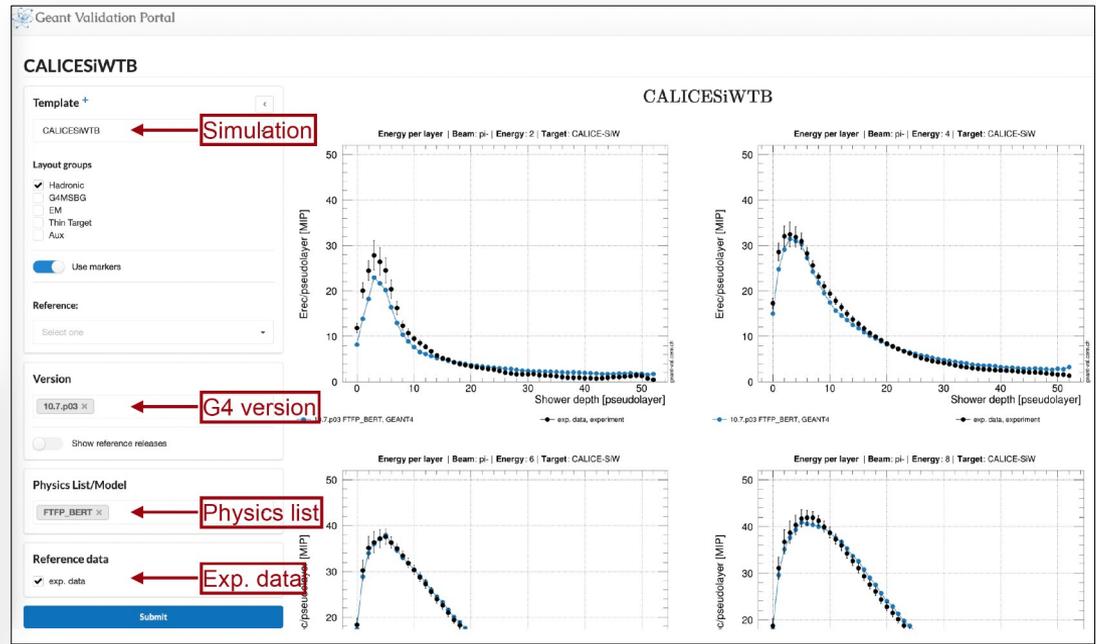
- 2008  $\pi^-$  data
  - 2, 4, 6, 8, 10 GeV
- NIM A794 (2015) 240-254

## AHCAL

- Planned

```

params.conf
|PHYSLIST=FTFP_BERT, QGSP_BERT
|CONST:ENERGY_UNIT=GeV
PARTICLE | ENERGY | PHYSLIST | NEVENTS
pi- | 20. | PHYSLIST | 50000
pi- | 30. | PHYSLIST | 50000
pi- | 40. | PHYSLIST | 50000
pi- | 50. | PHYSLIST | 50000
pi- | 60. | PHYSLIST | 50000
pi- | 80. | PHYSLIST | 50000
pi- | 100. | PHYSLIST | 50000
pi- | 120. | PHYSLIST | 50000
pi- | 150. | PHYSLIST | 50000
pi- | 180. | PHYSLIST | 50000
pi- | 200. | PHYSLIST | 50000
e- | 20. | PHYSLIST | 50000
e- | 40. | PHYSLIST | 50000
e- | 50. | PHYSLIST | 50000
e- | 80. | PHYSLIST | 50000
e- | 100. | PHYSLIST | 50000
e- | 119.1 | PHYSLIST | 50000
e- | 147.8 | PHYSLIST | 50000
    
```



## Nice improvements wrt to 2015

- Energy & #hits distributions

# Hadronic Shower Studies : sub-structures : track segments

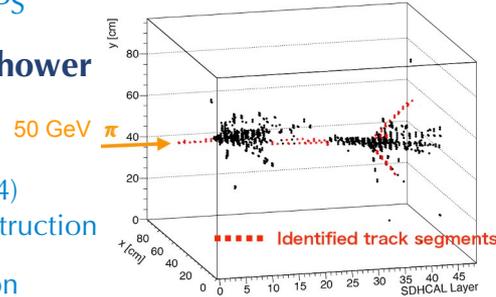
JINST 8(2013)P09001

## Track segments in SDHCAL

- Test beam data: pions 10-80GeV@CERN SPS

## Track segments found in dense hadronic shower

- Track finding using Hough Transform
- Useful for detailed shower study ( $\rightarrow$  Geant4) in-situ calibration and better energy reconstruction
- Slight improvement of energy reconstruction by weighting hits in tracks [reduce Landau fluctuations]

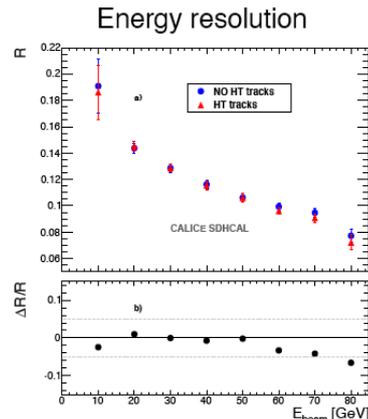
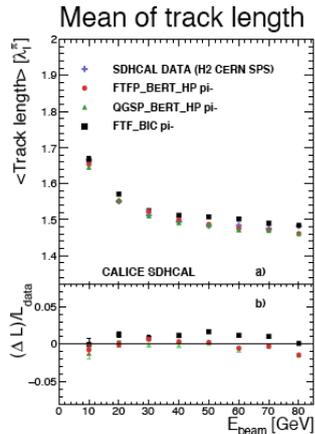
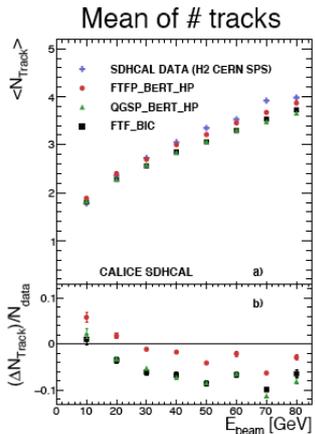
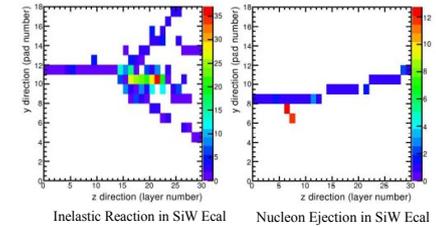
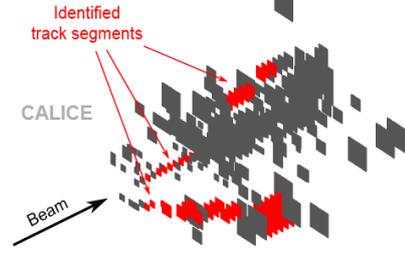


## AHCAL

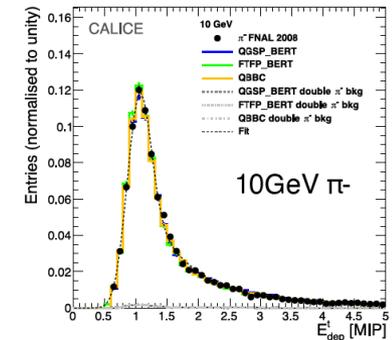
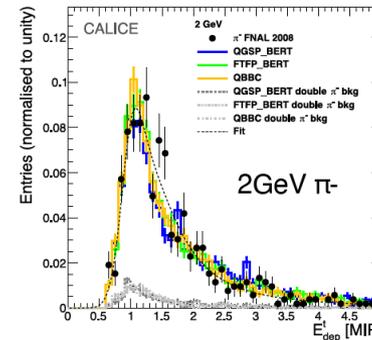
- Test beam data: 10–80 GeV  $\pi$ @CERN-SPS

## SiW-ECAL

- Test beam data: 2–10 GeV  $\pi$ @FNAL



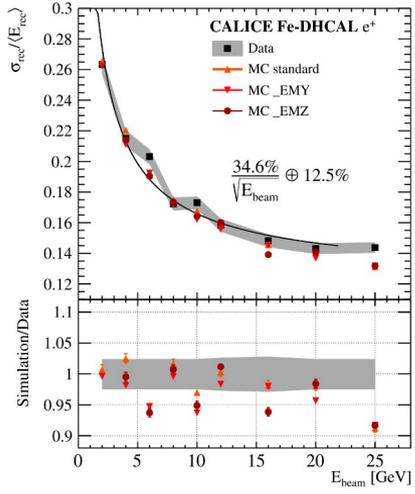
## Energy deposition of secondary tracks @ SiW-ECAL



# Hadronic Shower Reconstruction in Digital HCALs

JINST 11(2016)P04001

NIM A 939 (2019) 89-105

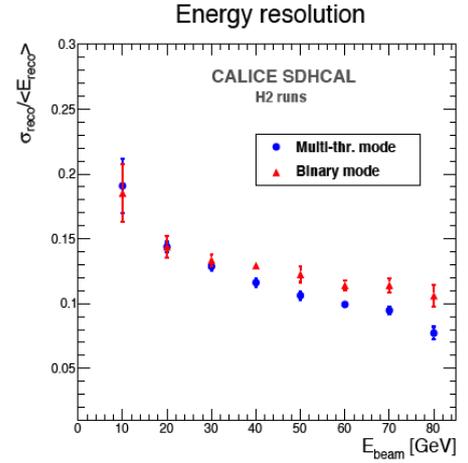
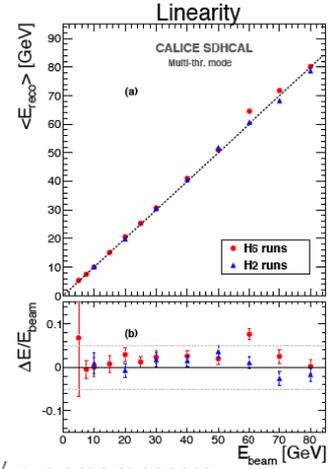
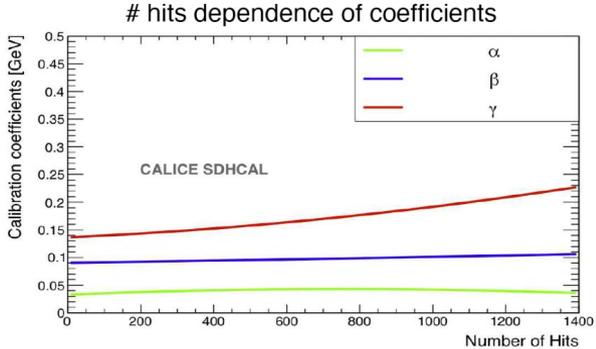
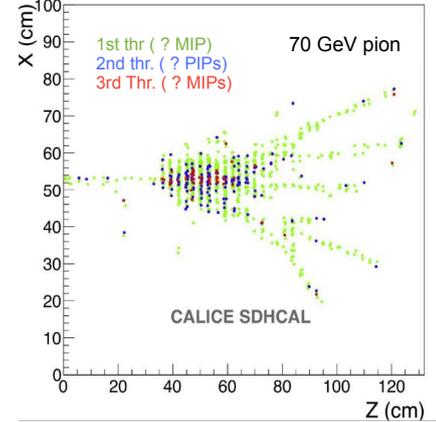


## ← Single threshold DHICAL

- Hit counting

## Multi-thresholds readout of SDHCAL →

- SDHCAL version of “software compensation”
- Different weights depending on three thresholds
  - $N_1, N_2, N_3$ : exclusive number of hits associated to 1<sup>st</sup>, 2<sup>nd</sup>, 3<sup>rd</sup> thresholds
  - $\alpha, \beta, \gamma$ : quadratic functions of total number values of hits
- Parameter fit using testbeam data @CERN SPS 5, 10, 30, 60, 80 GeV  $\pi^-$
- Mitigate saturation of energy resolution at high energy



# Energy Reconstruction: SW Compensation

JINST 7 (2012) P09017

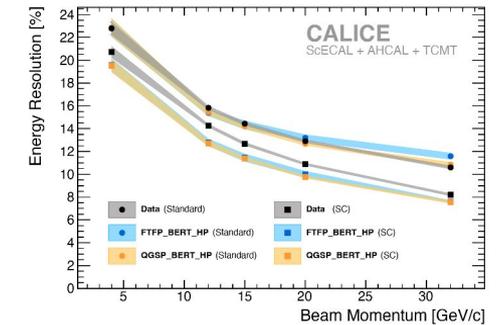
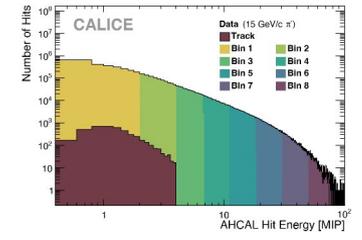
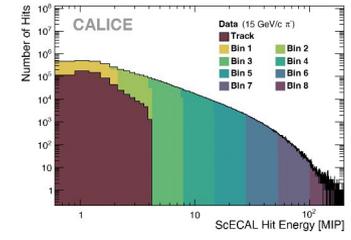
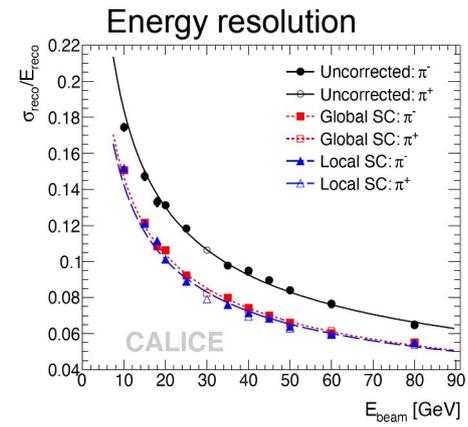
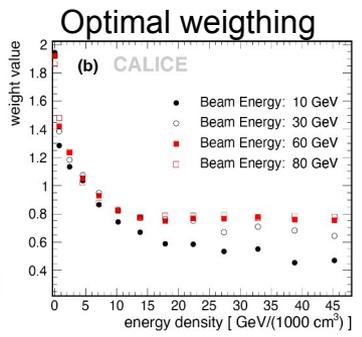
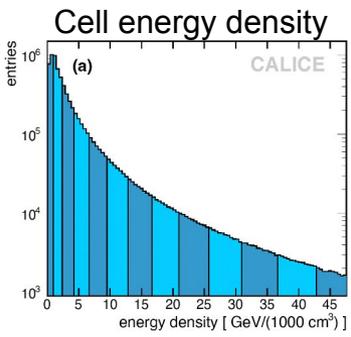
JINST 13 (2018) P12022

## CALICE calorimeters are non-compensating

- Some compensation can be restored by density weighting
- Beam Test : **AHCAL**
  - SPS H6 2007: 10–80 GeV  $\pi^\pm$
- Weightings :
  - Local Cell energy density (E/V) weighing
  - Global  $C(f_{lim}=5 \text{ MIPs})$
- Improvement by  $\sim 20\%$

## AHCAL and ScW-ECAL

- Combined Beam Test : ScW-ECAL + AHCAL + TCMT
  - FNAL 2009 4,12, 15, 20, 32 GeV  $\pi^-$
- Optimisation  $\sigma(E)/E$  on 51 parameters
  - Improvement by 10 to 20% (overestimated by simulation)

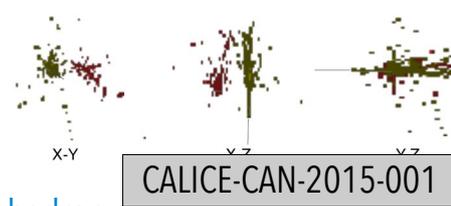


# Particle Flow Studies: particle separation & identification

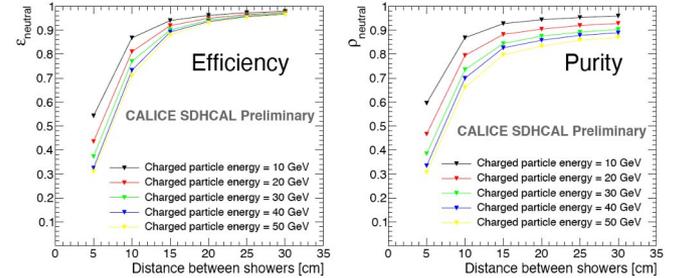
## Separation of neutral hadron shower from nearby charged hadron shower in SDHCAL

- Test beam data: 10-80GeV pions @CERN SPS
- 10GeV "fake" neutral hadron shower is generated by removing initial track segment and overlaid on charged hadron showers
- >90% efficiency and purity for nearby showers for distance > 15cm

10GeV neutral hadron overlaid with 30GeV charged hadron



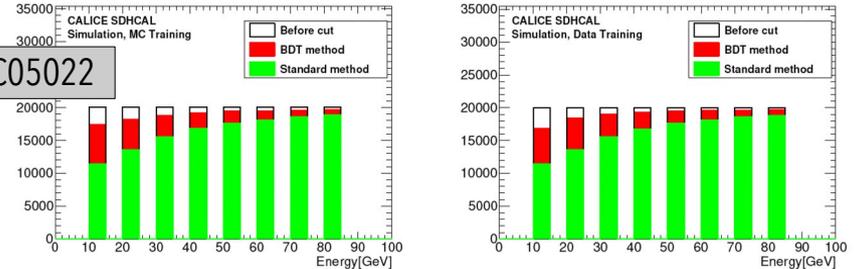
Separation of 10GeV neutral hadron from charged hadron



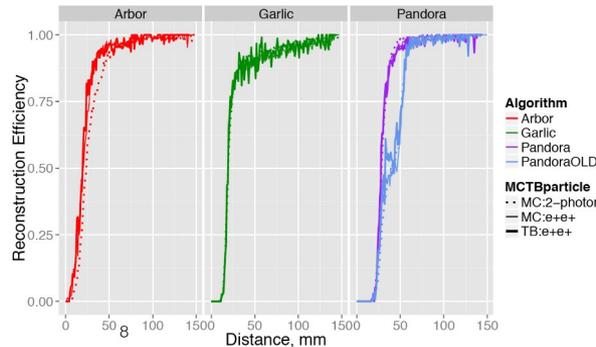
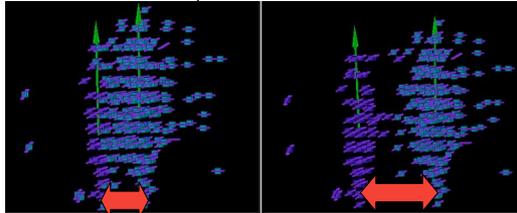
## Particle identification with multi-variate analysis

- Test beam data: 10-80GeV pions @CERN SPS
- BDT improves pion selection efficiency at low energies

JINST 15 (2020) C05022



## 4 + 25 GeV e+, reconstruction with Pandora



## Separation of EM showers (e+e+, $\gamma\gamma$ , $\gamma\pi$ ) on mixed events SiW-ECAL + (AHCAL || SDHCAL MC)

- Beam test from FNAL 2011 / CERN'07,
- Using Pandora, Garlic and Arbor
- Separation > 90% at 20mm
- Algorithm tuning mandatory

Axiv1802.00672v1,  
CALICE-CAN-2017-001

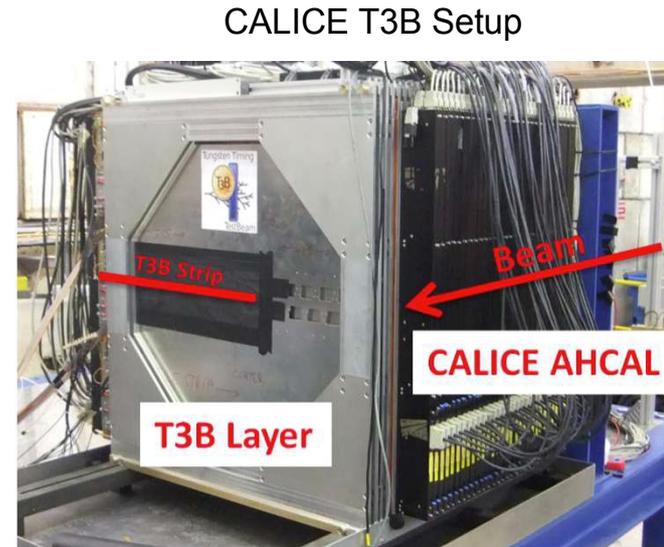
# Time structure of Hadronic Showers

## CALICE T3B Experiment

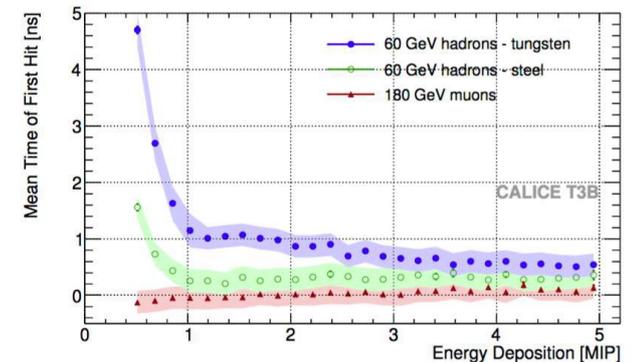
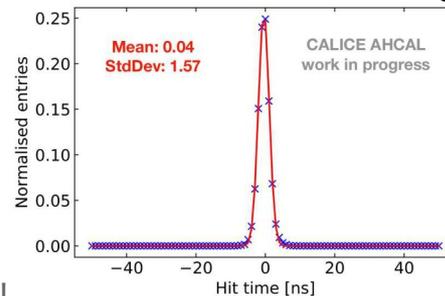
- Small dedicated setup of 15 scintillator tiles ( $30 \times 30 \text{ mm}^2$ ) with SiPMs placed behind CALICE hadron calorimeters (W-AHCAL, Fe-SDHCAL)
- Radial sampling of structure of hadronic showers with sub-ns time resolution over  $2.4 \mu\text{s}$  time window
- More late component in tungsten than in steel

## Hit time measurement capability at AHCAL technological prototype

- Hit time resolution of 1.6ns for muons @AHCAL technological prototype
  - Currently limited by front-end electronics
- Analysis for hadrons also in progress

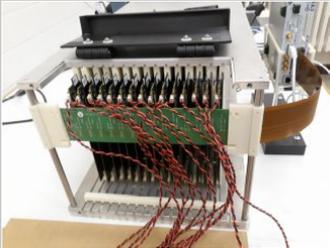
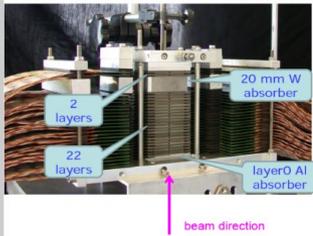
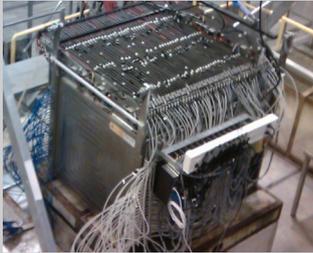


Hit time resolution with AHCAL technological prototype



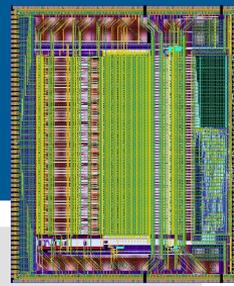
# Technological & new Physics Prototypes

## 4.5 prototypes, 15+ years of R&D, all tested

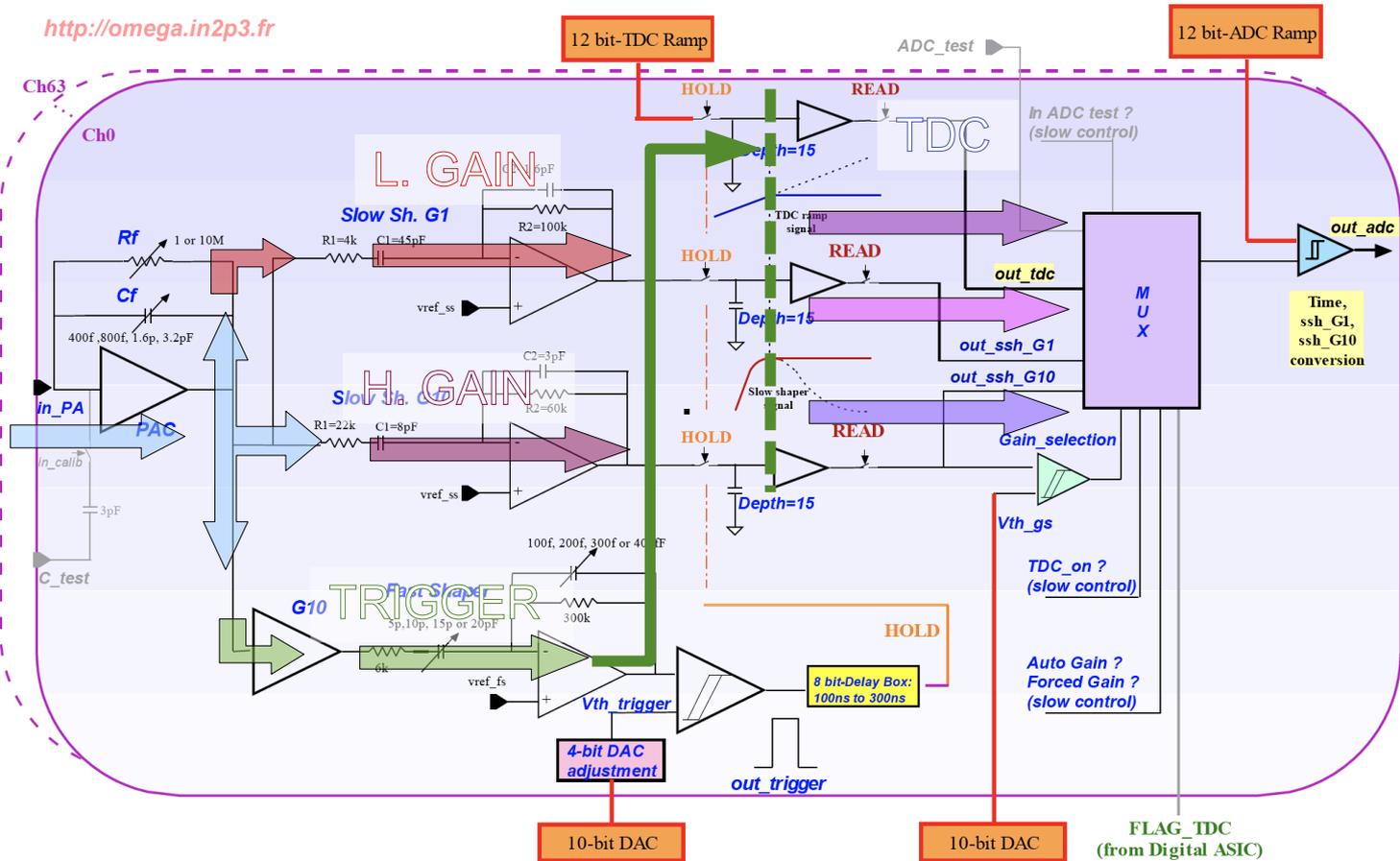
Si-W ECAL	(ALICE FoCAL)	Scint-W ECAL	AHCAL	SDHCAL
				
0,5×0,5 cm <sup>2</sup> ×15 (→30) Si layers + W	0,003×0,003 cm <sup>2</sup> × 24 MIMOSA layers + W	0,5×4,5 cm <sup>2</sup> ×30 Scint+SiPM lay. + SS	3×3 cm <sup>2</sup> × 38 Scint+SiPM lay. + SS	1×1 cm <sup>2</sup> × 48 layers GRPC + SS

### Purposes:

- Prove technological feasibility: electronics inside, thermal capacity, mechanical, DAQ, calibration, ...
- Extend physical prototypes : uniformity, “large” production, methods, ...



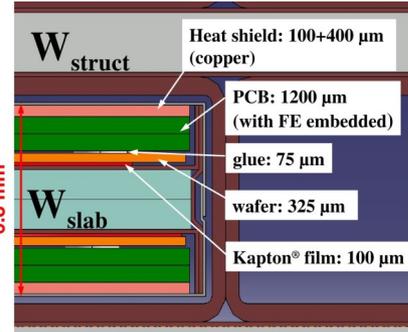
<http://omega.in2p3.fr>



- **36–64 channels**
- **Auto-triggered**
  - Partial 0-suppr. : 1 cell triggers all
- Preamp adapted to SiPMs, Silicons, RPCs + **2 Gains** (Auto-select) + **TDC (~ ns)**
- 15 (×2) analogue memories (128 digital)
- Dyn range 0.1 ~ 2500 mip/s
  - 12 bits ADC's
  - 2 bits ADC's
- ~600 configuration bits
- Low consumption
  - 25 μW/ch with 0.5% ILC-like duty cycle
- **Power-Pulsed**

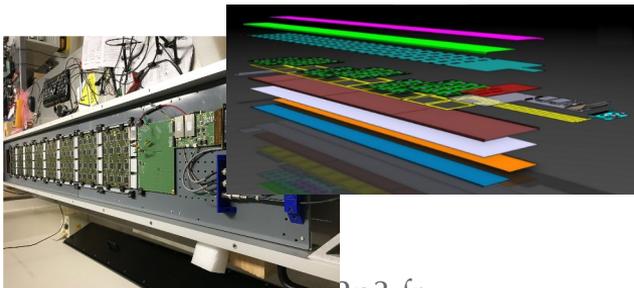
# CALICE Thin, long cassettes → all prototyped

## Silicon / Scint W-ECAL



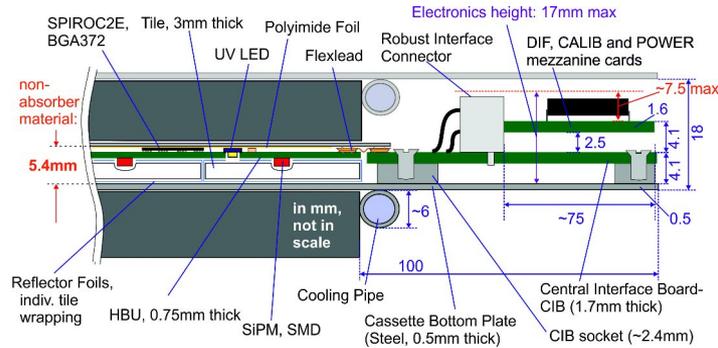
≤ 1.8m long

– Passive cooling

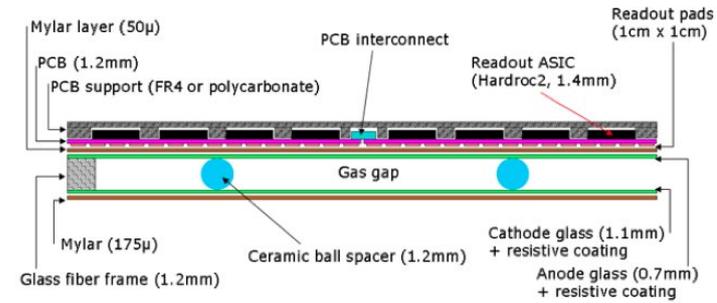


vincent.boudry@m2p3.fr

## Scint Analog HCAL (also used for HGAL)

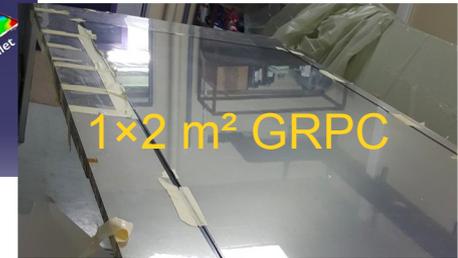
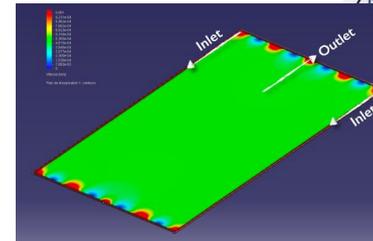
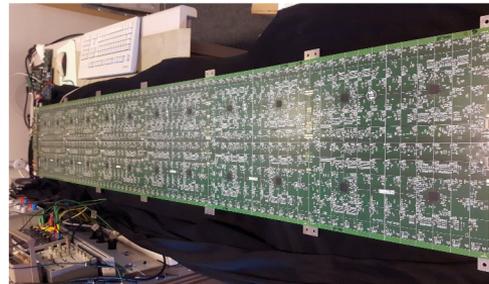


## (Semi)Digital Gaseous HCAL



≤ 3m long

No cooling or gas flow



# SiW-ECAL

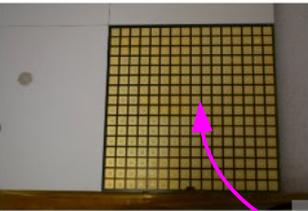
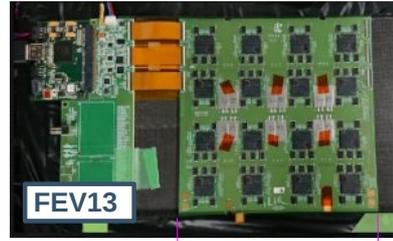
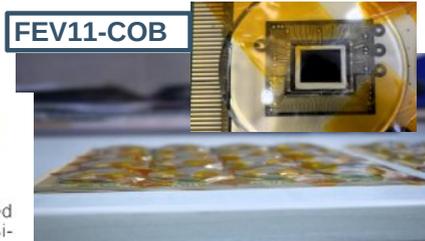
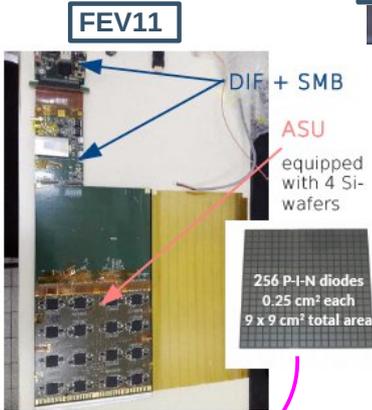
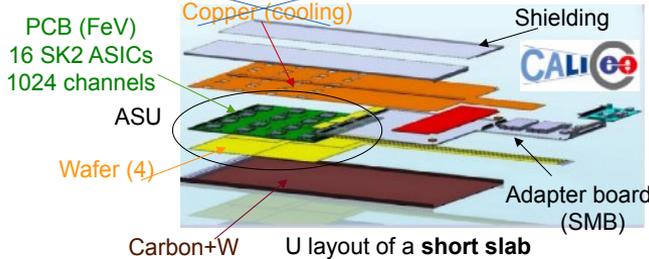
# Silicon-Tungsten ECAL

## Prototypes for the ILD/ILC

- cells of  $\sim 5 \times 5 \text{ mm}^2$ , density =  $2.6\text{k} - 3\text{k cell/dm}^3$
- Omega's Skiroc2/2a, 64 ch ASICs
- $25 \mu\text{W/ch}$  with 1% Power Cycle (0.3W for proto)

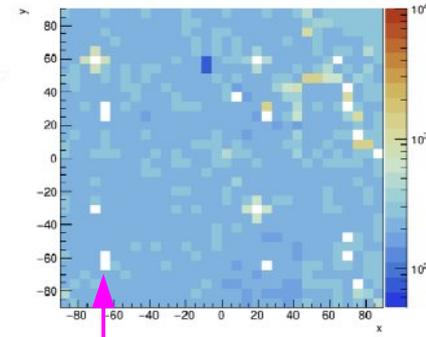
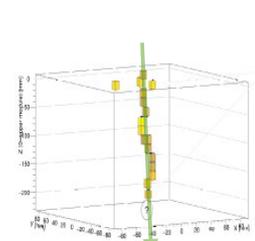
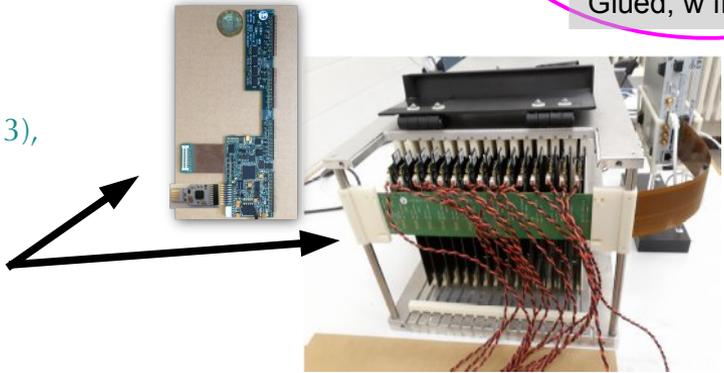
## Technological prototype

- "Physical prototype" (2005-11): 10k cells,  $\rho = 1.5\text{k cell/dm}^3$
- $S/N = \text{MPV}_{\text{mip}} / \sigma_{\text{Noise}} \geq 10$
- Stacks with **15+7** layers of 1024 ch (15360 cells in a single readout)
  - mix of PCB versions (v10, 11, 12, 13),
    - ⊗ packaged and on-board ASIC's
    - ⊗ 320, 500, 650  $\mu\text{m}$  Silicon wafers
  - New Integrated DAQ, 1<sup>st</sup> prototype toward ILD-like ( $\leq 3\text{cm}$ )



Glued, w floating GR

183mm



Noisy cells removed  $\sim 1-3\%$

# Silicon-Tungsten ECAL: Developments

See Roman's presentation for recent dev't



## Improvement in design

CERN 2015 "naked FEV11" (320 μm)

$S/N_{ADC} \sim 16-17$   
Ring X-talk / 10 wrt Phys. Proto.

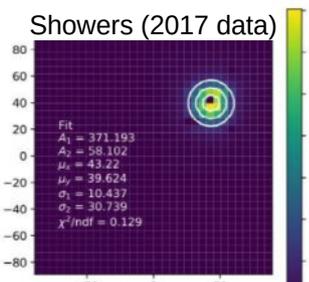
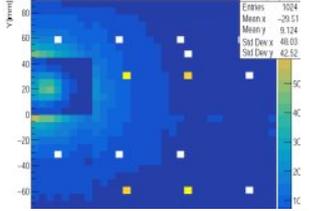
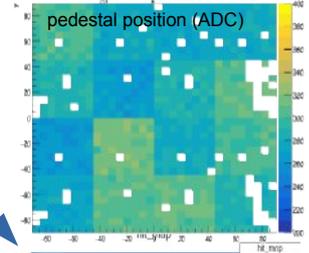
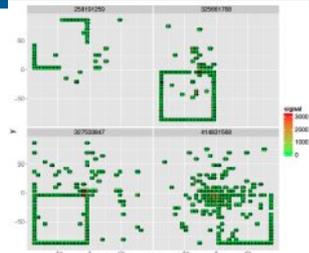
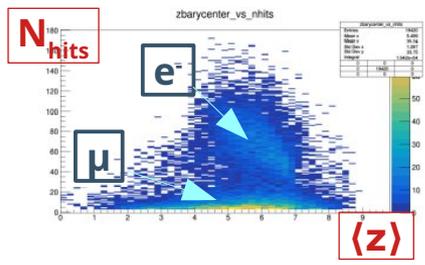
CERN 2017: 7 FEV11 (320 μm)

$S/N_{ADC} \sim 20.3 \pm 1.5$   
8% masking, 1T operation

DESY 2018: 7 FEV11 + 1 FEV13 (650μm)

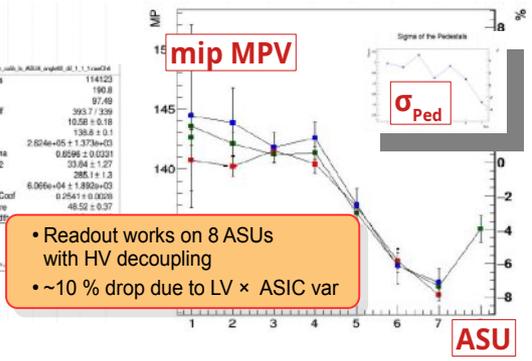
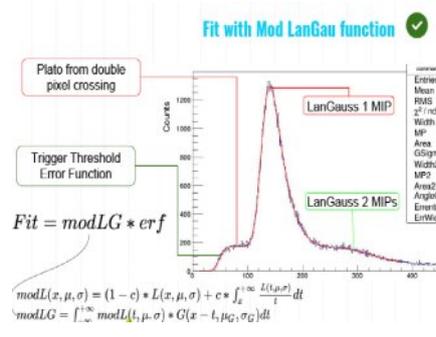
$S/N_{ADC} \sim 30.3 - 40;$   
 $S/N_{TRIG} \sim 11.6 \pm 0.7 \Rightarrow \text{Cut} \sim 1/3 \text{ mip} @ 4 \sigma$

CERN 2018: 6 FEV11 + 4 FEV13 + 24 X<sub>0</sub> W



## Long Slab

- 8 ASU's with baby wafers (2x2cm<sup>2</sup>)
- New FEV2.1



## R&D Highly Resistive Silicon Diodes:

- Ref = Hamamatsu "Guard-Ring-less" design
- 6" Towards 8" (à la CMS-HGCAL) x 725μm?
- ⇒ cost, design, perf.

# Sc-ECAL

# Scintillator-Tungsten ECAL

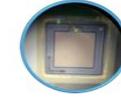
## Prototypes for the ILD/ILC & CEPC

- Omega's Spiroc2e, 36 ch ASICs
- 25  $\mu$ W/ch with 1% Power Cycle
- cells of  $\sim 5 \times 45$  mm<sup>2</sup>,  $\rho = 450$  cell/dm<sup>3</sup>

## Technological prototype

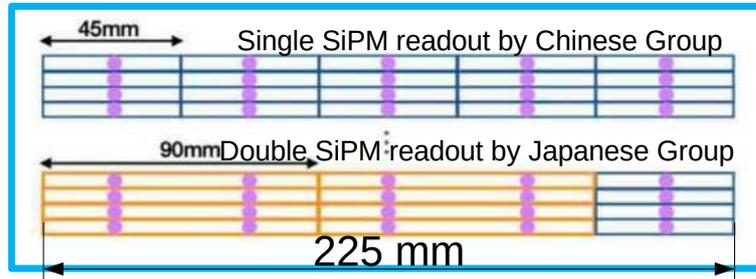
"Physical prototypes" (2005–11, 2013–15)

- Stack with 32 layers
  - aging test made (48h @ 50°C)
  - being assembled

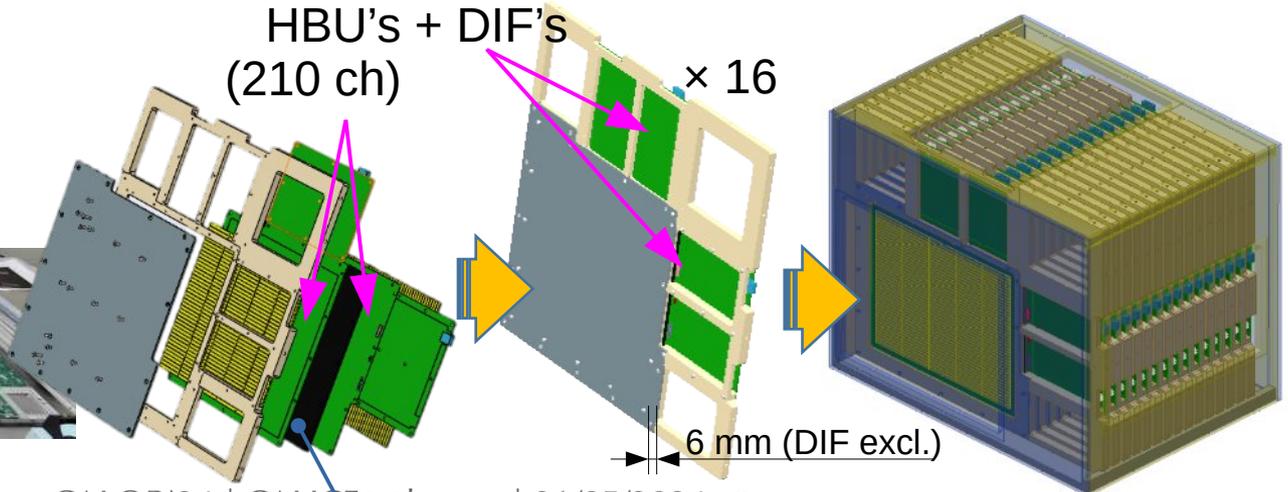


Baseline SiPM  
Hamamatsu S12571-010P

- size: 1mm  $\times$  1mm
- pitch: 10 $\mu$ m
- number of pixels: 10K



$\times 30$  10 $\mu$ m & 15  $\mu$ m SiPM  
 $\times 2$



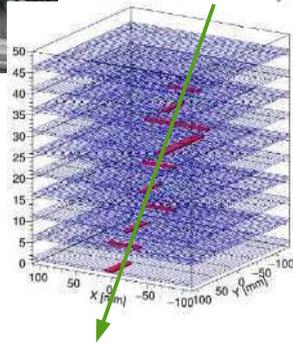
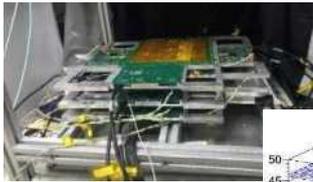
# ScECAL: commissioning

See Tatsuki Murata's and Xin Xia's presentations

## Sr90 Source

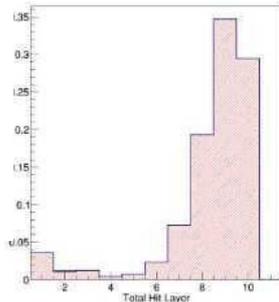
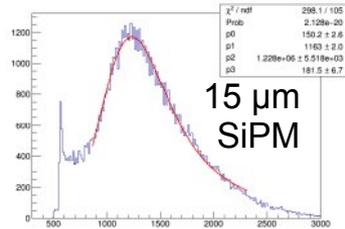
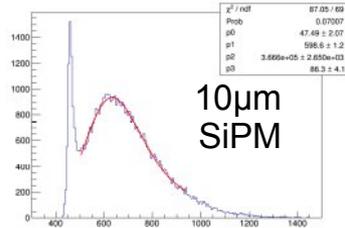
- 25 ns shaping auto-trig
- Landau  $\otimes$  Gauss

## Cosmics test



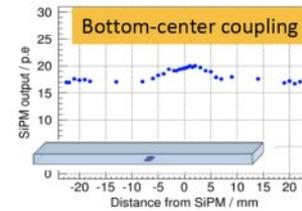
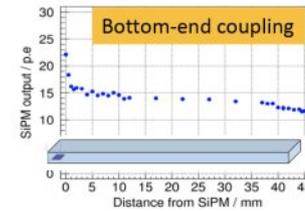
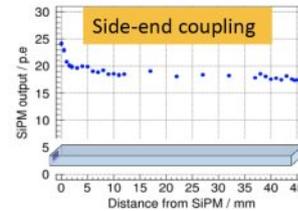
## Beam tests

- DESY, CERN.

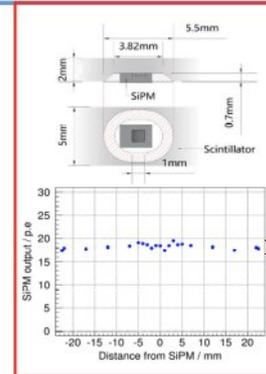
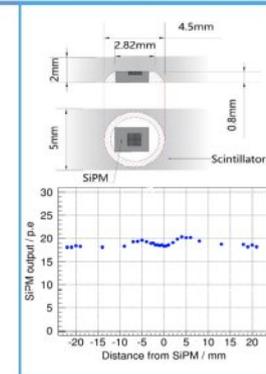
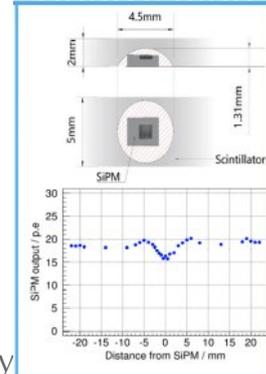


## R&D:

- Scintillator – SiPM coupling
  - non-uniformity  $\Rightarrow \sigma(E) \uparrow$
- SiPM position



- Groove form



4%

# AHCAL

# Scintillator AHCAL

## For ILC and CMS

- ILC with  $\Omega$ mega SPIROC2e
  - HL-LHC will be  $\Omega$ mega HGROCV3
- $3 \times 3 \text{ cm}^2$ , density  $\sim 55 \text{ cells / dm}^3$

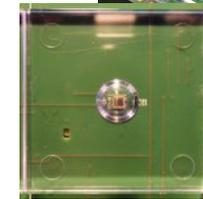
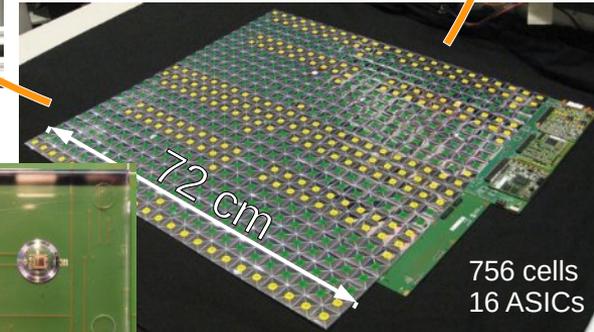
## Technological prototype $\geq 2017$

Physics prototype  $\sim 2006-11$  ( $3 \times 3 + 6 \times 6 + 12 \times 12$  tiles)

- Uniform  $3 \times 3 \text{ cm}^2$  tiles (moulded) read by SiPM mounted on PCB
- 38 layers of  $0,7 \times 0,7 \text{ m}^2$ , 22k cells
  - + additional layers of  $6 \times 6 \text{ cm}^2$
- 2018: Stand alone tests and with CMS HGAL
  - $4\lambda$  of stainless steel ( $1.7 \text{ cm} \times 38$ )
  - $\mathcal{O}(100\text{M})$  events accumulated

- **Combined beam test with ECALs**
- **Stand-alone with full W structure**

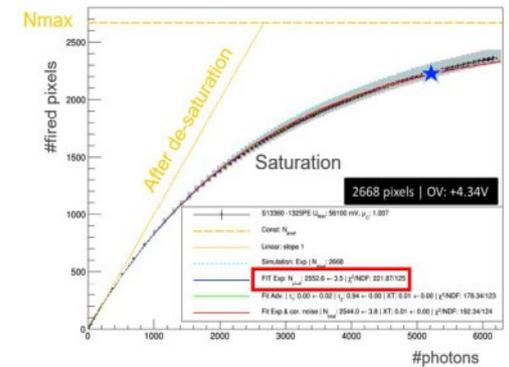
Vincent.Boudry@in2p3.fr



## Online corrections: on SiPM's:

⇒ EM Lin & Resol.

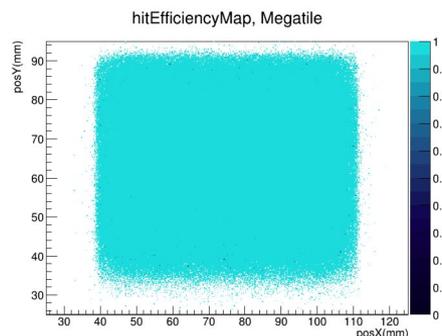
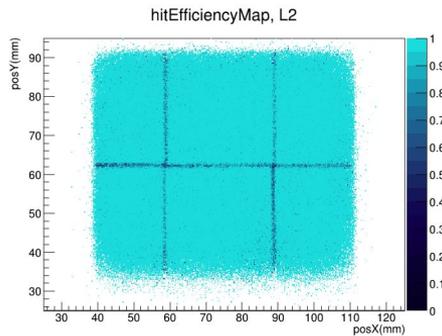
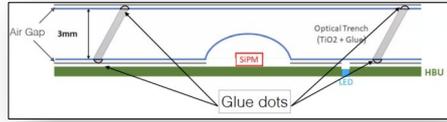
- Gain (Temperature, HV)
- Statistical saturation for  $E_{\text{hit}} \geq 100 \text{ mip}$  ( $N_{\gamma} \sim N_{\text{pix}}$ )
  - Corrected for  $E \leq 350 \text{ mip}$



# AHCAL developments

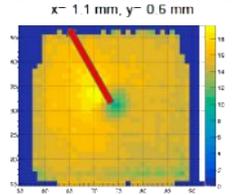
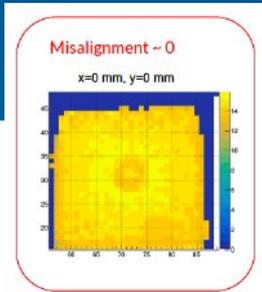
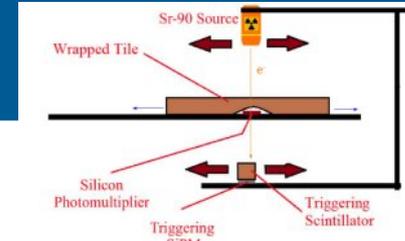
## “MegaTiles” R&D:

- Single Scintillator tile with trenches of 3×3 cm<sup>2</sup>
- 2019 Beam test:
  - Light Yield, Mip resp, Optical Cross-talk
  - Larger Cross-Talk than in cosmics (mechanics)



## R&D

- Scintillators optimisation
  - Measurements ⇒ Realistic Simulation
- SiPM/MPPC evaluations
- ADC consumption (KLAUS Chip)
  - → Next Gen



## Long Layer

- 2×6 HBU’s OK in lab...
- Goals:
  - 3×6 HBU’s (ILD)
  - ... in a test structure (absorbers)



## CMS HGCAL:

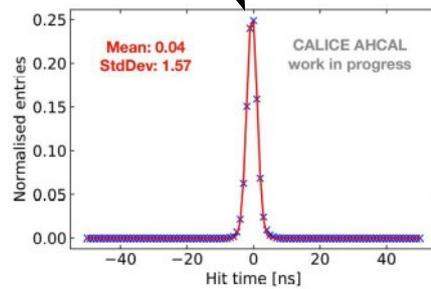
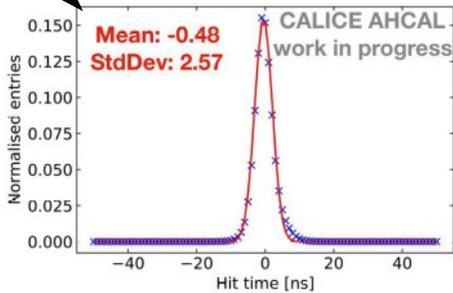
- Production on-going



# AHCAL analysis

## New: Hit time correlation

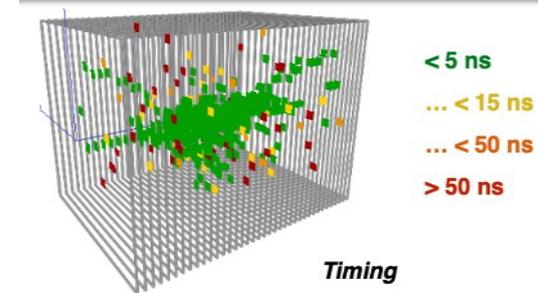
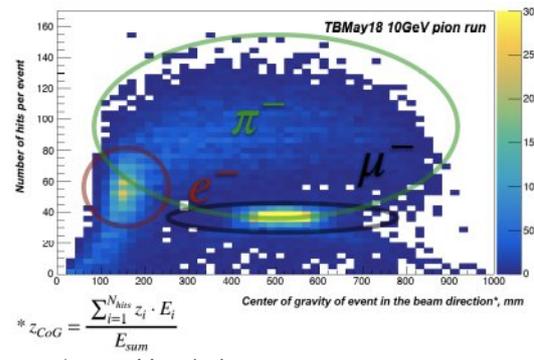
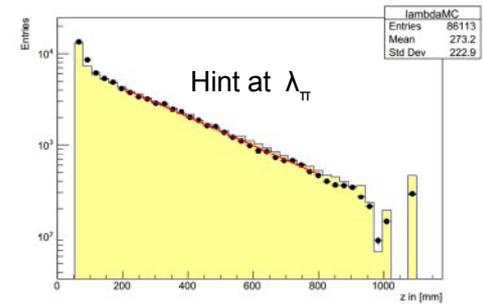
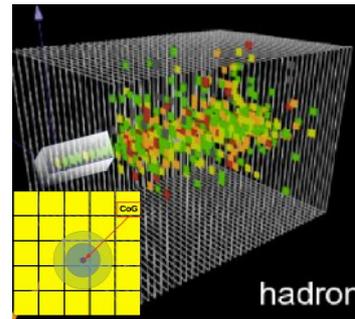
- Time profile from muons
  - SPIROC : double analog ramp  $\rightarrow$  ADC
  - with clocks
- at 250kHz (beam test mode) :  $\sigma \sim 2.6$  ns
- 5 MHz (ILC mode):  $\sigma \sim 1.6$  ns



– Goal: 1 ns in ILC mode

## High Level Analyses:

- Shower profiles & PFA tests ( $\geq 2011$ )
- Shower start, PID,  $f_{\text{neutrons}}$  (time)

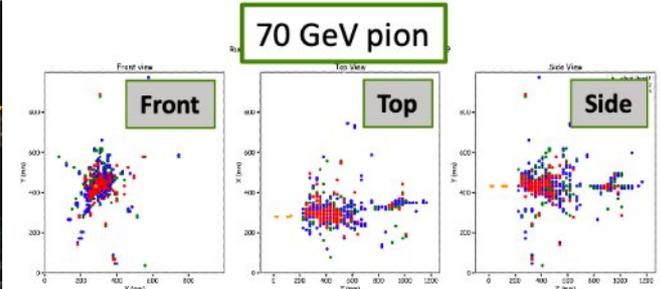
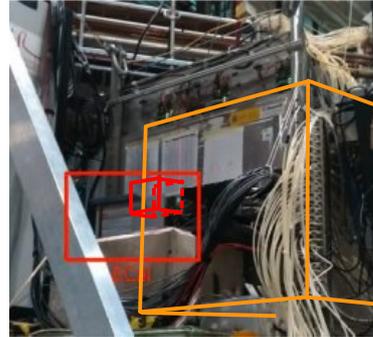


# **(T)-SDHCAL**

# SDHCAL: Semi-Digital Gaseous HCAL

## Technological prototype $\geq 2011$

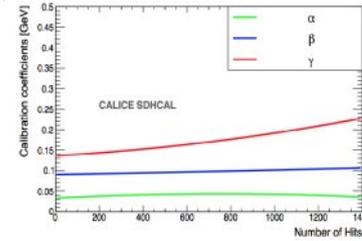
- Single and multi-gap thin GRPC's
- Cells of  $1 \times 1 \text{ cm}^2$ ,  $\rho = 380 \text{ cells/dm}^3$
- Omega HARDROC2
- 48 layers of  $1 \times 1 \text{ m}^2$ , 460k cells,  $6\lambda_i$  (2 cm Stainless steel)



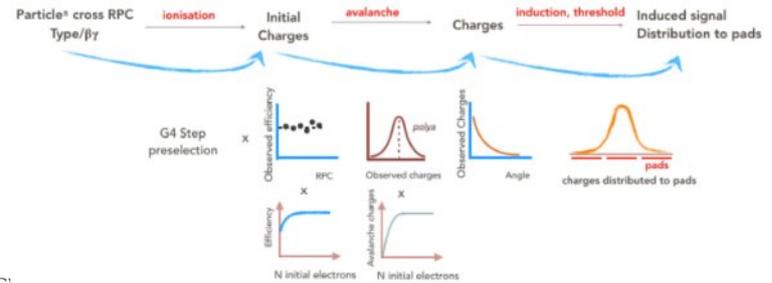
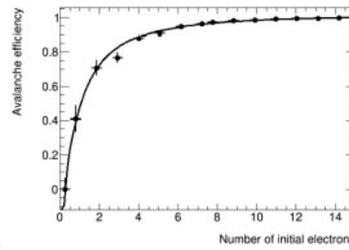
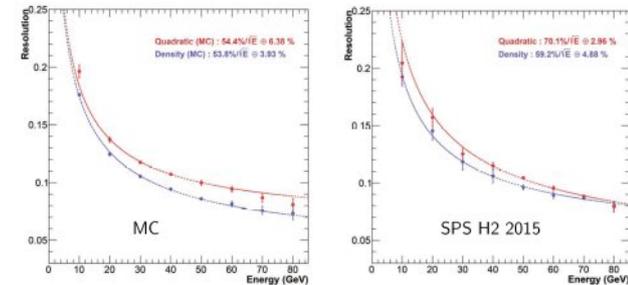
## Semi-Digital calorimetry: 3 thresholds

- Uniformity: efficiency & multiplicity
- Threshold optimisations (typ. 1/2 mips,  $\sim 5$ ,  $\sim 15$  mips)
  - and calibration by scans
- Energy measurement:
  - Linearity & Resolution to single  $e, \pi, p$
  - Simulation: **complex digitization**
    - Large number of overlapping effects in avalanches / readout / time
    - Now, reasonable  $\leq 40 \text{ GeV } e, \pi$

$$E_{\text{Quad}} = \alpha (N_{\text{tot}}) N_1 + \beta (N_{\text{tot}}) N_2 + \gamma (N_{\text{tot}}) N_3$$



$$E_{\text{Dens}} = \alpha B_1 + \beta B_2 + \gamma B_3 ; B_i = \text{Neighbours} \geq \text{thr. } i$$



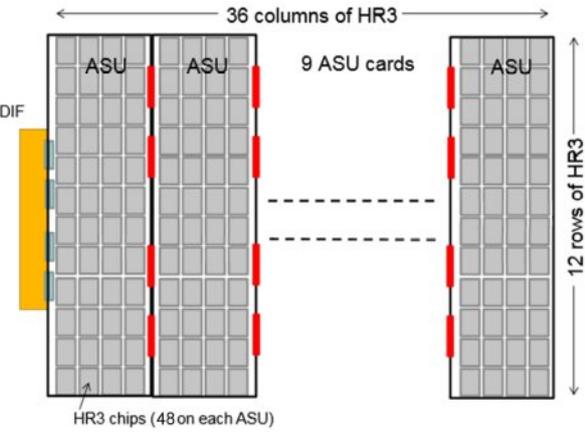
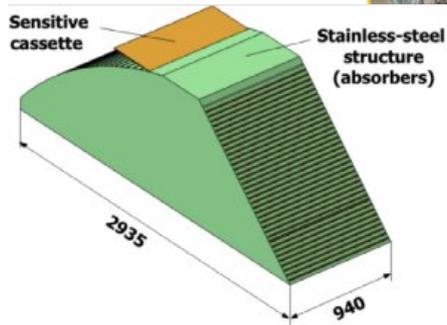
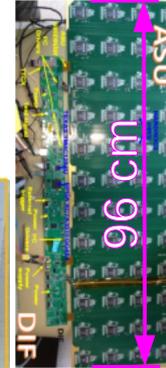
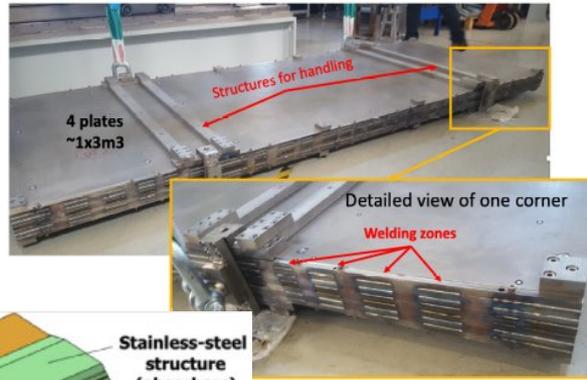
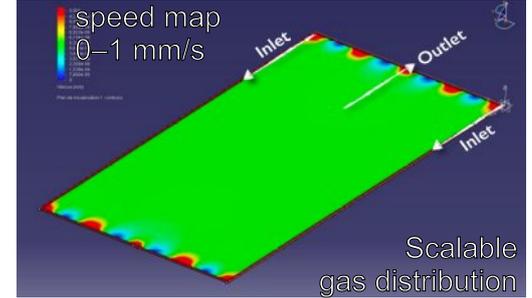
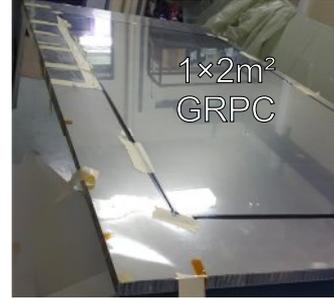
# SDHCAL developments

## Large cassettes: $1 \times 1 \text{ m}^2 \rightarrow 3 \times 1 \text{ m}^2$ :

- 432 ASICs HardRoc3:  
I2C, full zero-suppression,  
dynamic range  $\times 3$  (15  $\rightarrow$  50pC)

## Main goals:

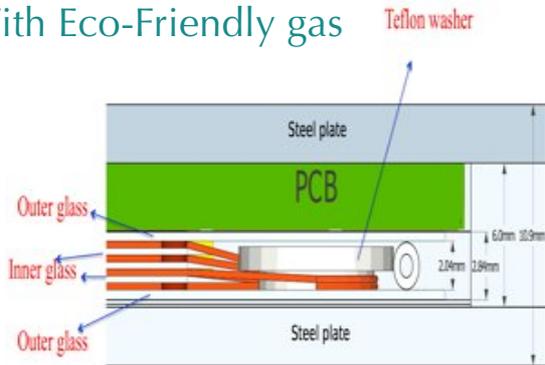
- Sensors: Large uniform GRPC's
- Large & flat PCBs:  $32 \times 96 \text{ cm}^2$ 
  - glued on single GRPC chamber
  - interconnections (in 3T field)
- Mechanical assembly
  - Electron Beam Welding



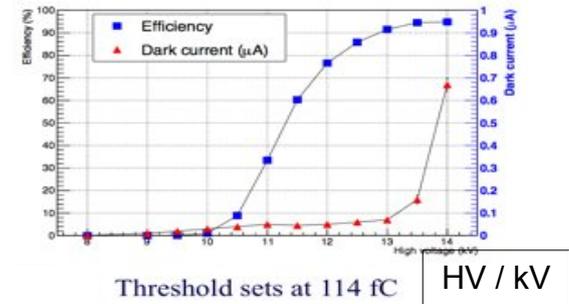
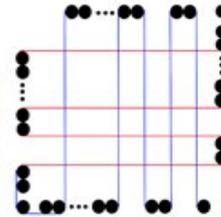
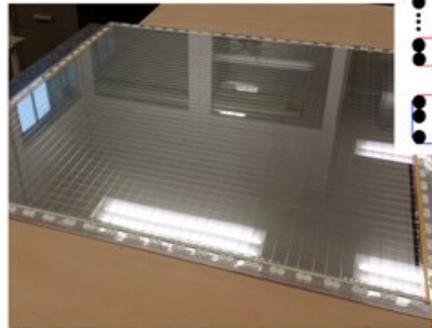
# SDHCAL → T-SDHCAL (for Circular colliders)

## Power issues ?

- RPC :
  - Rates OK for RPC ? → MGRPC
  - Cooling possible with gas
    - (flow to be determined from uniformity of response :
      - heat/laminar flow )
  - With Eco-Friendly gas

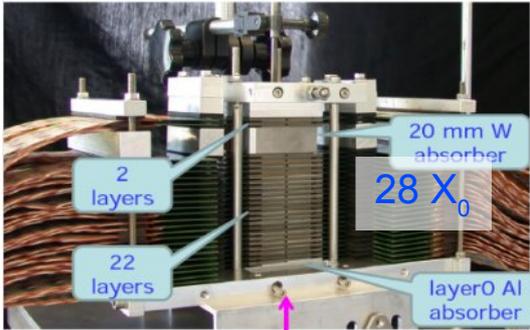


4-gap MRPC of 1 m<sup>2</sup>



# Others : **DECAL, Adriano**

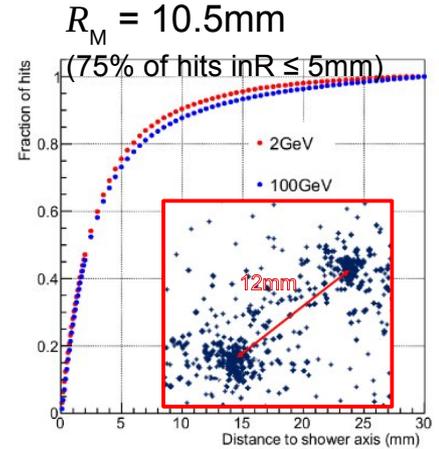
## FOCAL DECAL prototype



FOCAL = 2 layers of MAPS

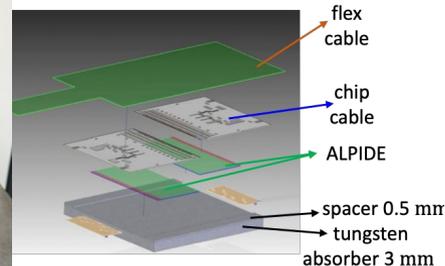
but How to build a full detector ?

- Services: Power + Cooling ?
- Gains by going fully digital ?
- For what physical gain ?
- Improved separation ✓
- Improved resolution ?



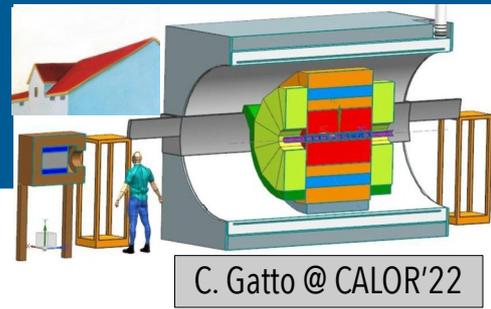
4 MIMOSA-26 / Layer CMOS sensors (IPHC)

- 6x6 cm<sup>2</sup>
- 30x30 μm<sup>2</sup> pixels
- 39 M pixels = full readout



- ▶ 24 layers with 3 mm tungsten and two ALPIDE chips each
  - chip size 30 mm x 15 mm
- ▶ 512 x 1024 pixels per chip:
  - 25 M pixels in total
  - pixel size: 26.88 μm x 29.24 μm

# ADRIANO2/3 - Dual/Triple Readout Calorimeter



## Primary experimental context: REDTOP / T1604 Collaboration

- ADRIANO2: PFA (Granularity) + Dual Readout ( $\checkmark$ /Scint)
  - 5D shower measurement, disentangling the neutron component of the shower.
- ADRIANO3: ps timing

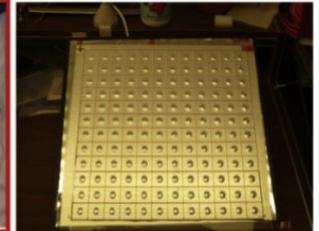
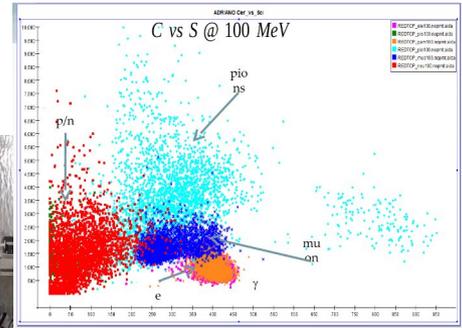
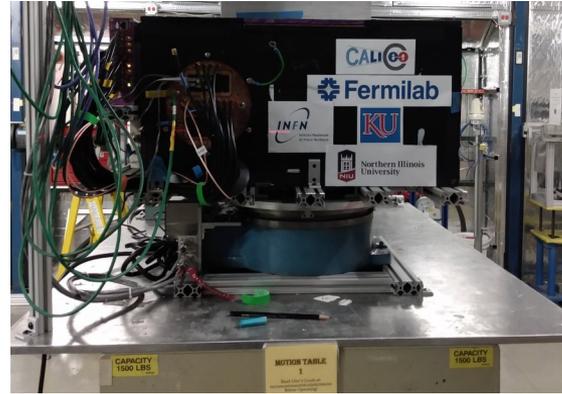
## Sensors:

- High-density glass as Cherenkov Medium (and absorber)
- Plastic scintillator tiles
- RPCs with  $\text{cm}^2$  pad readout for fast timing

## Key R&D goals

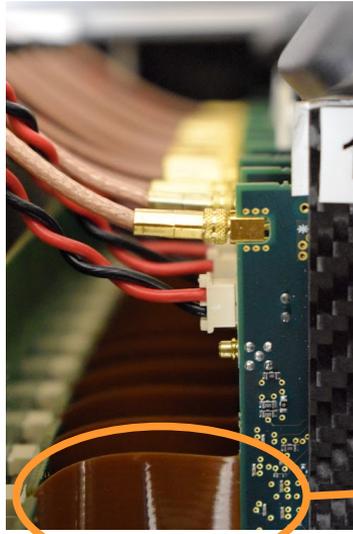
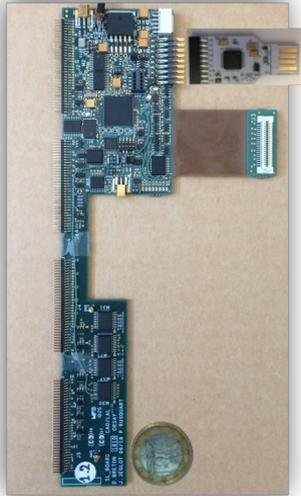
- optimization of the construction technique in terms of:
  - light yield, RPC efficiency, timing resolution, and cost
  - Test layers in 2024, small-scale prototype 2025

## Testing many configurations $\rightarrow$ larger prototype 2026–27



# Reminder on compact readout

Current detector interface card (SL Board) and zoom into interface region

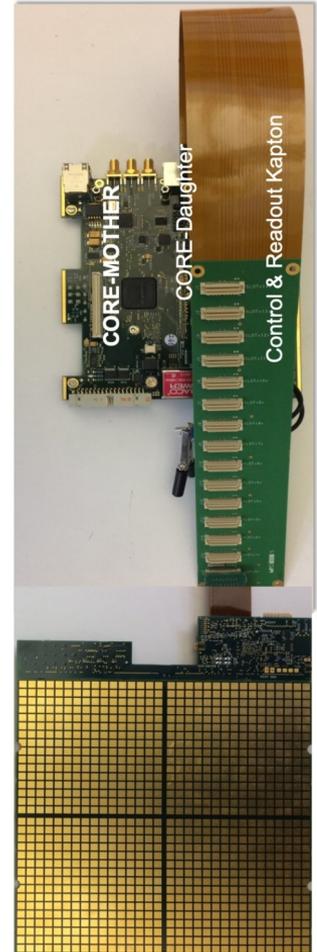


Complete readout system

More compactness  
⇒ Less flexibility  
~ in CRK

margin ~  $\pm 2 \times 2$  cm ?

- “Dead space free” granular calorimeters put tight demands on compactness
- Current developments in CALICE meet these requirements
- Can be applied/adapted wherever compactness is mandatory
- Components already tested in beam tests



# Future Directions and Challenges

## Immediate Applications:

- Use in real cases :
  - AHCAL for the CMS-HGCAL : on-going, full speed
  - SiW-ECAL for QED & Dark Photons experiments :LUXE@XFEL, EBES@KEK, Lohengrin@ELSA
  - DECAL for the ATLAS-FOCAL

## Further developments:

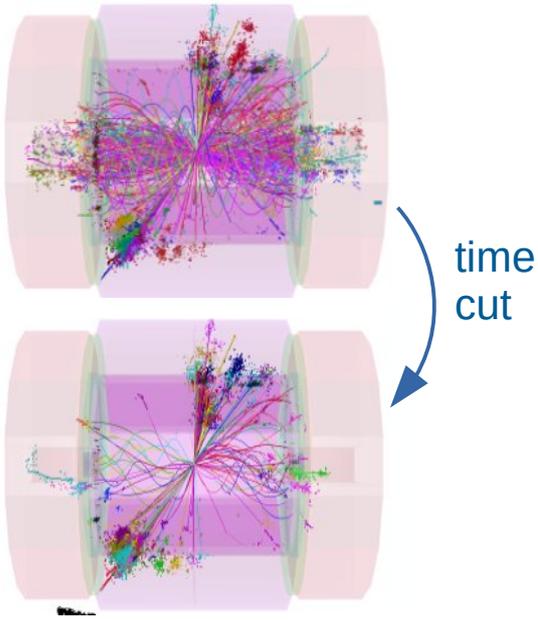
- Timing in calorimeters
- Adaptation to circular colliders

# Timing in Calorimeters: 0.1–1 ns range

Technically feasible  
but adding thermal constraint

1 cm/c = 30 ps

## Cleaning of Events



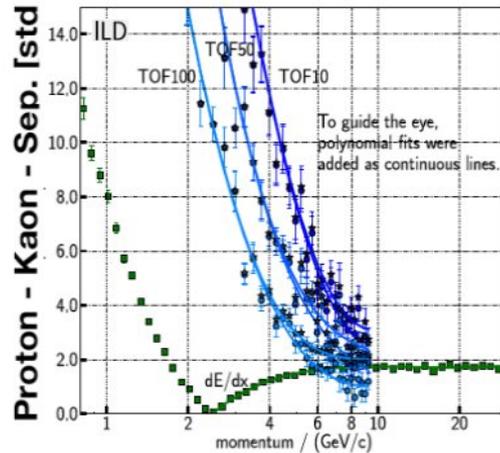
[CLIC CDR: 1202.5940]

adapted from L. Emberger

Vincent.Boudry@in2p3.fr

## Particle ID by Time-of-Flight

- Complementary to  $dE/dx$ 
  - here with 100 ps on 10 ECAL hits

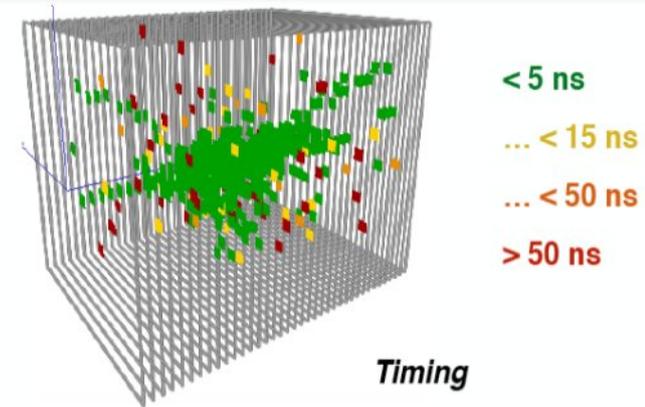


S. Dharani, U. Einhaus, J. List

CALOR 24 | CALICE, a legacy | 21/05/2024

## Ease Particle Flow:

- Identify primers in showers
- Help against confusion  
*better separation of showers*
- Cleaning of late neutrons & back scattering.
- Requires 4D clustering



Ch. Graf

37/54

# How to adapt calorimeters to circular collider conditions

## CALICE calorimeters:

- Embedded readout:  
compact design & DAQ
- Minimal consumption by power pulsing
  - 1–2ms readout , 198–199 ms off.
  - Passive cooling → no dead materials

## 1) Rates and cross-sections

- Z-peak out-of-scale wrt all the other configurations
  - One detector fits all ? “optimal” granularity ?
- DAQ Scheme ? Continuous readout ?

## 2) Continuous running

- Electronics base consumption × 100–200 wrt ILC

## 3) New opportunities: timing in calorimeters

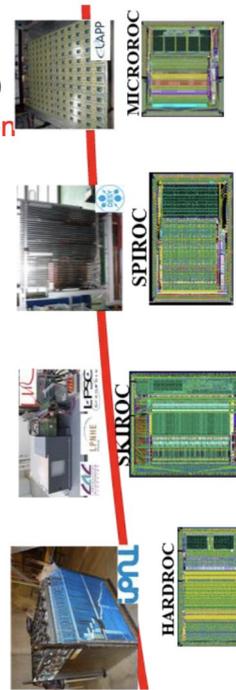
- Adds consumption
- Large potential but at what cost ? What precision ?

# New ASIC:

## DRD6 Common readout ASICs proposal [AGH, Omega, Saclay]



- Develop readout ASIC family for DRD6 prototype characterization
  - Inspired from CALICE SKIROC/SPIROC/HARDROC/MICROROC family
  - Targeting future experiments as mentioned in ICFA document (EIC, FCC, ILC, CEPC...)
  - Addressing **embedded electronics** and detector/electronics coexistence + **joint optimization**
  - Detector specific front-end but **common backend**
  - ⇒ allows common DAQ and facilitates combined testbeam
- Start from HGCROC / HKROC : Si and SiPM
  - **Reduce power** from 15 mW/ch to few mW/ch
  - Allows better granularity or LAr operation
  - Extend to LAr (cryogenic operation) and MCPs (PID)
  - Remove HL-LHC-specific digital part and provide flexible **auto-triggered** data payload
  - Several improvements foreseen in the VFE and digitization parts
- Several other ASICs R/Os also developed in DRD6 and it is good !
  - FLAME/FLAXE, FATIC...
  - Waveform samplers : commercial or specific (e.g. SPIDER)
  - DECAL



CdLT : future chips DRD1 10 jul 23

8

## Low Power

- Timing ?

## Low occupancy

- Self-trigger
- Less memory
  - if continuous readout

## Optimized dynamic range

# Conclusion:

## Development of calorimeters with unprecedented granularity

- Exploratory prototype for physics
- Validation of technologies for future experiments

## Opening on hadronics showers imaging :

- Precise 3D profiles → GEANT4 validation/adjustment
- In-shower sub-components:
  - Track identification → in-physics calibration, improved energy resolution
  - EM subshowers → SW compensation
- Validation of particle flow algorithms approach

**So ? Mission accomplished ?**

## Yes, ... but please go-on

- Completion of some prototypes
- Explore the potential of timing calorimetry
- Lots of data still to be (re)exploited (with Machine Learning)
  - See M. Borysova on Wednesday

**... in DRD6**

## An R&D Collaboration Model ?

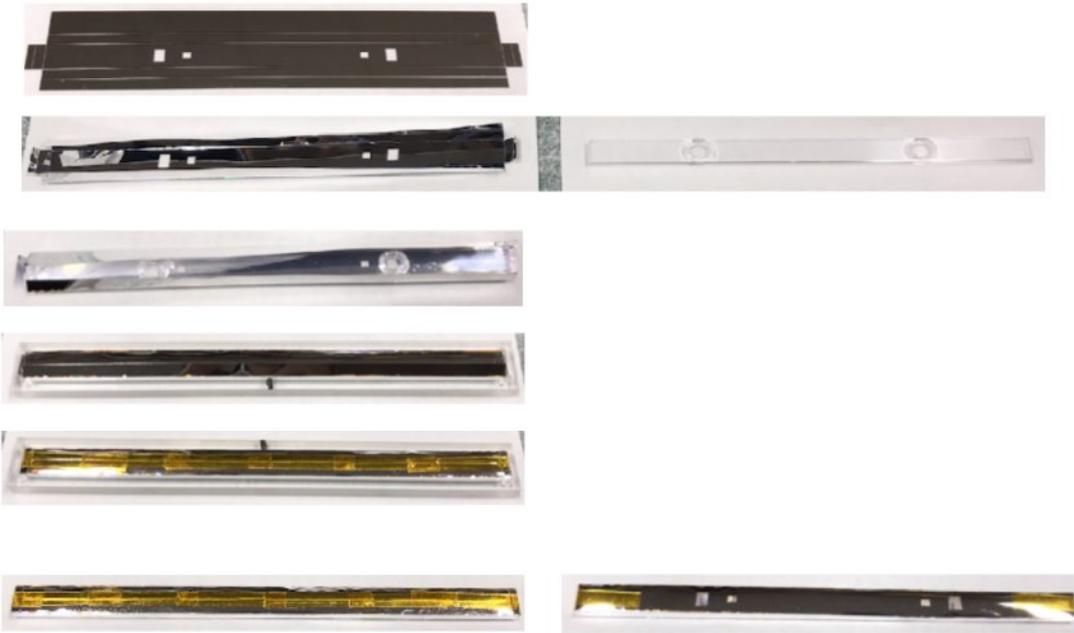
- Sharing of tools and experience between otherwise competitive teams
  - ASICs, DAQ, Beam test, Reconstruction techniques, Simulations,
  - Building of expertise & Visibility
- Very flexible framework:
  - Openness ... but no financial support
    - Common tasks on Goodwill

# Backups

# Scint-ECAL tile wrapping

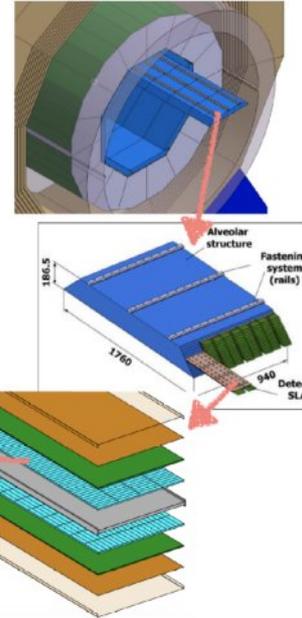
## Reflector wrapping (90mm strip)

- Wrapping by hand with a help of jig



## Sc-ECAL (reminder)

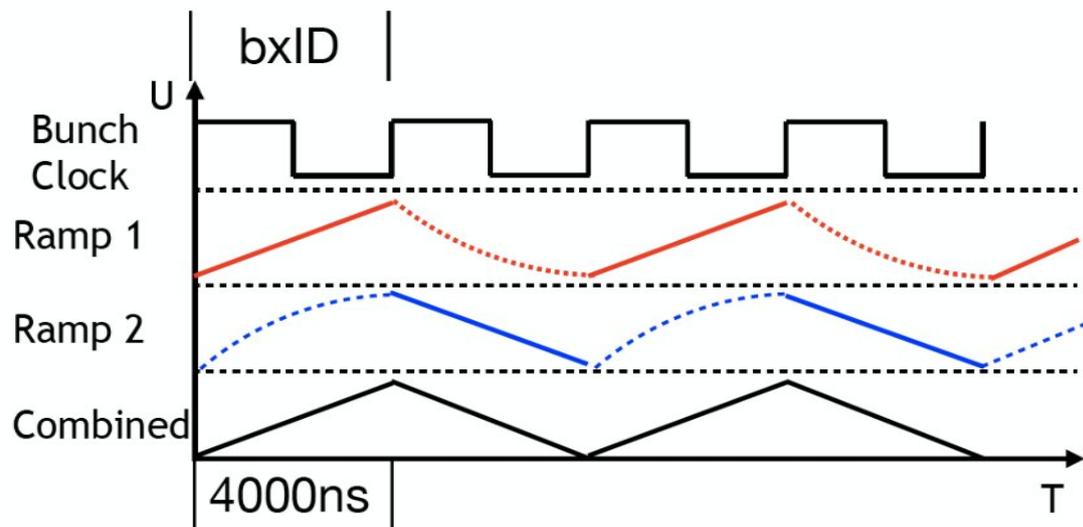
- Scintillator Electromagnetic CALorimeter (Sc-ECAL)
  - Technology option of EM calorimeter for ILD
- Based on scintillator strips readout by SiPM
  - $5 \times 45 \times 2$  mm scintillator strip
- Virtual segmentation : 5mm x 5mm with strips in x-y configuration
- Timing resolution < 1 ns
- Low cost



# Time calibration (HW)

Time measurement with Spiroc2E: TDC  
(time to digital converter)

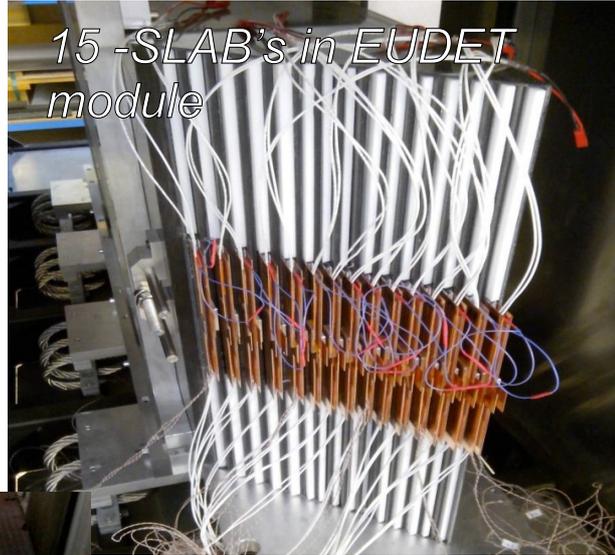
1. Common external clock with  $\sim 1\text{ns}$  bins
2. Ramp up voltage during one bunch crossing ID



Lorenz Emberger (MPI. Munich)

# Integration in ILD: thermal studies

by Denis GRONDIN / Julien GIRAUD (LPSC)



15 -SLAB's in EUDET module



Heat exchanger assembly



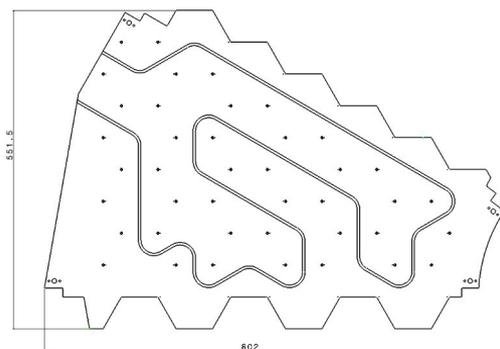
Insulation

Puissances ASU / SLAB (W)	1	2	1	2	
Puissances Front / SLAB (W)	1	1	2	2	
Total ASU SLAB (W)	15	30	15	30	
Total FRONT SLAB (W)	15	15	30	30	
	Total (W)	30	45	45	60

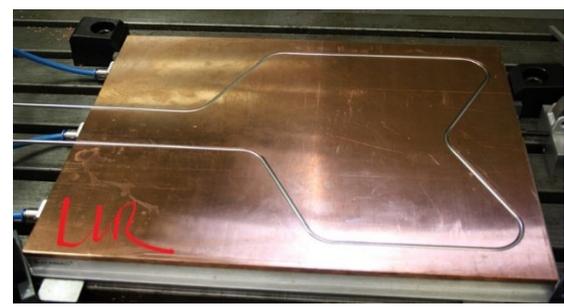
**Important thermal inertia => 4 days minimum of stabilization**

# Active cooling

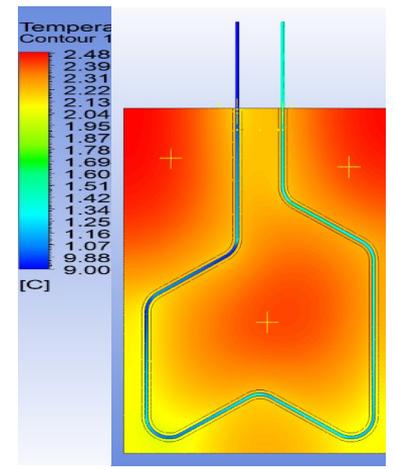
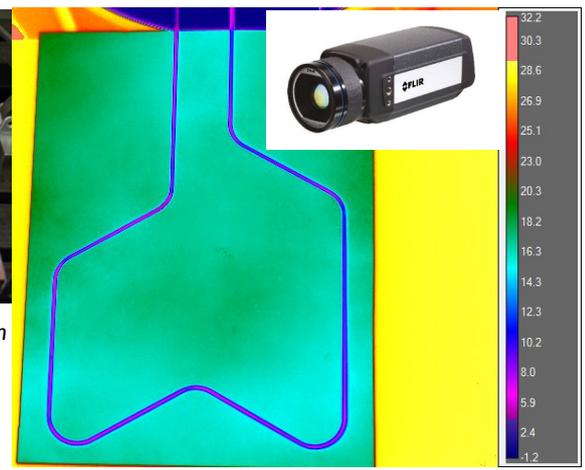
R&D using CMS studies (Thanks to Th. Pierre-Emile from CMS-LLR group)



Copper plate prototype dimensions information

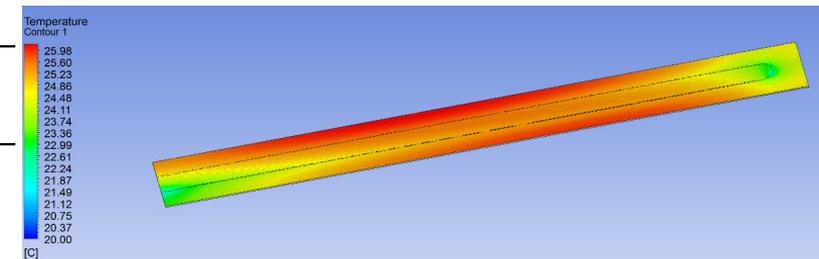


Pipe insertion on a cooling prototype for FEA correlation



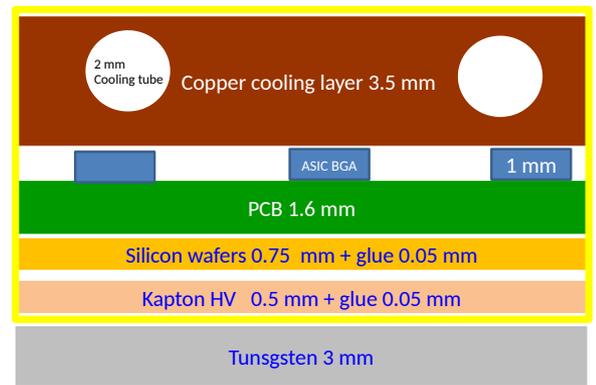
Pipe insertion on a cooling prototype

$\Delta T = 2.5^\circ C$



Thermal static CFD analysis thermal field example using Fluent with 100W extracted and water mass flow rate of 7g/s through 1,5mm ID pipe

- Pipe insertion process introduces some efficiency loss due to the thermal contact resistance.
- The benefit remains significant with regard to a passive cooling



**⇒ 9 mm / layer**



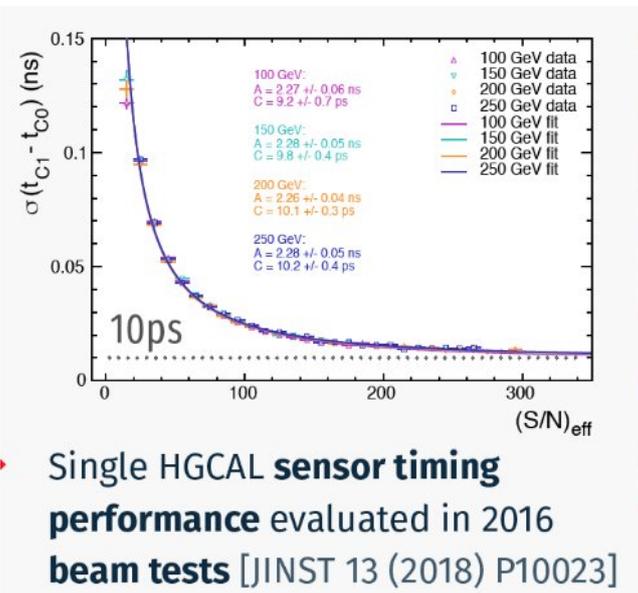
# Timing

## Timing of Showers

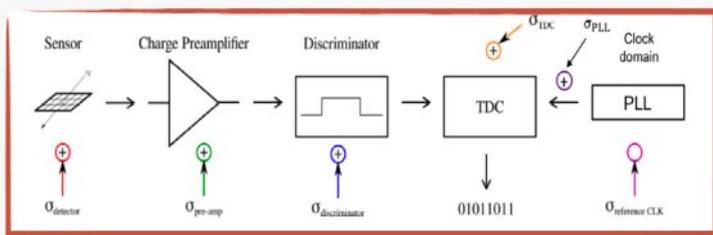
- For events reconstruction
- From Core Hits to avoid contamination

## R&D

- HGCROC ASIC: 3 stage TDC
- Clock distribution (CEA)

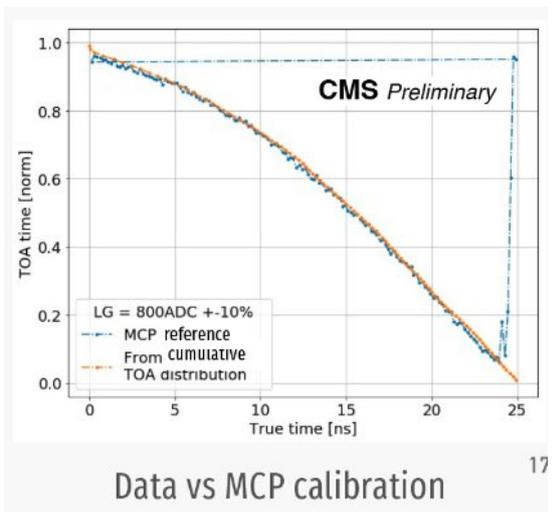


The **clock distribution system** is expected to contribute < 15 ps jitter



$$\sigma_t^2 = \left(\frac{t_{rise}}{S/N}\right)^2 + \left(\frac{t_{rise} V_{th}}{S}\right)_{RMS}^2 + \left(\frac{TDC_{bin}}{\sqrt{12}}\right)^2 + ([TDC]_{RMS})^2 + ([CLK]_{RMS})^2$$

Preamplifier    Time walk    TDC quantization noise and linearity    CLK jitter



- Correction of non-linearity of ToA response

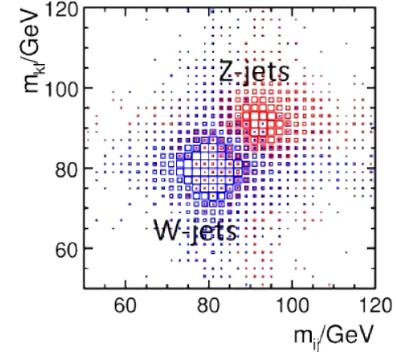
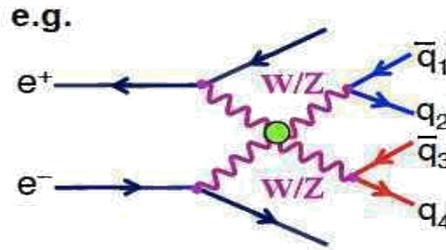
# Requirements from Physics

Basis: sep of  $H \rightarrow WW/ZZ \rightarrow 4j$

- $\sigma_Z/M_Z \sim \sigma_W/M_W \sim 2.7\% \oplus 2.75\sigma_{sep}$

$\Rightarrow \sigma_E/E \text{ (jets)} < \sim 4\%$

- Sign  $\sim S/\sqrt{B} \sim (\text{resol})^{-1/2}$   
 $60\%/\sqrt{E} \rightarrow 30\%/\sqrt{E} \Leftrightarrow + \sim 40\% \text{ in } \mathcal{L}$



Large acceptance

Large Tracker

- Precision and low  $X_0$  budget
- Pattern recognition

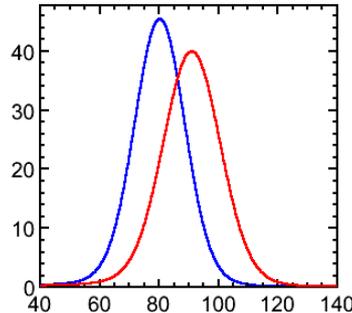
High precision on Si trackers

- Tagging of beauty and charm

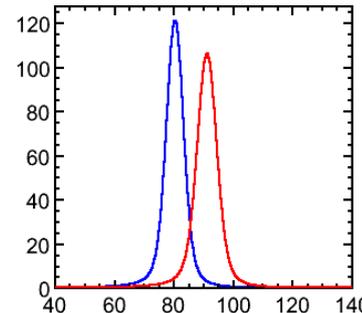
Fwd Calorimetry:

- lumi, veto, beam monitoring

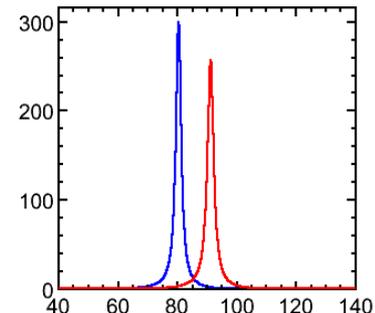
Jets at LEP



3%



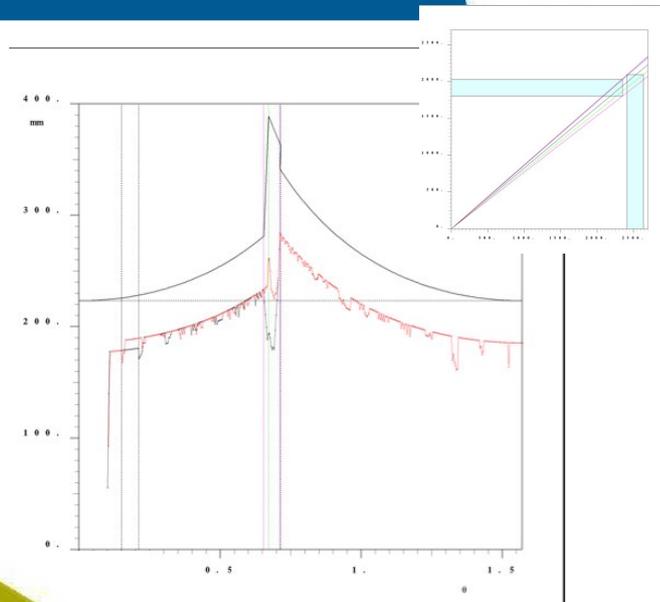
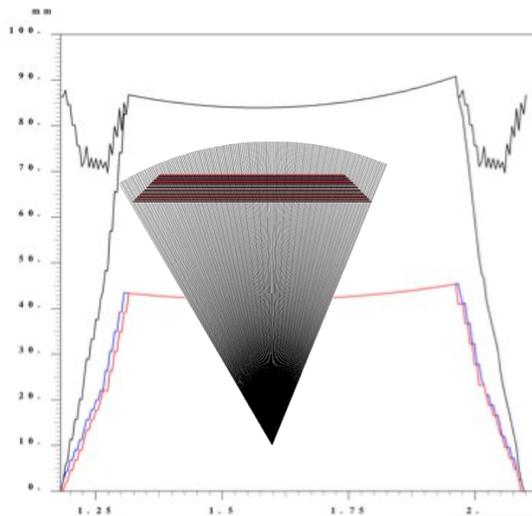
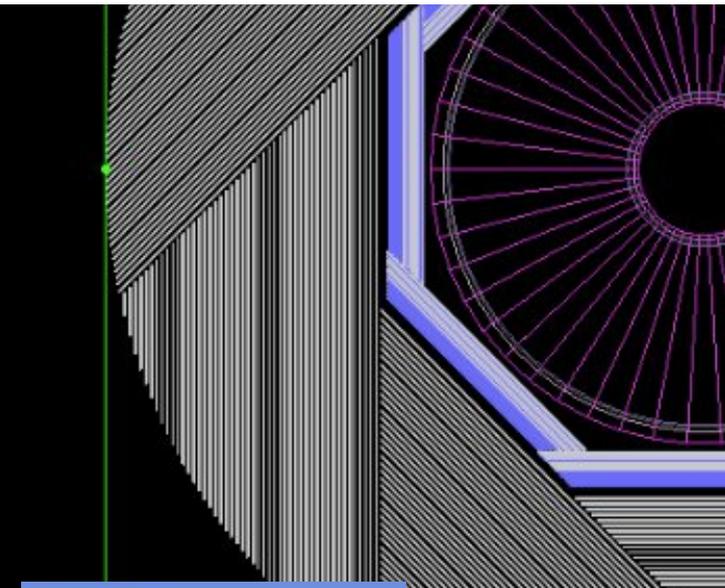
Perfect



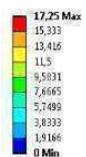
$\sigma_E/E \text{ (}\gamma\text{)} \leq 10 \text{ } \%/ \sqrt{E}$

**Tau Physics ( $\gamma$  vs  $\pi_0$ )  $\rightarrow$  Photons in jets ?**

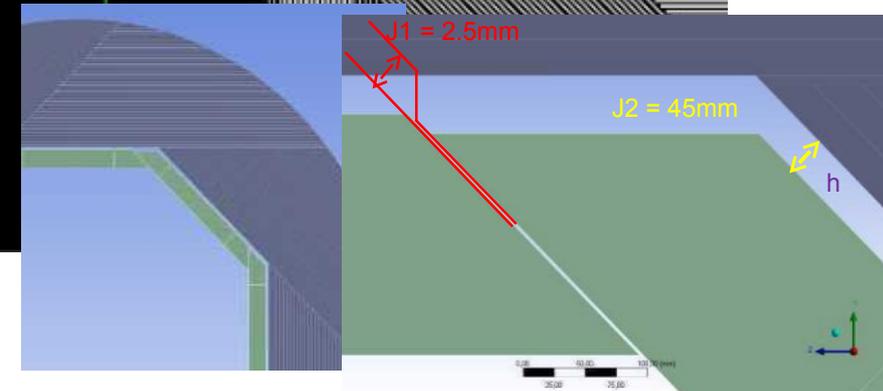
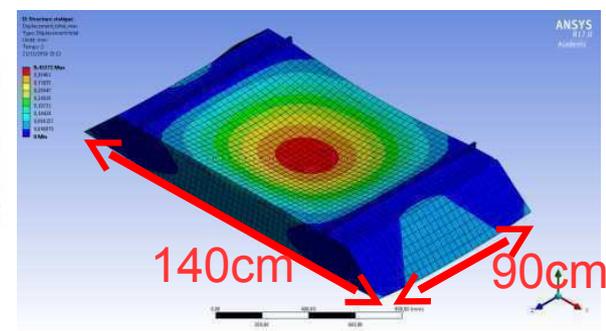
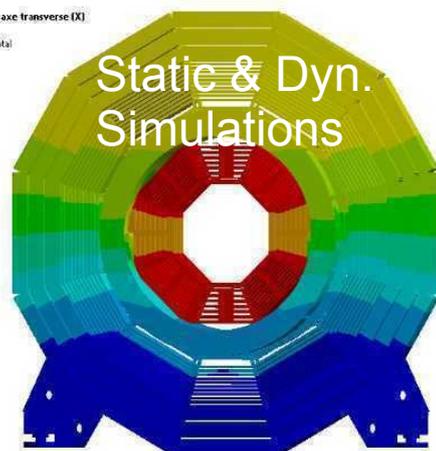
# A crack-less ECAL geometry



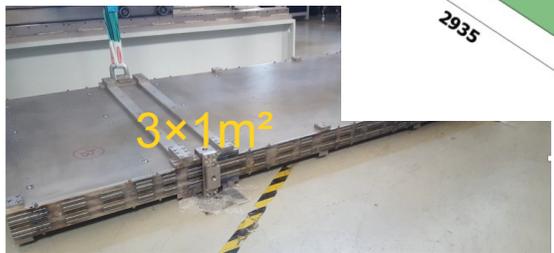
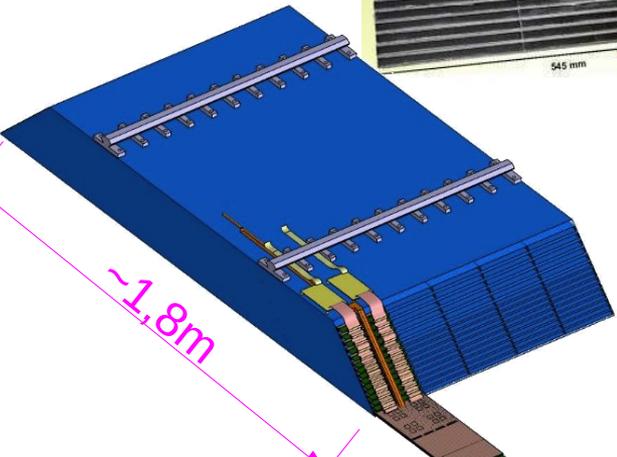
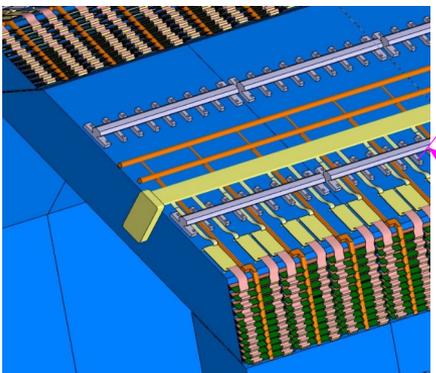
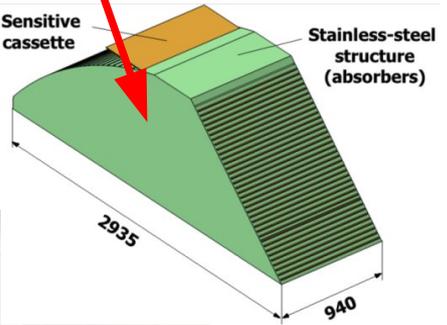
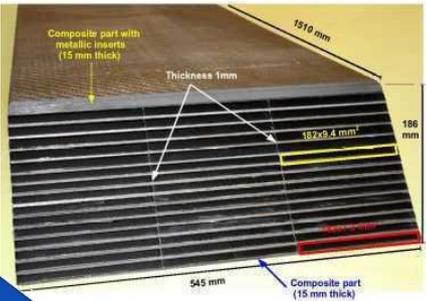
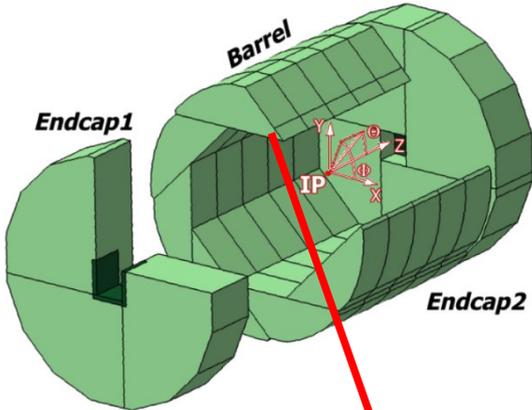
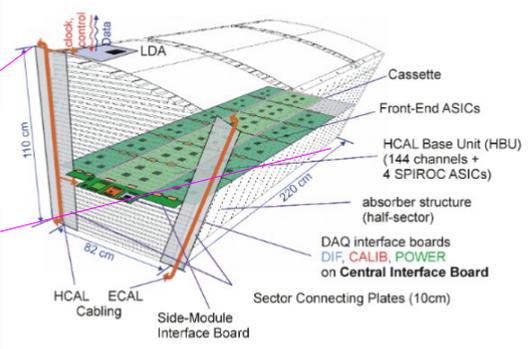
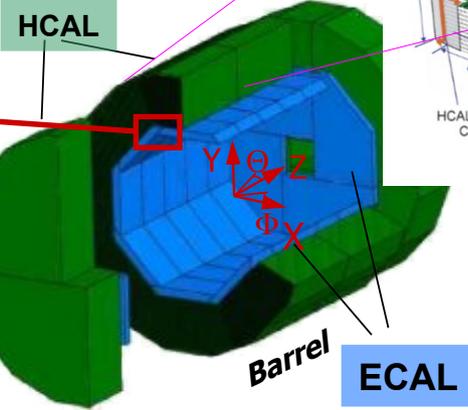
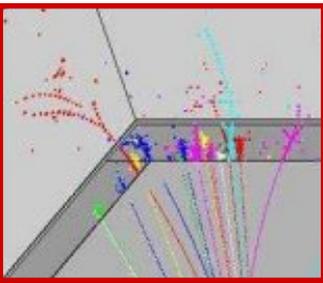
J: Réponse spectrale axe transverse (X)  
 Déplacement total  
 Type: Déplacement total  
 Unités: mm  
 Temps: 0  
 04/09/2017 10:31



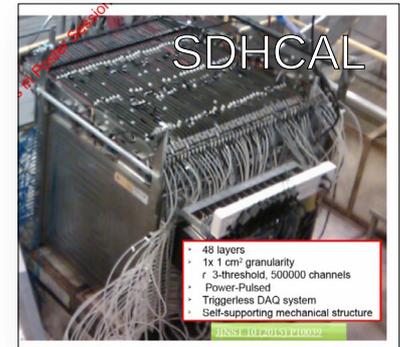
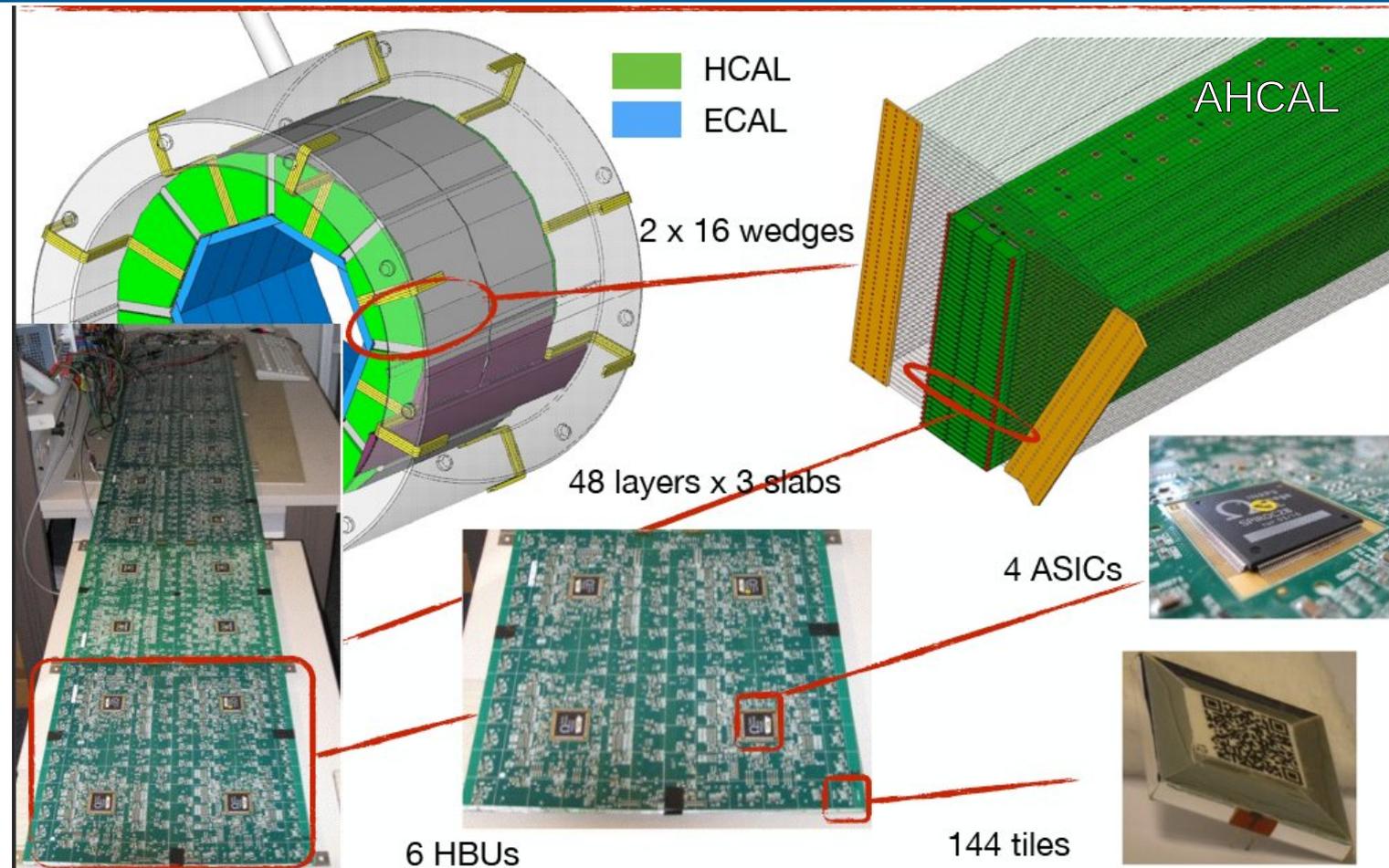
Static & Dyn.  
 Simulations



# Geometries



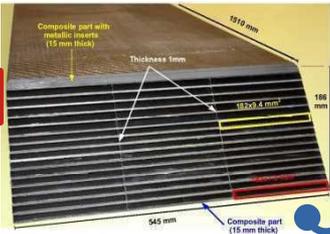
# Large Scale Building : CALICE HCALs



# Reste à faire

## Prototypes technologique de grandes dimensions

LLR



## Pilotes (« modules-0 »)

- 3x1m² HCAL's
- 1.5x0.2m² x 3-5 ECAL

## Intégration du «timing centimetrique» : 1 cm = 30 ps.

- Partout ?
- Couche(s) dédiées ?

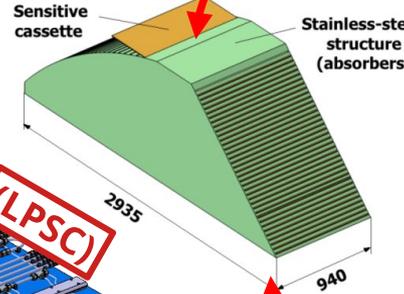
Besoin d'études approfondies

Omega

## Electronique « v3 »

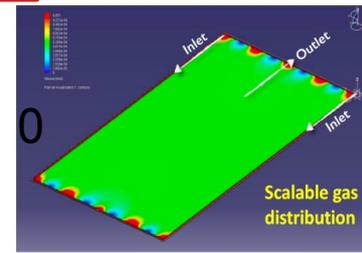
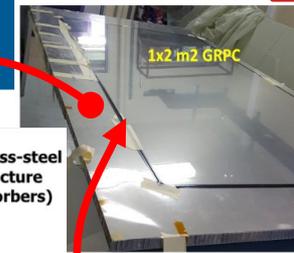
- full 0-suppr, power, timing, nv techno (AMS → TSMC)

LLR + IJCLab

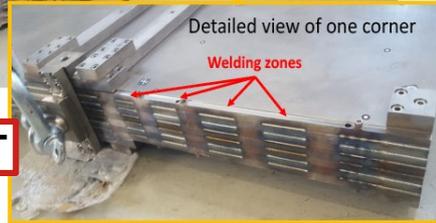
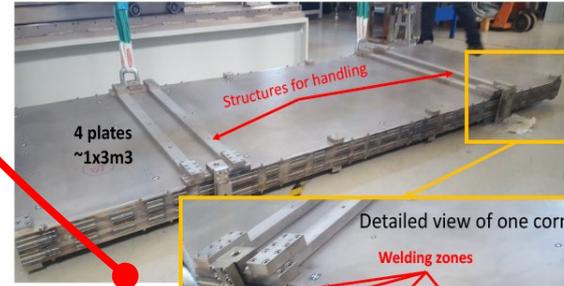


(LPSC)

IP2I



IJCLab



CIEMAT

## Electron beam welding

# AHCAL Plans: Hardware Developments

## Common Readout

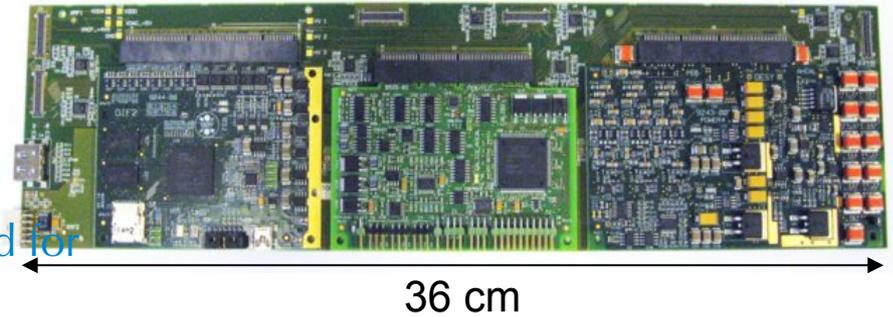
- Harmonise readout between CALICE SiW ECAL and AHCAL

## Reduce size of AHCAL interface boards

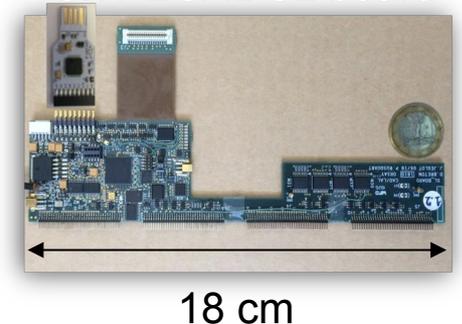
- Current design is from 2007
- Focus was on modularity
- New SiW ECAL interface board (SL board) optimized for compactness
- Plan to follow SiW design as much as possible
  - Some differences in powering concept
  - Additional LED calibration system in AHCAL

**Status: just started**

AHCAL interface boards



SiW ECAL SL board



# T-SDHCAL: cooling & rates

## Cooling:

Previous studies were performed on Hardroc (full regime)

We have to do the studies with the new ASICs and the mechanical structure in mind

## High-rate capability

Low resistivity materials

Low-resistive PEEK ( $10^9 \Omega \cdot \text{cm}$ )



C: sans power pulsing  
Température  
Type: Température  
Unité: °C  
Temps: 1  
31/07/2015 11:28

