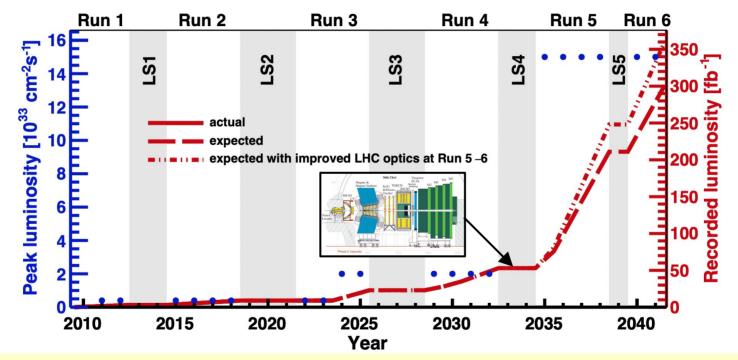
LHCb Upgrade II: UK involvement in R&D activities Tim Gershon

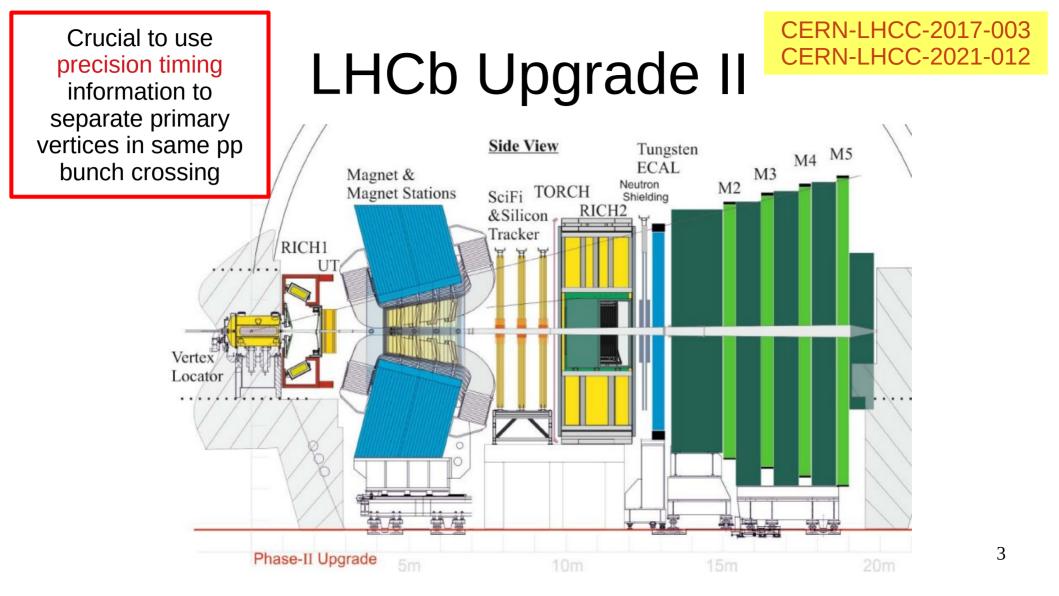
University of Birmingham, University of Bristol, University of Cambridge, University of Edinburgh, University of Glasgow, Imperial College London, *University of Lancaster*, *University of Leicester*, University of Liverpool, University of Manchester, University of Oxford, Rutherford Appleton Laboratory, University of Warwick

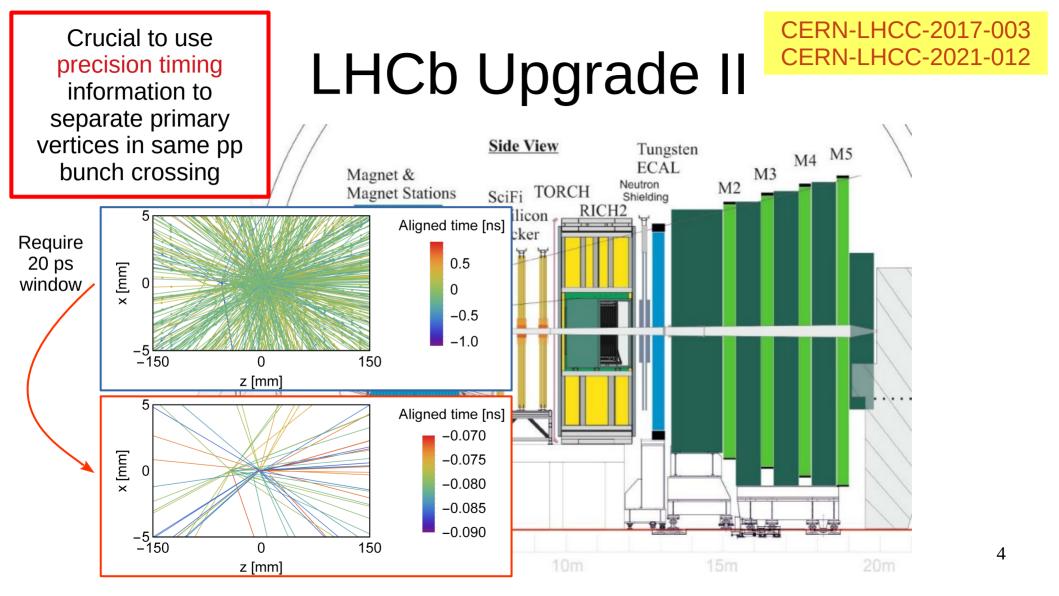
DRD UK Steering Board meeting 5 December 2023

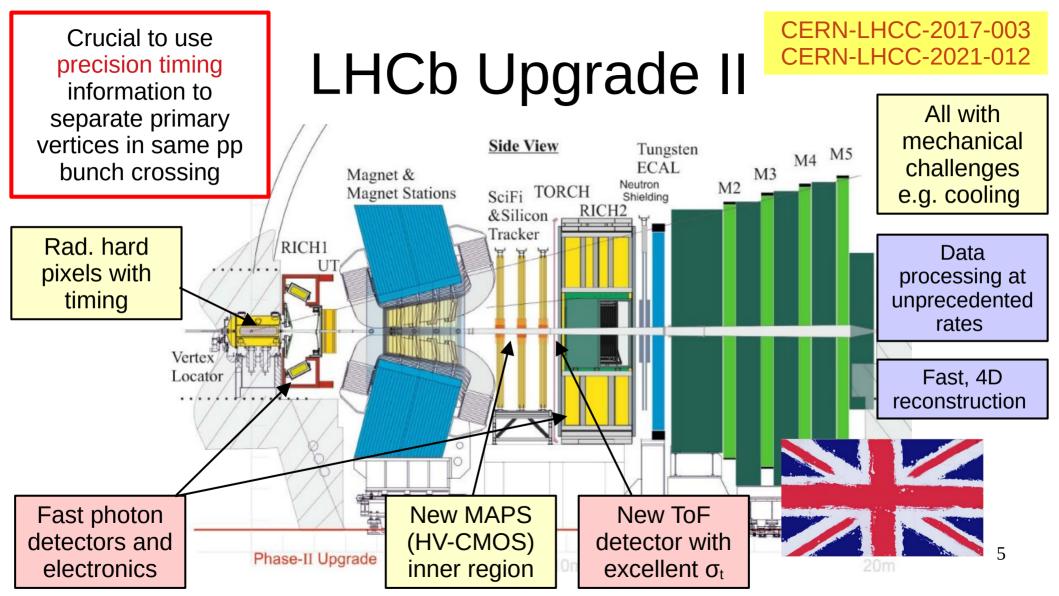
LHCb Upgrade 2: The ultimate HL-LHC flavour physics experiment



50 fb⁻¹ design goal of Upgrade 1 will be achieved by LS4 Opportunity to design detector to increase sample to 300 fb⁻¹ or more Only way to achieve ESPPU goal of full HL-LHC exploitation, including flavour physics

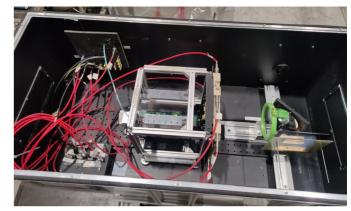






RICH

- Photon detectors
 - Testing LAPPD & SiPMs as candidates
 - Timing response, granularity, radiation hardness
- Electronics
 - ASIC development: FastIC \rightarrow FastRICH
- Mechanics
 - Cooling solutions
 - Minimising GWP risk
 - Leak-proof vessels, alternatives to high GWP radiator gases

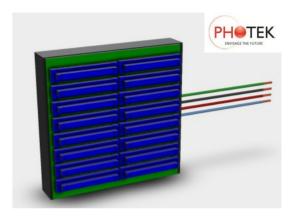


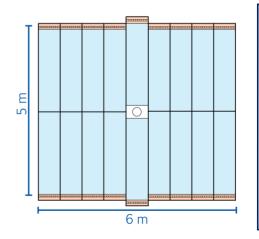
Latest LAPPD workshop https://indico.bnl.gov/event/18642/



TORCH

- Photon detectors
 - Developing next generation MCP-PMTs
 - Collaboration with Photek
 - SiPMs also an option for highest occupancy region
- Electronics
 - Synergy with RICH
- Mechanics
 - Carbon fibre support for large quartz plates

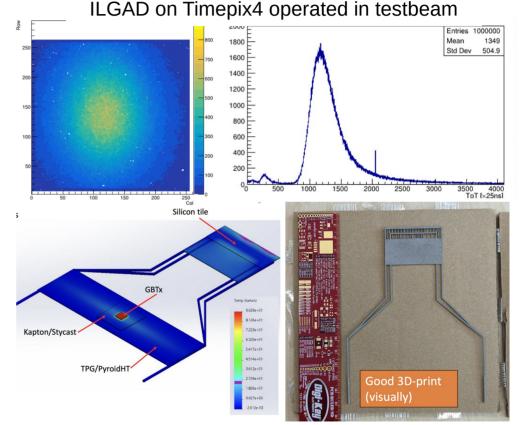




VELO

Sensor

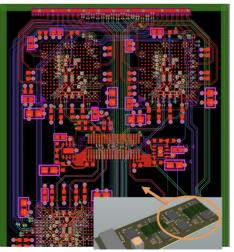
- LGADs for testbeam telescope
- Data transfer
 - Silicon photonics
- Substrate
 - Developing additive manufacturing process
 - Collaboration with Royce institute (Sheffield)
- RF shield
 - Ultra-thin low-Z composite solution



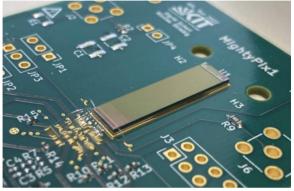
Mighty Tracker

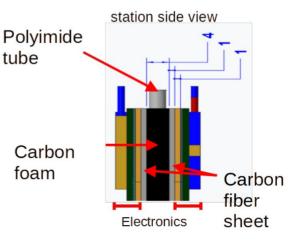
- MightyPix chip
 - evolution of ATLASpix & mupix
 - LHCb compatibility & characterisation
- Electronics & data transfer
 - on & off-module
- Module assembly
 - Cooling challenge

Design of off-chip module electronics with multi-layer PCB layout



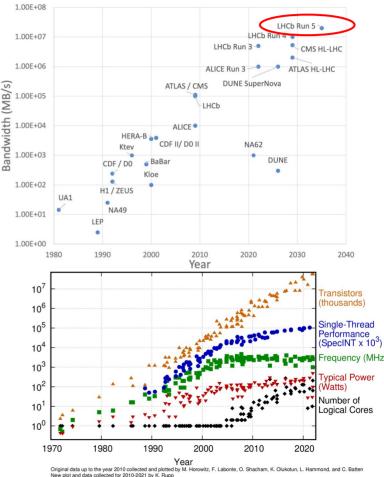
¹/₄ size (0.5 cm x 2 cm) prototype





Data processing

- LHCb data processing scheme must continue to evolve to cope with Upgrade II data rates
 - More complicated events, including timing information
 - Develop algorithms to best exploit most suitable computing architectures
 - Run 3 HLT1 tracking on GPUs; VELO clustering on FPGAs
 - Extend to PID, HLT2 ... investigate other accelerators (IPUs)
- Similar evolution of simulation required
 - Use of heterogeneous architectures & accelerators
 - Full simulation + fast (parametrised) simulation
 - Avoid potential bottlenecks to physics output



10

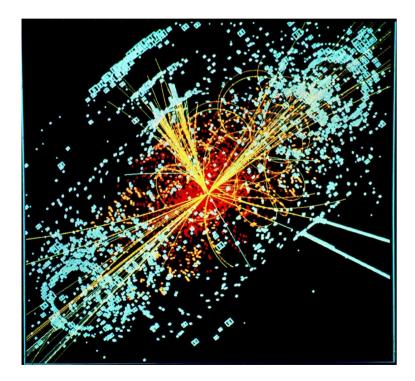
LHCb Upgrade 2 in a nutshell

- Unique science programme with BSM discovery potential
 - Unprecedented sensitivity for B & D physics
 - Broad (general purpose) programme
 - Unique forward acceptance
 - spectroscopy, EW precision measurements, top quark and Higgs physics, dark sector, heavy ions and fixed target physics ...
 - Beyond \sqrt{N} scaling with new subdetectors and reconstruction techniques
- Exciting technology roadmap
 - high granularity, fast timing, extreme radiation hardness
 - developments with impact both inside and outside of HEP
 - relevant for many future experiments, and possible DRD collaborations





LHCb Upgrade II Science Case



Two main ways to search for physics beyond the Standard Model at the LHC

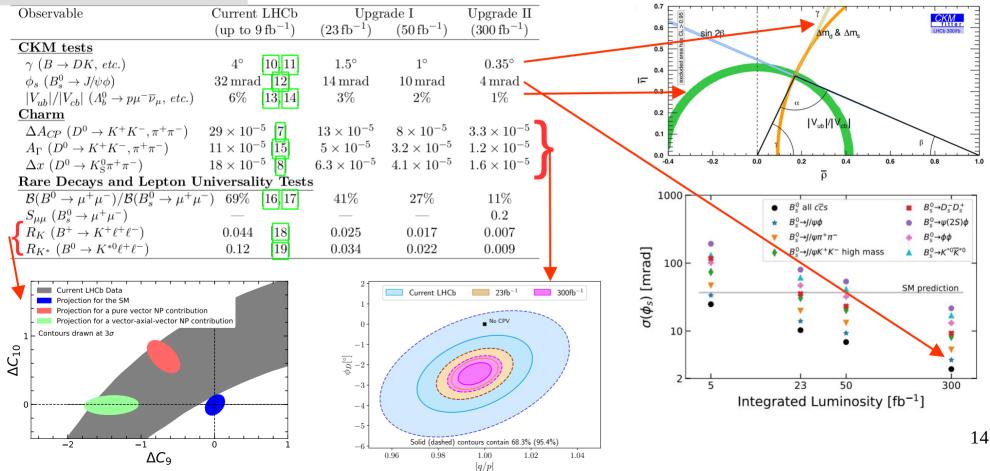
1) Look for new heavy particles produced "on-shell"

- Produced centrally \rightarrow ATLAS & CMS
- Limited by available collision energy
- 2) Look for influence of off-shell particles in loop processes
 - Exploit copious production of beauty and charm hadrons in forward acceptance \rightarrow LHCb
 - Limited by precision, i.e. by available sample size
 - Sensitive to very high energy scales

CERN-LHCC-2018-027 CERN-LHCC-2021-012

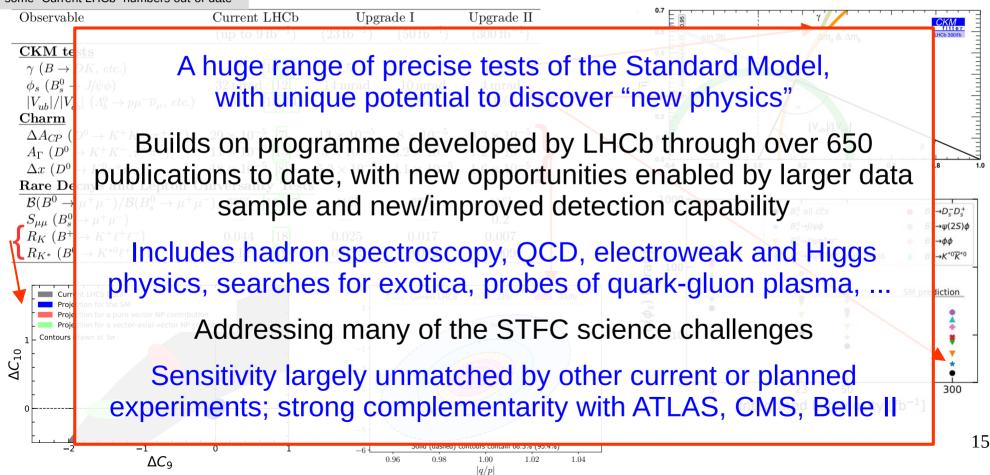
LHCb Upgrade II Science Case

some "Current LHCb" numbers out-of-date



LHCb Upgrade II Science Case

some "Current LHCb" numbers out-of-date



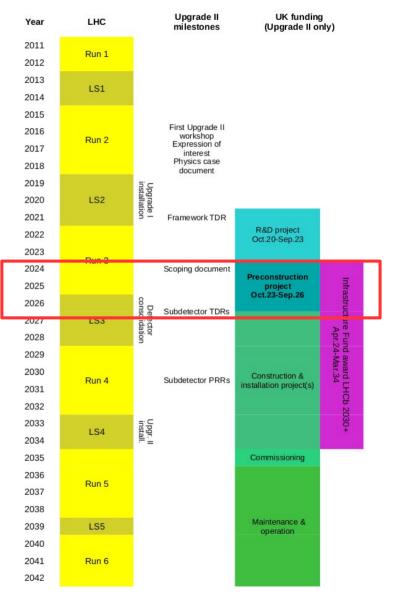
Timeline and UK context

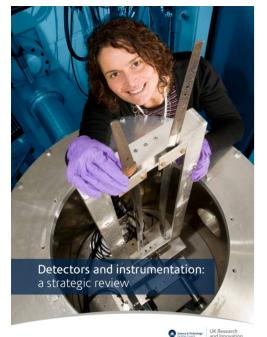
International

- CERN approval processes being established by RRB & LHCC (CERN-LHCC-2022-012, in preparation)
- First step:
 - Framework TDR (2021) + Scoping document (2024)
- Second step:
 - Subdetector TDRs (2026/7)

UK

- Initial R&D project 2020-2324
- Preconstruction phase 202324-26
- UKRI Infrastructure Fund award (LHCb 2030+)
 - Full business case approval expected after Scoping Document





Strategic technologies:

- ultra-fast detectors
- CMOS technology
- Si photodetectors
- data chain, ...

Impact – economic

TORCH detector R&D has already brought a new product to market





PCS-256

Multi-Channel Photon Counting System

The AuraTek PCS-256 is an innovative, photon counting system that combines a Multi-Anode MCP-PMT with the high performance TOFPET readout electronics. The easy-to-use system contains 256 independent, high performance photon counting channels, each having a time stamp with 44 ps resolution and timing performance of < 100 ps rms after amplitude walk correction. The system is self-triggering and event driven, with time and amplitude data provide for each photon detected. The maximum count rate for each channel is 160 kcps. An optional user provided event identifier can be time stamped and included in the data stream by replacing one of the 256 anodes with an external input. The 160 MHz



GRAFHCORE

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UNIVERSITY OF BRISTOL TACKLES HPC CHALLENGES IN PARTICLE PHYSICS WITH GRAPHCORE'S IFU

Collaboration with industry on novel MIMD architecture

Strong engagement with DRD activities at UK and European levels