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SOI Pixel Technology

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Silicon-On-Insulator(SOI) technology was developed in semiconductor industry to realize high-performance and low-power devices, and now it is widely used in many companies. We have been collaborating with Lapis Semiconductor Co. Ltd. (former OKI Semiconductor) to apply this technology for radiation imaging sensor.

The SOI chip has a layer of integrated-circuit which is separated from the chip's substrate by a thin layer of o

To develop the SOI pixel technology was not so easy task, but to keep this kind of technology for long time is mu

Therefore we have been performing Multi-Project Wafer (MPW) run service to the people in universities and laboratories of all over the world. By doing this, we can maintain some amount of volume to keep the process.

Details of the technology and recent results will be presented.

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