

International Workshop on Semiconductor Pixel Detectors for Particles and Imaging (PIXEL2012)

Thursday 6 September 2012

Session6 (08:30 - 12:00)

-Conveners: Junji Haba; Yasuo Arai

time	[id] title	presenter
08:30	[102] Monolithic pixel detectors	SNOEYS, Walter
09:00	[22] High-Voltage Pixel Detectors in Commercial CMOS Technologies for ATLAS, CLIC and Mu3e Experiments	Dr PERIC, Ivan
09:20	[78] Monolithic Active Pixel Matrix with Binary Counters (MAMBO) ASIC, using a nested well structure to decouple the detector from the electronics	FAHIM KHALID, Farah
09:40	[99] Design and manufacturing of a Monolithic Active Pixel Sensor in a CMOS Image Sensor (CIS) 180 nm technology	MYLROIE-SMITH, James Edwin
10:00	[62] Test of TRAPPISTe Monolithic Detector System	SOUNG YEE, Lawrence
10:20	Coffee break	
10:40	[19] Monolithic Pixel on High Resistance Substrate and Sparsifying Redaout Architecture	GIUBILATO, Piero
11:00	[100] Design and characterisation of a Highly Miniaturised Radiation Monitor	GUERRINI, Nicola Carlo
11:20	[16] PImMS1 and PImMS2, monolithic CMOS event-triggered time-stamping image sensors with storage of multiple timestamps at 25ns resolution	JOHN, Jaya John
11:40	[5] New fabrication and packaging technologies for CMOS pixel sensors: closing gap between hybrid and monolithic approach	DULINSKI, Wojciech