

# Performant programming for GPUs

*Thursday 13 June 2024 08:45 (1 hour)*

Programming for Heterogeneous Architectures - lecture 2

- From SIMD to SPMD, a programming model transition
- Thread and memory organization
- Basic building blocks of a GPU program
- Control flow, synchronization, atomics

## Summary

**Presenter:** CAMPORA, Daniel (NVIDIA)