

# NextGen Work Package 1: Infrastructure, Algorithms and Theory

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Next Generation Triggers WP1 Intro  
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# Role in NextGen

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- Providing R&D advances that other tasks from other WPs can benefit from

# Role in NextGen

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- Providing R&D advances that other tasks from other WPs can should benefit from!
- From infrastructure to ML inference: common building blocks for NextGen
- Not a one-way street: to do *relevant* work we want to see your needs
  - What's missing in ML inference on FPGAs to make it your perfect solution?
  - Heterogeneous task scheduling: how many GPUs? FPGAs? Task duration?

# This is the start, with documentation to stay

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- Most of the tasks are still booting up
  - Task members about to be hired
  - Too early to create communication paths?
- Please share this event with your future hires
- We ensure the communication paths shown here stay valid
  - Expect interactions to pick up starting July

# Your WP1

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- Please reach out if you see "potential for improvement"
  - [axel@cern.ch](mailto:axel@cern.ch) ☕
- It's important (to me) that we keep the barriers low
  - Encourage also younger people to suggest "improvements to the system"
- WP1's success is not just its milestones:
  - It's our **relevance for the rest of NextGen**