

Session Program

8-12 Apr 2024

5th 28 nm Mixed-Signal Design Workshop

Analog design

CERN, 593/R-013

Monday 8 April

13:30

Analog design

Session | **Location:** CERN, 593/R-013

13:30–13:50

Analog simulation with Explorer and Assembler

Speaker

Helga Dornelas

15:15

13:50–15:15

Lab session (Analog simulation)

Speaker

Helga Dornelas

Tuesday 9 April

10:45

Analog design

Session | **Location:** CERN, 593/R-013

10:45–11:00

Analog Backend VXL Best Practices

Speaker

Philippe Carriere

12:00

11:00–12:00

Lab session (Analog backend)

Speaker

Philippe Carriere

13:30

Analog design

Session | **Location:** CERN, 593/R-013

13:30–14:45

Lab session (Analog backend)

Speaker

Philippe Carriere

14:45

15:50

Analog design

Session | **Location:** CERN, 593/R-013

15:50–16:10

DRC/LVS with PVS

Speaker

Philippe Carriere

17:30

16:10–17:30

Lab session (DRC/LVS with PVS)

Speaker

Philippe Carriere