1st FPGA Developers' Forum (FDF) meeting



Contribution ID: **70** Type: **not specified**

Test Abstract

test

Talk's Q&A

Talk duration

Will you be able to present in person?

Yes

Primary author: Dr ZOU, Rui (Cornell University (US))

Presenter: Dr ZOU, Rui (Cornell University (US))

 $\textbf{Session Classification:} \ \ \textbf{Algorithm implementation}$

Track Classification: Sharable HDL cores