## 1st FPGA Developers' Forum (FDF) meeting



Contribution ID: 32 Type: not specified

# **CERN** control group cores and tools

Tuesday, 11 June 2024 16:50 (30 minutes)

The CERN control group (in particular the BE-CEM-EDL section, previously BE-CO-HT) is at the origin of the White Rabbit technology. But in addition to this well known project, the section has also developed a set of generic cores (named general-cores), a tool to automatically build project for simulators and synthesizers starting from a python description (hdlmake), as well as a tool to generate HDL, header files and documentation from a register map (Cheby).

I will present how we develop designs using those tools and library.

# Talk's Q&A

During the talk

#### Talk duration

20'+10'

## Will you be able to present in person?

Yes

Primary author: GINGOLD, Tristan (CERN)

Presenter: GINGOLD, Tristan (CERN)

Session Classification: Sharable HDL Cores

Track Classification: Sharable HDL cores