

## **Session Program**

**11-13 Jun 2024**



## **1st FPGA Developers' Forum (FDF) meeting**

### ***Sharable HDL Cores***

CERN, 30/7-018 - Kjell Johnsen Auditorium

## Tuesday 11 June

15:40

### Sharable HDL Cores

**Session** | **Location:** CERN, 30/7-018 - Kjell Johnsen Auditorium | **Conveners:**  
Mathieu Saccani, Dr Francesco Gonnella

15:40–16:20

#### Convenient and reliable clock domain crossings, using scoped constraints and reusable blocks

**Speaker**

Lukas Vik

16:20–16:50

#### COLIBRI: Towards a CERN-wide common cores library

**Speaker**

Alberto Perro

16:50–17:20

#### CERN control group cores and tools

**Speaker**

Tristan Gingold

17:20–17:50

#### Fast Monitoring of FPGA algorithms using SpyBuffers

**Speaker**

Iacopo Longarini

17:50–18:20

#### The BondMachine Project

**Speakers**

Mirko Mariotti, Mirko Mariotti

18:20