

1st FPGA Developers' Forum (FDF) meeting

Wednesday 12 June 2024

Solutions to everyday digital design problems: Morning - 30/7-018 - Kjell Johnsen Auditorium (09:00 - 10:45)

-Conveners: **Filiberto Bonini; Paschalis Vichoudis**

time	[id] title	presenter
09:00	[58] Fine-grained hierarchical placement constraining for timing closure (and more)	NAVARRO TOBAR, Alvaro
09:25	[48] From C to Routed Circuits for FPGAs in Seconds	GUERRIERI, Andrea Prof. GUERRIERI, Andrea
09:45	[53] Method for achieving phase determinism in the transmission side of AMD transceivers	ORZES, Edoardo
10:10	[22] Prospects of RFSoc technology in astroparticle physics experiments	NOVIKOV, Alexander
10:30	[42] FPGA firmware design and verification for the ATLAS Liquid Argon Calorimeter trigger processor	OLIVEIRA FACIO VICCINI, Lucca SILVA OLIVEIRA, Marcos Vinicius AGUIAR, Melissa