

Figure 1: Block diagram of PicoTDC board design.

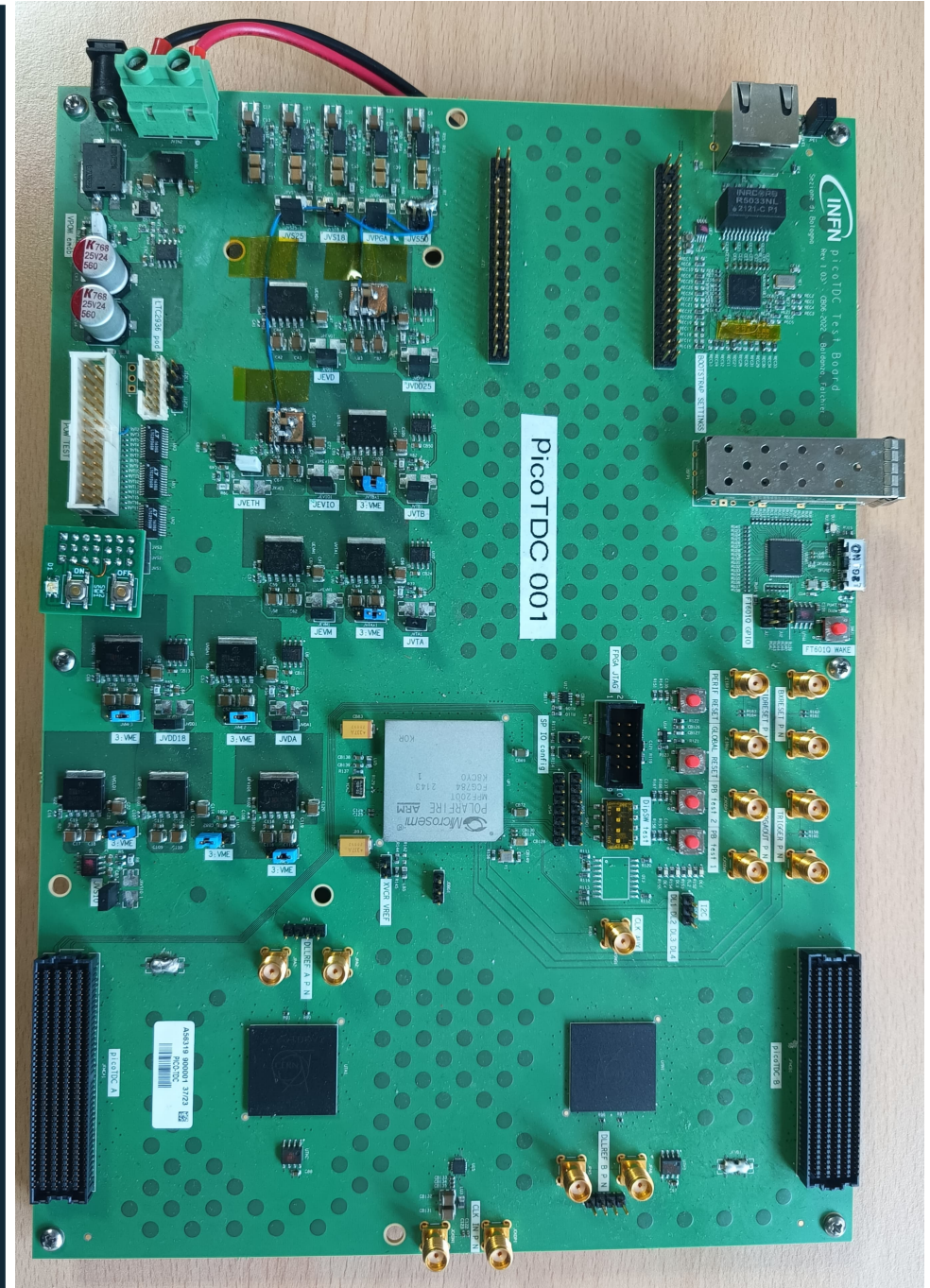


Figure 2: PicoTDC board designed at INFN Bologna. The Cypress Explorer Kit for superspeed USB-C connection is not connected in the picture (piggyback board).

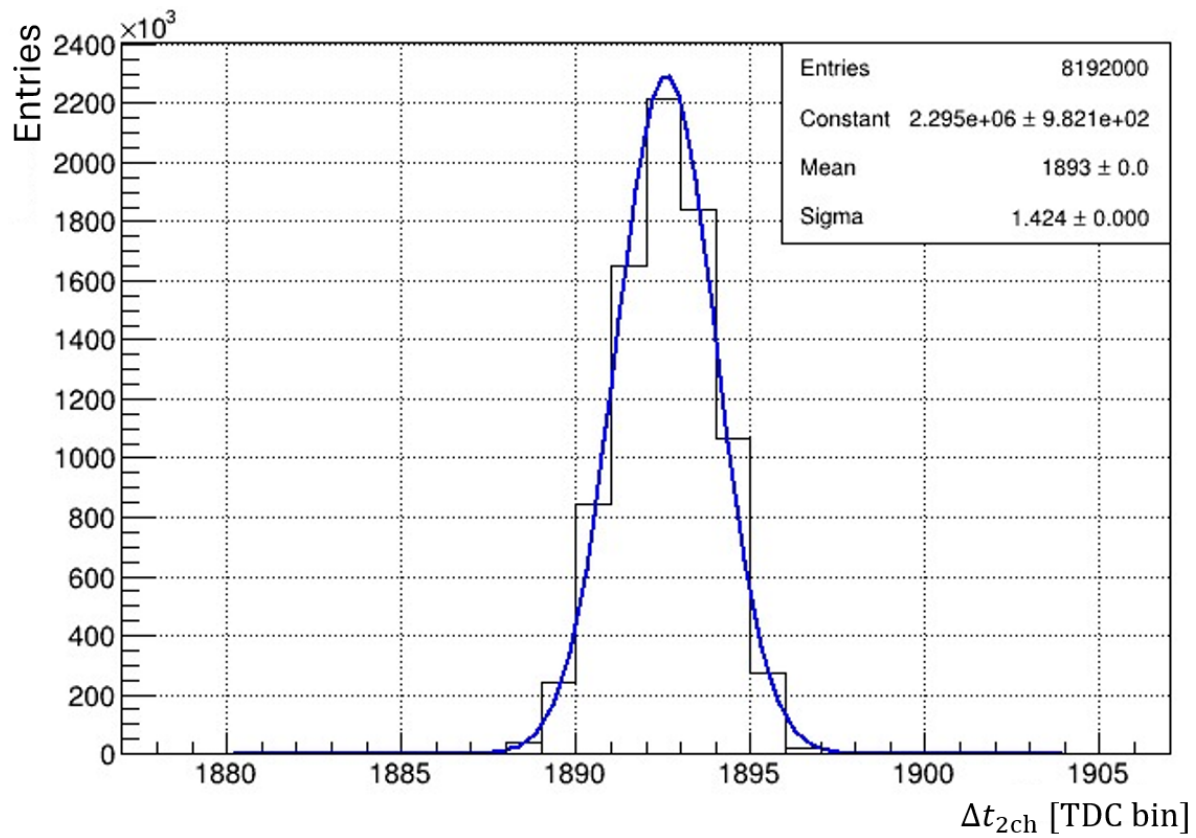


Figure 3: Resolution measurement obtained via a controlled delay line using two PicoTDC channels.

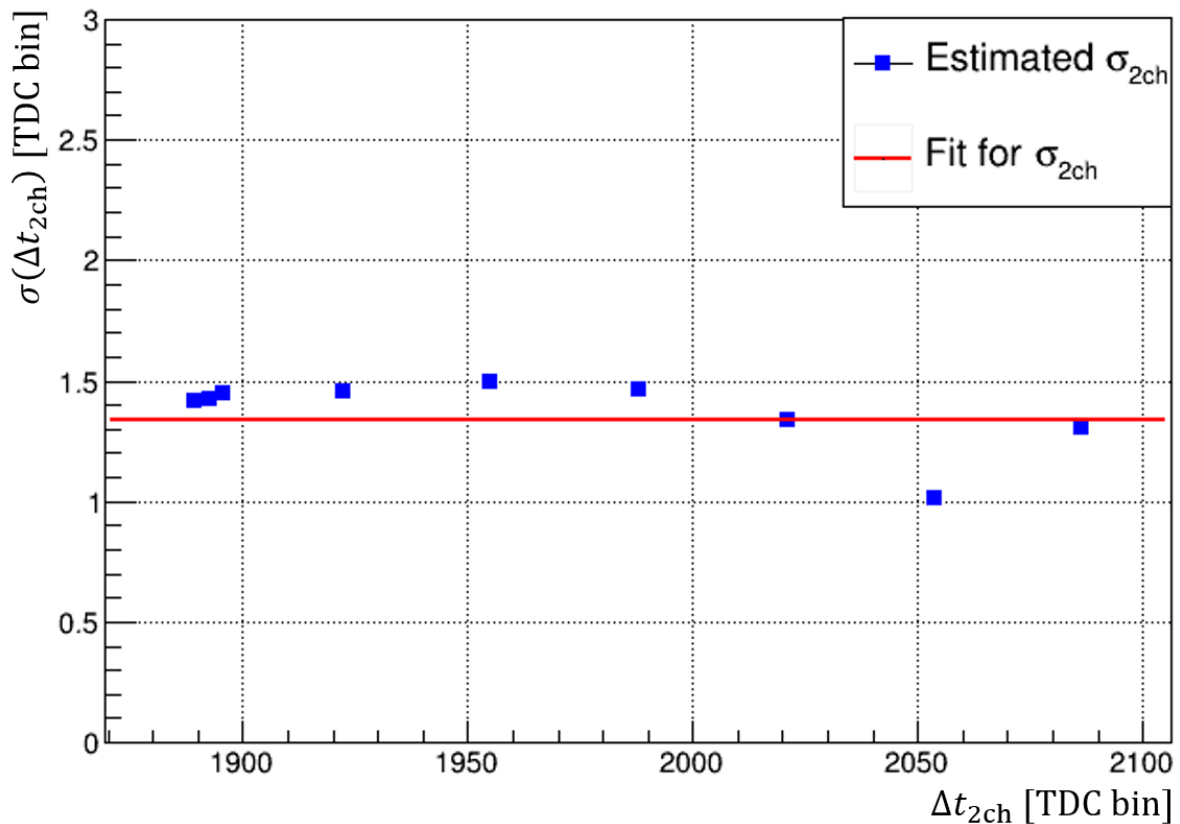


Figure 4: Resolution measurements with different delay line values (from 10 to 600 ps).