

TWEPP 2024 Topical Workshop on Electronics for Particle Physics

Wednesday 2 October 2024

Programmable Logic, Design and Verification Tools and Methods (14:00 - 15:00)

-Conveners: **Andrea Boccardi; Cristina Fernandez Bedoya**

time	[id] title	presenter
14:00	[26] Development of methodology and implementation of SoC-based compact single-board validation system for the ATLAS Phase-II level-0 muon trigger system	NARUKAWA, Yoshifumi
14:20	[45] A Low-Cost, Low-Power Media Converter Solution for Next-Generation Detector Readout Systems	PERRO, Alberto
14:40	[121] Mitigating Multiple Single-Event Upsets During Deep Neural Network Inference using Fault-Aware Training	Mr VINCK, Toon

Programmable Logic, Design and Verification Tools and Methods (15:30 - 16:10)

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time	[id] title	presenter
15:30	[132] Applications of PixESL framework on pixel detectors for High Energy Physics experiments	DHALIWAL, Jashandeep
15:50	[145] Improvements for the implementation of RDMA on FPGA devices	VASILE, Matei