# Performance and Upgrade of the CMS detector

### G. Pugliese

INFN & Politecnico of Bari On behalf of the CMS Collaboration

2024 LHC Days, Hvar 30 September 5 October 2024





# LHC and HL-LHC schedule



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Technical Design Report

 $\Box$ 

uctio

prod

and

R&D



### 1997 Muon Project TDR



### **Outline:**

- CMS performance in RUN3
- CMS upgrade project for Phase 2

# LHC and CMS overview



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CMS instantaneous luminosity  $\sim 2 \ 10^{34} \ \text{cm}^{-2} \ \text{s}^{-1}$ Mean number of interactions per bunch crossing in 2024  $\sim 63!$ 



### Luminosity delivered to CMS:

- Run1+Run2+Run3 ~  $377 \text{ fb}^{-1}$
- In Run3 ~ 184 fb<sup>-1</sup>
- In 2024 ~ 110 fb<sup>-1</sup>
  - ~93% of the delivered data were recorded by CMS!



### Highly irradiated environment, challenging conditions

CMS Luminosity Public results







### CMS performance in RUN3



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#### Silicon Pixel

Sensor technology: n+ implant in n bulk (n-in-n)

- $100x150 \ \mu m^2 \ pixel$
- 79M barrel and 45M forward pixels in 2  $m^2$
- 4 layers in the barrel and 3 disks in the forward
- Operation: -22°C
- Radiation tolerance:  $3x10^{15} n_{eq}/cm^2/yr$

#### Silicon Strip

Sensor technology: p-in-n

Outer cell size ~20cm x 100-200µm Inner cell size ~10cm x 80µm

- 10 layers in the barrel and 12 rings in the endcap
- Operation -25 °C (since June 2024)
- Radiation tolerance  $\sim 1.5 \times 10^{14} \text{ n}_{eq}/\text{cm}^2$

### $\delta \; p_T\!/p_T\!\!< 1 \;\%$ for $p_T < 50 \; GeV \;$ and $< 10 \;\%$ for $p_T = 1 \; TeV$





Strip unchanged

since Run 1

# **RUN3** Tracker performance

Linear fit on thick ercent: 24

Integrated luminosity (fb<sup>-1</sup>)

### **Pixel detector:**

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- ▶ 96%(Barrel), 98%(Forward) operational channels
- > Excellent performances (cluster charge, hit efficiency, hit resolutions, etc.) measured in RUN3. Barrel Layer 1, which was replaced in LS2, is now stabilized









# **CMS** Calorimeters



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#### Electromagnetic system

Homogeneous and hermetic with high granularity leadtungstate (PbWO<sub>4</sub>) crystals

- Barrel (EB): 61200 crystals in 36 super-modules,  $|\eta| < 1.48$ ,  $\approx 26 \text{ X0}$ , Avalanche Photo-Diode (APD) readout
- Endcaps (EE): 14648 crystals in 4-Dees, 1.48< $|\eta|$  <3.0,  $\approx$  25 X0 Vacuum Photo-Triode (VPT) readout

Pre-shower (endcaps only): 3X0 of Pb/Si strips,  $1.65 < |\eta| < 2.6$ 

#### Hadronic system

- Hadronic Barrel (HB) and Endcap (HE) calorimeters: sampling calorimeter of brass absorber plates and plastic scintillators,  $|\eta| < 3.0$ 
  - HB: 36 wedges, brass absorber plates and 17 plastic scintillator tiles; 40000 scintillator tiles
  - HE: two brass/scintillator discs with 19 longitudinal layers

Outside the magnet coil:

- Hadronic Outer (HO) system: scintillator tiles 1/2 layers, SiPMs readout, |η|<1.26</li>
- Hadronic Forward (HF) system: 11.5 m from the interaction point. 36 wedges mads of steel absorber with quartz fibers embedded along its length ( $\approx$ 1000km fibers),  $3 < |\eta| < 5$





Density of 8.3 g/cm3, radiation length 0.89 cm, Molière radius 2.2 cm,  $\approx 80\%$  of scintillating light in  $\approx 25$  ns, refractive index 2.2, light yield spread among crystals  $\approx 10\%$ 

### $\sigma$ (E ) $\sim$ 1.6 – 5%/ $\sqrt{E} \oplus$ 0.5%

### Run2 design





 $\sigma$  (E) ~ 84.7%/ $\sqrt{E} \oplus 7.6\%$  HB Brass absorber



## RUN3 ECAL performance



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### Evolution of the ECAL response to laser light



- ➤ The energy resolution, as computed with the di-electron invariant mass (Z → e+e-), is stable (2022 and 2023)
- Successfully automated calibration workflow



# CMS Muon Systems



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four

Muon system: based on three gaseous technologies for muon identification, timing and momentum measurement

Muon acceptance:  $|\eta| < 2.4$ 

#### Drift Tubes (DT)

- 250 chambers, ≈ 170k channels
- o 32 number of hits
- Spatial resolution≈100  $\mu$ m
- Time resolution  $\approx 2 \text{ ns}$



**RUN2** design

#### **Resistive Plate Chambers (RPC)**

- 540 trapezoidal chambers,  $\approx 120$ k channels
- 6 (4) number of hits
- Spatial resolution  $\approx 1 \text{ cm}$
- Time resolution  $\approx 1.5$  ns





#### Cathode Strip Chambers (CSC)

- 540 trapezoidal chambers,  $\approx$ 500k channels
- 24 number of hits
- Spatial resolution  $\approx 50 \div 140 \,\mu m$ 
  - Time resolution  $\approx 3$  ns



#### CMS Preliminary pp data, 2022, 2023, 2024 (13.6 TeV) 160 **cham** 2024: <eff> 99.8% - # underflows 2 CMS Preliminary **Š** 140 2023: <eff> 99.7% - # underflows 2 Number of Rolls 2022: <eff> 99.8% - # underflows **RPC** Barrel 250 120 Mean(>70%) %(<70% 2018 (59.90 fb<sup>-1</sup>, 13 TeV) 2.62% 95 62% 100 13 30 5 1.16% 200 2022 (29.81 fb<sup>-1</sup>, 13.6 TeV 95.07% 14 273 95.22% 1.79% 15 25 2 1 31% 80 2024 (22 04 fb<sup>-1</sup> 13 6 TeV) 95 70% 150 1.6 22.8 17 207 1.8 18.8 19 17 0 100 50 20 4.0 2.1 5 6 7 8 9 10 3 4 11 12 z (m) 0 70 0.92 0.94 0.96 0.98 100 75 80 85 90 95 **DT Segment Reconstruction Efficiency** Efficiency [%] **DT** segment efficiencies Barrel RPC efficiency in RUN3 (2022, in RUN3 CSC active channel in RUN3 2023, 2024) and RUN2 (2018) **CMS** Preliminary 2024 31.0 fb<sup>-1</sup> (13.6 TeV) CMS Preliminary e ME+42 2023 2024 0 9 CSC Segment Efficiency ME+41 ME+31 ME+22 ME+21 ME+13 ... ME+12 **CSC** segment ME+11B ME+11A efficiencies in 2024 ME-114 ME-11B 0.4 ME-12 ME-13

0.2

#### Stable performance of the Muon legacy system over time and with instantaneous luminosity increases

ME-21

ME-22

ME-31 ME-32 ME-41

ME-42

10

15

20

25

30

35 Chamber #

**RUN3** Muon System performance



100

95

94

93<sup>L</sup>

2022

20

Live channels

60

40

Readout Channels (stable beams only)

80

Elapsed time in weeks since July 05, 2022

100

120

CSC channels in readout (%)

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1 02



HCAL ECAL

Silicon tracker

0

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- 72 Super-Chambers (SC), consisting of two triple-GEM detectors Installed in LS2
   3456 VFAT3 chips, 432 GBT and VTRx optical link
   2 number of hits
   Spatial resolution~100

- $\circ$  Time resolution  $\approx 10$  ns
- coverage  $1.55 < |\eta| < 2.18$
- Several calibration runs successfully taken in 2024  $\geq$ at different HV settings and Frontend chip (VFAT) configurations
- Chamber efficiencies optimized reaching 94%  $\geq$ efficiency







1.8 18.8° 1.9 17.0%

20 154

2.8 7.0°

4.0 2.1°

50 0.77 12 z (m)



 $\geq$ New on-chamber electronics FPGA firmware deployed to suppress the strip x-talk significantly improving the time resolution



CMS Preliminary

Efficiency

0.6

04

0.2

# CMS Trigger System



40 MHz Detectors Digitizers Front end pipelines 100 kHz Readout buffers Switching networks Processor farms

18 fb<sup>-1</sup> (13.6 TeV)

. . . . . . . . . . . .

BMTF

|n| ≤ **0.83** 

Tight L1 guality

p\_<sup>µ,L1</sup>≥5 GeV

40 50 60 p<sup>µ</sup><sub>-</sub> (probe reco) [GeV] The CMS Trigger System is organized in two levels:

- Level-1 Trigger hardware based reduces the data/event rate from the crossing rate of 40 MHz to no more than 100 kHz, with 4µs latency
- 2. High Level Trigger (HLT) filtering events with software running on computing farm, to further reduce the event rate for storage to 1 kHz
  - The Level 1 Trigger operations were stable at throughout 2024 collecting data up to 115 kHz recording up to 64 simultaneous collisions/event (2.5x CMS design, 45% of HL-LHC)
  - Good performance of all standard Level-1 Trigger Objects

18 fb<sup>-1</sup> (13.6 TeV)

**1.24 <** |η| ≤ **2.4** 

– p<sup>⊥,L1</sup>≥ 22 GeV

p\_<sup>µ,L1</sup>≥5 GeV

40 50 60 p\_{\_{T}}^{\mu} (probe reco) [GeV]

Tight L1 guality

EMTF

### L1 muons efficiency

**CMS** Preliminary

0.6

04

0.2

DP-2023/057

18 fb<sup>-1</sup> (13.6 TeV)

OMTF

0.83 < |ŋ| ≤ 1.24

Tight L1 quality

- p\_\_\_\_\_2 22 GeV

- p\_\_<sup>µ,L1</sup>≥ 5 GeV

40 50 60 p<sup>µ</sup><sub>-</sub> (probe reco) [GeV] CMS Preliminary

20



### L1 $\tau$ efficiency

# INFN' Level-1 Trigger & High Level Trigger HLT) (a) RUN3



Detectors **40 MHz** Digitizers LV1 Front end pipelines 100 kHz Readout buffers Switching networks HLT Processor farms ~1 kHz

The CMS Trigger System is organized in two levels:

- Level-1 Trigger hardware based reduces the data/event rate from the crossing rate of 40 MHz to no more than 100 kHz, with 4µs latency
- High Level Trigger (HLT) filtering events with software running on computing 2. farm, to further reduce the event rate for storage to 1 kHz

### Smooth data-taking at HLT.

> Average HLT rates:  $\sim 2$  kHz Promptly reconstructed,  $\sim 5$ kHz Parked data, ~27 kHz HLT-Scouting





Comparison of the dimuon spectra obtained with scouting and offline







CMS upgrade project for Phase 2

# 

20

00

60

Mean number of interactions per crossing

80

100

# LHC and HL-LHC schedule



G. Pugliese 2024 LHC Davs Today Phase 1 Phase 2 HL-LHC LHC Run 2 Run 3 Run 4 - 5... Run 1 LS1 LS2 LS3 EYETS EYETS 13.6 TeV 13.6 - 14 TeV 13 TeV energy **Diodes Consolidation** splice consolidation LIU Installation cryolimit **HL-LHC** 8 TeV button collimators interaction inner triplet 7 TeV installation Civil Eng. P1-P5 pilot beam radiation limit regions **R2E project** 2016 2027 2011 2012 2013 2014 2015 2017 2018 2019 2020 2021 2022 2023 2024 2025 2026 2028 2029 2040 5 to 7.5 x nominal Lumi **ATLAS - CMS** upgrade phase 1 **ATLAS - CMS** experiment beam pipes **HL upgrade** 2 x nominal Lumi 2 x nominal Lumi **ALICE - LHCb** nominal Lumi upgrade 75% nominal Lumi integrated 3000 fb<sup>-1</sup> 30 fb<sup>-1</sup> 190 fb<sup>-1</sup> 450 fb<sup>-1</sup> luminosity 4000 fb<sup>-1</sup> **HL-LHC TECHNICAL EQUIPMENT:** C **DESIGN STUDY** INSTALLATION & COMM. **PROTOTYPES** CONSTRUCTION PHYSICS CMS Average Pileup (pp,  $\sqrt{s}$ =13 TeV) 6000 ■ Run II: <µ> = 34 (00.1/ 5000 HLC HL-HLC HL-HLC 10 years of running at higher **2018:** <µ> = 37 2017: <µ> = 38 rates and radiation doses "ultimate" "baseline" 2016: <u> = 274000 **2015:** <µ> = 13 uminosity 3000 L Vertex Density (PU)  $\int \mathcal{L} / \text{year}$  $5 \cdot 10^{34} \,\mathrm{cm}^{-2} \,\mathrm{s}^{-1}$  $\sigma_{in}^{pp}$  (13 TeV) = 80.0 mb  $250 \, {\rm fb}^{-1}$ Baseline 140 0.8/mm 2000 Ultimate  $7.5 \cdot 10^{34} \text{ cm}^{-2} \text{ s}^{-1}$  $> 300 \, {\rm fb}^{-1}$ 200 1.2/ mm Recor 1000

220

140

260

280

220

200



# CMS Upgrade Project



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The CMS detector has to be upgraded to cope with expected HL-LHC conditions (highest rate, fluence and pileup ever achieved) for new measurements and new physics searches





# CMS Upgrade Project





### Let's focus on the new detectors!









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resolution lower than 40 ps!





Total surface 14 m<sup>2</sup>



# Tracker Upgrade



**INNER TRACKER** 

 $5 \text{ m}^2 - 2G$  channels

TBPX (Tracker Barrel PiXel): 4 layers

TFPX (Tracker Forward PiXel): 8 small disks

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Key features of the new Phase 2 Tracker:  $\blacktriangleright$  Extended tracking acceptance up to  $|\eta| = 4$ ► Increased granularity

#### Phase 2



# Tracker Upgrade



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Key features of the new Phase 2 Tracker:
Extended tracking acceptance up to |η| = 4
Increased granularity

#### Phase 2



#### New functionally: inclusion of the Outer Tracker data in the Level 1 Trigger

Local transverse momentum  $(p_T)$  measurements done using the pair layers on each module (local track-stub finding)



The reconstruction mechanism was verified in test beam campaigns

I. Zoi, https://doi.org/10.22323/1.448.0021

#### Stub efficiency for 2S prototypes



#### **OUTER TRACKER**

1.8

2.0

2.2

2.4

2.6

3.0 3.2 4.0

η

z (mm)

**190 m<sup>2</sup> – 213M channels** TBPS (Tracker Barrel with PS modules) TB2S (Tracker Barrel with 2S modules) TEDD (Tracker Endcap Double Disks)

- → Two type of technologies: microstrips and macro-pixel
- → Tilted barrel geometry for better trigger performance and reduction on number of modules



- The module design is the final
- Almost all components are in preproduction or production
- Good noise performance was found with pre-series PS and 2S modules

# Muon Upgrade Project

0.8

40.40

02 03 04 05 06

HCAL

ECAL

Silicon

R (m)

73 1° 67 7° 62 5° 57 5° 52 8° 48 4° 44 3°

Wheel 1

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- 1. New detectors will be installed in the high-eta region to:
- Restore redundancy
- > Extend the muon coverage up  $\eta = 2.8$
- Improve Trigger efficiency without increasing the trigger rate
- Improve Muon reconstruction





2. New electronics for the legacy detectors:
DT: replace all on-board electronics (OBDT), BE
RPC: replace all off-chamber electronics, BE
CSC: replace selected FE boards, replace all BE
3. Longevity Studies (including the use of ecological gas mixtures)
Few results in back-up







n 0°



and 1.6 cm in  $\eta$ 

Working Point = 7.2 kV

![](_page_24_Picture_0.jpeg)

# ME0 design & performance

![](_page_24_Figure_2.jpeg)

![](_page_24_Figure_3.jpeg)

#### **Requirements :**

- $\blacktriangleright$  97% module efficiency
- $\geq$  < 500 µrad resolution
- $\blacktriangleright$  8-10 ns time resolution
- ▶  $\leq 15\%$  gain uniformity
- ▶ Rate capability: 150 kHz/cm<sup>2</sup>
- ➢ Radiation hardness: 7.9 C/cm<sup>2</sup>

### ✓ Detector prototype performance satisfies all requirements:

The First ME0 stack prototype was assembled and tested at CERN 904 laboratory and in several test beams

![](_page_24_Figure_13.jpeg)

Spatial Resolution: 240 μrad Efficiency locally > 99% 
 CMS Muon Preliminary
 MEO GIF++ test beam

 0.975
 No compensation - τ = 141.2 ± 11.8 ns

 0.950
 Compensation - τ = 90.7 ± 6.8 ns

 0.925
 0.900

 0.925
 0.900

 0.875
 0.850

 0.825
 0.800

 10<sup>4</sup>
 10<sup>5</sup>

 Measured background rate (kHz/strip)

Rate capability: 2.5% efficiency loss with the highest background (loss in highest eta region) mitigated (1%) by the redundancy

![](_page_24_Picture_17.jpeg)

![](_page_24_Figure_18.jpeg)

**Average time resolution** of track segments as a function of the number of ME0 chambers (layers) used to reconstruct the segment. The time resolution 5.4 ns for 6-layer segments

![](_page_24_Picture_20.jpeg)

![](_page_25_Picture_0.jpeg)

# CMS Upgrade in production..

![](_page_25_Picture_2.jpeg)

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![](_page_25_Picture_4.jpeg)

![](_page_26_Picture_0.jpeg)

# CMS Upgrade in production..

![](_page_26_Picture_2.jpeg)

![](_page_26_Picture_3.jpeg)

![](_page_27_Picture_0.jpeg)

# Conclusion

![](_page_27_Picture_2.jpeg)

![](_page_27_Picture_3.jpeg)

 Smooth RUN3 data-taking and excellent performance of the CMS detector
 We are pushing the detector performance beyond design limits!

The challenging CMS Phase 2 upgrade project defined to exploit the full potential of the HL-LHC luminosity is progressing well
 Most upgrade projects moved into mass production; a few are finalizing the pre-production validation; GE1/1 chambers have already been installed and integrated into CMS

![](_page_28_Picture_0.jpeg)

![](_page_28_Picture_1.jpeg)

## Thanks!

![](_page_28_Picture_3.jpeg)

# Backup slides

# Credits to the CMS People

![](_page_28_Picture_6.jpeg)

# HL-LHC: beyond present detector ability

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![](_page_29_Picture_2.jpeg)

At higher pileup (140-200PU), due to growing spatial overlap of tracks and energy deposits, degradation in resolutions, efficiencies reconstruction, and misidentification rates are expected

![](_page_29_Figure_4.jpeg)

- 4Dimensional (space and time) vertex reconstruction with unprecedented tracktiming precision (~30 ps)
- Higher detector granularity to keep almost the same occupancy

![](_page_29_Figure_7.jpeg)

ECAL Barrel Calorimeter Upgrade

#### Key features for the Barrel Calorimeter:

Lead-Tungstate crystals

APDs Pre-Amplifier

- ➤ Keep the lead tungstate crystals and APDs
- Decrease operation temperature from 18 to 9°C to mitigate the increase in the APD leakage current

Master IpGBT ASIC Control (2.5Gbps) Readout (10Gbps)

x Readout IpGB eadout (106bos)

ersatile link plus Control link

Readout links

New VFE card New FE card

Replace the on- and off-detector electronics. New front-end boards will allow the exploitation of the information from single crystals in the LV1 trigger

Test beam campaigns in 2018 and 2021 proved that the new electronics met the requirements for HL-LHC:

ADC

- ✓ Energy resolution in agreement with Phase 1 performance
- ✓ Time resolution <30 ps with electron energy beam >50 GeV
- Integration test of all electronics chain (VFE-FE card-off detector) expected for fall 2022

![](_page_30_Figure_10.jpeg)

CMS Preliminar

![](_page_30_Figure_11.jpeg)

![](_page_30_Figure_12.jpeg)

![](_page_30_Picture_13.jpeg)

ECAL Barrel

![](_page_30_Figure_14.jpeg)

![](_page_30_Picture_15.jpeg)

![](_page_30_Picture_16.jpeg)

# Muon Upgrade

![](_page_31_Picture_1.jpeg)

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New GEM and RPC detectors needed to improve efficiency reconstruction and trigger performance at HL\_LHC

![](_page_31_Figure_4.jpeg)

- To maintain the high level performance in HL-LHC environment, the CMS muon system is being upgraded
  - to increase the muon spectrometer redundancy, to sustain the high radiation in the endcap region
  - □ GEM+CSC allow for muon momentum measurement in a single station, which helps reduce considerably L1 trigger rate

![](_page_32_Picture_0.jpeg)

![](_page_32_Picture_1.jpeg)

![](_page_32_Picture_2.jpeg)

	R134a (%)	HFO-1234ze (%)	CO <sub>2</sub> (%)	i-C <sub>4</sub> H <sub>10</sub> (%)	SF <sub>6</sub> (%)	GWP
STD	95.2			4.5	0.3	1485
ECO2		35	60	4	1	476
ECO3		25	69	5	1	527
Density (g/l)	4.68	5.26	1.98	2.69	6.61	
GWP	1430	7	1	3	22800	

### 1. Double gap layout of iRPC prototype

![](_page_32_Picture_6.jpeg)

→ ~ 45 mC/cm<sup>2</sup> of cha integrated, so far
 → Stable performance with without gamma background

![](_page_32_Figure_8.jpeg)

#### Aging test with Gas mixture: ECO2 mixture

![](_page_32_Figure_10.jpeg)

→ ~200 mC/cm<sup>2</sup> of charge integrated, so far
 → a slight shift of the efficiency curves toward higher voltages in 2023, and an increase in the current density observed

![](_page_33_Picture_0.jpeg)

![](_page_33_Picture_1.jpeg)

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The on-detector Refurbishment of Electronics in LS2

![](_page_33_Picture_5.jpeg)

 108 ALCT-LX150T Mezzanine boards installed in all ME234/1

 288 ALCT-LX100T Mezzanine boards installed in ME1/1,123/2

o 504 DCFEBv2 installed in ME1/1 and 45 in ME+2/1, older DCFEB from ME1/1 → ME234/1

 New boards capable of optical readout

![](_page_33_Picture_10.jpeg)

### CMS

### **Chamber Re-Installation**

![](_page_33_Picture_13.jpeg)

![](_page_33_Picture_14.jpeg)

2: Transport

3: Load on Fixture

![](_page_33_Picture_17.jpeg)

![](_page_33_Picture_18.jpeg)

4: Hoist with crane

![](_page_33_Picture_20.jpeg)

5: Install+Commission on CMS

x288 Inner-Ring Chambers!

### CSC Electronics Upgrade motivation

**On-chamber and off-chamber electronics** to be replaced in order to handle the CMS trigger requirements at HL-HC

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![](_page_34_Figure_3.jpeg)

(D)CFEB event losses for HL-LHC conditions

Board	Num.	Where	Main reasons for upgrade
DCFEB	540	ME12/1	Latency and rate, rad-hardness
ALCT	396	ME1234/12	Latency and rate, rad-hardness
LVDB5	108	ME234/1	Power levels of DCFEBv2s
OTMB	108	ME234/1	Receive optical link from DCFEBv2s
ODMB	180	ME1234/1	Increased DAQ output bandwidth
HV	40/12	ME1234/1	Increased current due to higher occupancy
FED	14	USC	Increased data volume, number of links

![](_page_34_Figure_5.jpeg)

![](_page_34_Picture_6.jpeg)

![](_page_35_Picture_0.jpeg)

# **GEM Overview Project**

![](_page_35_Picture_2.jpeg)

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![](_page_35_Figure_4.jpeg)

![](_page_36_Picture_0.jpeg)

### Phase 2 OUTER Tracker modules

![](_page_36_Picture_2.jpeg)

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![](_page_36_Picture_4.jpeg)

- Two type of modules:
  - 2S Modules
    - 2 different spacing : 1.8mm & 4mm
    - 2 micro strip sensors with 5cm x 90μm strips
    - Sensor dimension are 10cm x 10cm
      - two column of 1016 strips

![](_page_36_Figure_11.jpeg)

- PS Modules
  - 3 different spacing : 1.6mm & 2.6mm & 4mm
  - One strip sensor: 2.5cm x 100μm strips
  - One macro Pixel sensor : 1.5mm x 100  $\mu m$  pixels
  - Sensor dimension 5cm x 10 cm
    - <u>two column of 960 strips</u>
    - o <u>32x960 pixels</u>

![](_page_36_Picture_19.jpeg)

First prototypes (with almost final chips and hybrids) assembled this year  $\rightarrow$  now it's time to test and test and test...

![](_page_36_Picture_21.jpeg)

![](_page_37_Picture_0.jpeg)

### Phase 2 Tracker Upgrade

![](_page_37_Picture_2.jpeg)

#### G. I ugnese

### **Tilted Barrel Geometry**

- Track stubs that cross different modules in lower and upper sensor are lost
- With tilted geometry ineffiencies are recovered

![](_page_37_Figure_7.jpeg)

![](_page_37_Figure_8.jpeg)

![](_page_37_Figure_9.jpeg)

MTD: barrel timing layer Sensors

### **BTL sensors: LYSO:Ce crystals & SiPMs**

### LYSO:Ce crystals

- Well established technology (PET)
- Fast scintillation kinetics:
  - rise time ~100 ps
  - decay time ~ 40 ns

- Radiation hard proven up to:
  - 50kGy with y from 60Co source

~10% LO loss

2

- 3 x 1014 1MeV nea/cm2
- High Light Yield: 40000 v/ MeV

Comprehensive study of LYSO:Ce crystals from 12 producers performed to identify potential BTL suppliers and set the QA/QC requirements for the production stage.

Parameter of the crystal array	Specification before irradiation (mean value)	Specification after irradiation to 50 kGy (mean value)	RMS for crystals within one array
Light output (LO) / end	> 4000 photons/MeV		< 5%
LO (30ns) / LO (450ns)	> 26%		< 3%
Decay time $(\tau)$	< 45  ns		< 3%
$(LY/\tau @-30^{\circ}C) / (LY/\tau @20^{\circ}C)$	> 1		
Loss of light output after irradiation		< 18%	< 5%
Optical cross talk	< 15% and	< 15%	< 5%

![](_page_38_Picture_15.jpeg)

#### SiPMs

- Well established technology
- · Compact and robust
- Insensitive to magnetic fields
- Fast recovery time <10 ns</li>
- High dynamic range (10<sup>5</sup>)
- PDE@Lyso emission peak 20-40%
- Radiation hard proven up to: - to 2 x 1014 1MeV neg/cm2

#### FBK, 20 µm thin glass enc HPK, 300 µm silicon resin enc.

![](_page_38_Picture_25.jpeg)

![](_page_38_Picture_26.jpeg)

![](_page_38_Picture_27.jpeg)

![](_page_38_Picture_28.jpeg)

![](_page_38_Picture_29.jpeg)

![](_page_38_Picture_30.jpeg)

10 11 12

Producer #

![](_page_38_Picture_31.jpeg)

![](_page_39_Picture_0.jpeg)

# DAQ and Trigger Upgrade

![](_page_39_Picture_2.jpeg)

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### **HL-LHC DAQ-HLT Parameters**

CMS datastar	LHC Phase 1	HL-1 Pho	LHC
Peak /PU	Filase-1	140	200
	00	140	200
L1 accept rate (maximum)	100 kHz	500 kHz	750 kHz
Event Size at HLT input	2.0 MB <sup>a</sup>	6.1 MB	8.4 MB
Event Network throughput	1.6 Tb/s	24 Tb/s	51 Tb/s
Event Network buffer (60s)	12 TB	182 TB	379 TB
HLT accept rate	1 kHz	5 kHz	7.5 kHz
HLT computing power <sup>b</sup>	0.7 MHS06	17 MHS06	37 MHS06
Event Size at HLT output <sup>c</sup>	1.4 MB	4.3 MB	5.9 MB
Storage throughput <sup>d</sup>	2  GB/s	24 GB/s	51 GB/s
Storage throughput (Heavy-Ion)	12 GB/s	51 GB/s	51 GB/s
Storage capacity needed (1 day <sup>e</sup> )	0.2 PB	1.6 PB	3.3 PB

![](_page_39_Figure_6.jpeg)

![](_page_40_Picture_0.jpeg)

![](_page_40_Picture_1.jpeg)

See dedicated talk by Jean-Baptiste Sauvan ( (this session)

- Brand new calorimeter in the endcap region: the High Granularity Calorimeter (HGCal)
- High granularity and timing performance for the electromagnetic and hadronic sampling calorimeters
- Mixed technologies. Challenging integration in CMS

![](_page_40_Picture_7.jpeg)

![](_page_40_Picture_8.jpeg)

	Electromagnetic CE-E		Hadronic CE-H	
Active	Silicon Sensors (Hexagonal shape)		Plastic Scintillator tiles	
Area	620 m <sup>2</sup>	400 m <sup>2</sup>		
N. of Modules/channels	30k/6M		4k/240k	
Channel Size	0.5 -1 cm <sup>2</sup>		4 -30 cm <sup>2</sup>	
N. of layers	28		22	
Depth	th $26 X_0 / 1.7 \lambda$		9 λ	
Absorber	Lead	Stainless Steel		

![](_page_40_Picture_10.jpeg)

![](_page_40_Picture_11.jpeg)

Prototypes showed excellent results on test beams:

![](_page_40_Figure_13.jpeg)

![](_page_40_Figure_14.jpeg)

Space-time precision needed to clearly separate objects and clean pileup hits

![](_page_40_Figure_16.jpeg)

**Energy rand time resolution (~2**0 ps for positron energy > 200 GeV)

VBF H  $\rightarrow \gamma \gamma$ 

# INFN G. Pugliese

# **Beam Radiation Instrumentation and** Luminosity (BRIL)

![](_page_41_Picture_2.jpeg)

BRIL system: 14 technical detectors installed in CMS for luminosity measurement, beam induced background, beam loss (abort) and timing monitoring

LS<sub>2</sub>

BRIL

The Fast Beam Condition Monitor (BCM1F) and the Pixel  $\succ$ Luminosity Telescope (PLT) were upgraded to achieve the required high luminosity precision during the Run 3 and were calibrated in emittance scans at early LHC collisions

#### Luminometers showing excellent performance $\succ$

![](_page_41_Figure_6.jpeg)

HFET/HFOC: Hadron Forward calorimeter with the transverse energy sum method and online occupancy method

- Background and abort systems all operational
  - Good progress with Beam Halo Monitor

![](_page_41_Picture_10.jpeg)

![](_page_41_Figure_11.jpeg)

### BRIL UPGRADE project

![](_page_42_Picture_1.jpeg)

#### G. Pugliese

INFN

![](_page_42_Figure_3.jpeg)

#### Bonner Sphere Spectrometer (BSS) Unfolded neutron spectrum

![](_page_43_Picture_0.jpeg)

### ECAL Phase 2 Upgrade

![](_page_43_Picture_2.jpeg)

G. Pugliese

### **Upgraded FE Electronics: Single Channel**

![](_page_43_Figure_5.jpeg)

Crystal and APDs are kept from Phase 1

➡ Two new ASICs have been designed in the VFE:

- 1 The **CATIA** (CAlorimeter Trans-Impedance Amplifier) with two gain channels to cover a dynamic range from 50 MeV to 2 TeV
- The LiTE-DTU (Lisbon-Turin Electronics Data Transmission Unit) to perform analog-to-digital conversion, gain selection, data compression and transmission
- The data words from the single channel are sent to one of the four IpGBT ASICs on the new FE board via e-links

![](_page_44_Picture_0.jpeg)

### ECAL Phase 2 Upgrade

![](_page_44_Picture_2.jpeg)

#### **G.** Pugliese

#### **CATIA ASIC Overview**

12 14 10 18 20

![](_page_44_Figure_5.jpeg)

sample #

- Two gain stages after the pre-amplifier: G1 and G10
- Implemented in 130 nm (TSMC)
- Range: from 50 MeV to 200 GeV (G10) and 2 TeV (G1)
- Bandwidth: 35 MHz

A faster analog electronics improves

- ➡ Time resolution
- Spike rejection
- Noise mitigation

**LITE-DTU ASIC Overview** 

External test clock 1.28 GHz 160 MHz PLL .28 GHz 1. ADCs Gain Selection 2 ATU Data Compression Control Unit ADC Test Unit 4 Serializers

Sampling rate 40 MHz  $\rightarrow$  160 MHz ➡ improves time resolution

- Implemented in 65 nm (TSMC)
- Two 12-bit 160MHz ADCs bought from an external company
- PLL from lpGBT design

Digital logic to perform:

> Gain selection

1.28 Gb/s

- Baseline subtraction >
- > Data compression > Serial transmission
- The digital logic and the configuration registers are protected against SEU with TMR

![](_page_45_Picture_0.jpeg)

- HCAL is successfully operating in RUN3 collisions with timing and conditions derived using splash events and LHC commissioning runs
- Collision data used for channel-by-channel timing adjustment

![](_page_45_Figure_3.jpeg)

- New online algorithm for Trigger Primitive Transverse Energy (E<sub>T,TP</sub>) deployed and compared with the offline reconstructed hit energy E<sub>T, RH</sub>. The response is:
  - consistent across the three different pileup scenarios within uncertainties
  - $\triangleright$  stable along  $\eta$  in the barrel and endcap regions

![](_page_46_Picture_0.jpeg)

![](_page_46_Picture_1.jpeg)

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### Lead Tungstate Crystal Longevity

- Main concern for ECAL crystals is ageing due to radiation
- Scintillation mechanism is not affected by radiation
  - Radiation creates crystal defects which reduce the crystal transparency and therefore light output
- Effect is monitored and corrected using a dedicated light injection system
- MC simulations have been used to predict the light output in Phase II
  - Validated using test beam data studying effect of hadron irradiation on crystals

![](_page_46_Figure_10.jpeg)

Left: ECAL laser response over Run 1 and Run 2 (2011-2018) Right: Expected Phase II light output for 50 GeV photon showers with respect to CMS conditions in 2010

![](_page_46_Figure_12.jpeg)

#### Avalanche Photodiode (APD) Longevity

- Two causes of radiation damage to APDs:
  - Gamma rays creating surface defects
    - Increasing surface current
    - Reducing quantum efficiency
  - Hadrons creating bulk damage
    - Causing an increase in the bulk current
- Main concern for HL-LHC is the increase of dark current
  - Electronic noise depends on square root of bulk current
  - Can be mitigated by reducing the operating temperature

![](_page_46_Figure_23.jpeg)

![](_page_47_Picture_0.jpeg)

### Run3 ECAL Calorimeter performance

Response to laser light

![](_page_47_Picture_2.jpeg)

G. I ugnes

The new laser workflow, which allows updates to HLT conditions once per fill, has been successfully deployed

The automation of calibration workflows is also being commissioned

Now is 2022-08-30 14:00:12  $\eta$ =2.5 ix=iy=39 EE+4 Id=872436647 65 60 55 60 1 last tag - hlt tag 45 40 $10^{-0}$  to 0 to 0 to  $20^{-0}$  to  $10^{-1}$  to  $10^{-0}$  to  $10^{$ 

The 2022 history of one channel in EE, showing that the per-fill update of the HLT tag (which is not performed in no-collision periods) follows closely the system response.

![](_page_48_Picture_0.jpeg)

![](_page_48_Picture_1.jpeg)

![](_page_48_Figure_3.jpeg)

## MTD: Endcap timing layer

#### G. Pugliese

ETL will be instrumented with Low Gain Avalanche Diodes (LGADs) optimized for timing measurements

LGADs are provided with a gain layer, a highly-doped thin layer near the p-n junction

- ----> High local electric field producing charge multiplication
- ---- Moderate gain factor 10-30 to maximize signal/noise ratio

#### Sensor requirements:

- Pad size determined by occupancy and read-out electronics (rather large capacitance, 3-4 pF)
- · Gain uniformity
- Low leakage current to limit power consumption and noise
- Provide large and uniform signals. >8 fC when new. >5 fC after highest irradiation point
- Minimized "no-gain" area, interpad distance < 50 µm</li>

![](_page_49_Figure_13.jpeg)

![](_page_49_Figure_14.jpeg)

Low Gain Avalanche Diode

### M. Tornago, **ICHFP 2022**

The final sensor will be a 50  $\mu$ m-thick 16  $\times$  16 pad array with 1.3  $\times$  1.3 mm<sup>2</sup> pads

![](_page_49_Figure_18.jpeg)

The Endcap Timing Layer Read-Out Chip (ETROC) is the ETL read-out ASIC

![](_page_49_Figure_19.jpeg)

![](_page_49_Picture_20.jpeg)

# Muon Reconstruction

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![](_page_50_Figure_2.jpeg)

**Global muon reconstruction (out side –in)**: a standalone muon is propagated to match a tracker track. If matching is positive a global fitting is performed.

**Tracker Muon (inside – outside)**: a tracker track is propagated to muon system and qualified as muon if matching with standalone or one segment.

![](_page_50_Picture_6.jpeg)

![](_page_51_Picture_0.jpeg)

# 2024 LHC Days

![](_page_51_Picture_2.jpeg)

### **MEO**

- ME0 modules based on triple-GEM technology (same as GE1/1 and GE2/1)
- 18 stacks per endcap
- Each stack is made of six triple-GEMs
- Total 216 triple-GEM chambers
- Coverage 2.0 <  $\eta$  < 2.8 and 0.6 < R < 1.5 m
- 20° trapezoidal shape

### **Stack design**

![](_page_51_Figure_11.jpeg)

![](_page_51_Picture_12.jpeg)

![](_page_51_Picture_13.jpeg)

![](_page_51_Picture_14.jpeg)

![](_page_52_Picture_0.jpeg)

L1+HLT efficiency

1.2

1.1-

0.9

0.8

0.7

0.6

Mu24

IsoMu24

0.5 -2 -1.5 -1 -0.5 0 0.5 1 1.5 2

η<sup>reco</sup>

![](_page_52_Picture_1.jpeg)

Istituto Nazionale di Fisica Nuclear Sezione di Pavia

Primary Dataset Rates

## **Muons at HLT**

- **Total HLT prompt rate** for v1.4 menu = 1771 Hz, excluding rate from parking • dataset (calculations done with instantaneous luminosity =  $2 \cdot$  $10^{34} cm^{-2} s^{-1}$ 
  - PhysicsMuon stream rate = ~450 Hz
  - Old SingleMuon and DoubleMuon united in a unique Primary Dataset

p\_reco [GeV]

0 20 40 60 80 100 120 140 160 180 200

Monitoring the muon reconstruction efficiency at L1 and HLT using Z ٠ resonance Tag & Probe

![](_page_52_Figure_7.jpeg)

0.5

-2

0

2

1

3

φ<sup>reco</sup>

-1

15 20 25 30 35 40 45 50 55 60

Number of reconstructed primary vertices

![](_page_53_Picture_0.jpeg)

![](_page_53_Picture_1.jpeg)

13.6 TeV

 $p_{\tau}^{reco} > 26 \text{ GeV}$ 

Run ≥ 357610

L1 SingleMu22

## Muons at HLT

η

- Total HLT prompt rate for v1.4 menu = 1771 Hz, excluding rate from parking dataset (calculations done with instantaneous luminosity =  $2 \cdot 10^{34} cm^{-2}s^{-1}$ 
  - PhysicsMuon stream rate = ~450 Hz
  - Old SingleMuon and DoubleMuon united in a unique Primary Dataset
- Monitoring the muon reconstruction efficiency at L1 and HLT using Z resonance Tag & Probe

![](_page_53_Figure_7.jpeg)

CMS Preliminary Run 2022D

Run < 357610

L1 SingleMu22

![](_page_53_Figure_8.jpeg)

 $p_T$ 

- Improve Trigger hit time resolution from 25 ns to 1.5ns
- 1376 link boards, 216 control boards

![](_page_53_Figure_11.jpeg)

efficiency

1.2

φ