Manufacturing techniques for PCB and Detectors:

Base materials Photolithography Multilayer structures Drilling/milling Plating Test Example of productions

Tuesday December 3, 2024

# Rigid /Flex materials

#### Rigid

#### • Glass epoxy

- G10 , FR4, High TG, Low loss , low Dk , anti CAF , low CTE etc..
- Aramid Epoxy
  - Low CTE
- Glass polyimide
  - G30 ,High temperature , high reliability
- Glass Teflon , ceramic Teflon
  - High frequencies
- Bakelite
  - Low cost

#### Flex

- Polyimide
  - General flex application
  - Low intrinsic radiation
  - Low outgassing

LCP , PEEK

- High reliability flex application
- low moisture absorption , low outgassing
- High frequencies
- PET, PEN
  - Low cost







Elite Material Co., Ltd.

#### Technical Data

http://www.emctw.com

### Lead-free , Halogen-free Material EM-370(5) / EM-37B(5)

- Superior thermal resistance for lead-free process
- Halogen, antimony and red phosphorus free
- For LCD, memory module and mobile device application

#### Basic Laminate Property

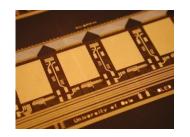
Item		IPC-TM-650	Test condition	Unit	Typical Value
Glass transition temp.		2.4.25	DSC	°C	155
CTE, X-, Y-axis		2.4.24	Pre-Tg, TMA	ppm/'C	12/15
CTE, Z-axis		2.4.24	Alpha 1, TMA	ppm/'C	40
			Alpha 2, TMA	ppm/'C	190
Z-axis Expansion		2.4.24	50~260°C, TMA	%	2.60
Decomposition temp.		2.4.24.26	TGA	°C	385
Thermal stress 10sec 288°C		2.4.13.1	Clad	-	Pass Visual
			Etched	-	Pass Visual
Water abs	orption	2.6.2.1	E-1/105+D-24/23	5	0.11
			as received	lb/in	7.4
	0.5 oz	248	- · ·		7.4
Peel strength		ample o	8.6		
	1		after thermal stress	lb/in	8.4
Permittivity		0550	0.04/09/50	-	4.8
Permittivity	1 MHz	2550	0.04/09/60		4.0
(RC 50%)	1 MHz 1 GHz	2.5.5.9	C-24/23/50	-	4.0
(RC 50%)				_	
	1 GHz	2.5.5.9	C-24/23/50 C-24/23/50	_	4.3
(RC 50%)	1 GHz 1 MHz 1 GHz			  MΩ-cm	4.3 0.009
(RC 50%) Loss tangent (RC 50%)	1 GHz 1 MHz 1 GHz esistivity	2.5.5.9	C-24/23/50	_	4.3 0.009 0.013
(RC 50%) Loss tangent (RC 50%) Volume re	1 GHz 1 MHz 1 GHz esistivity	2.5.5.9 2.5.17.1 2.5.17.1	C-24/23/50 - C-96/35/90		4.3 0.009 0.013 >10 <sup>10</sup>
(RC 50%) Loss tangent (RC 50%) Volume re Surface re	1 GHz 1 MHz 1 GHz ssistivity ssistivity	2.5.5.9	C-24/23/50 C-96/35/90 C-96/35/90	MΩ-cm MΩ	4.3 0.009 0.013 >10 <sup>10</sup> >10 <sup>8</sup>

Specification Sheet : IPC-4101C / 127 · 128

# Photolithography

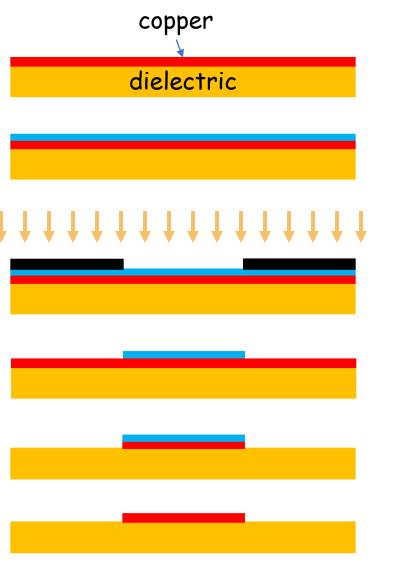
- Basic process steps
- Photo-imageable materials
- Resist deposition
- Masks
- Resist exposure
- Resist development

## Photolithography basics (substrative)









Base material with copper

Photoresist deposition

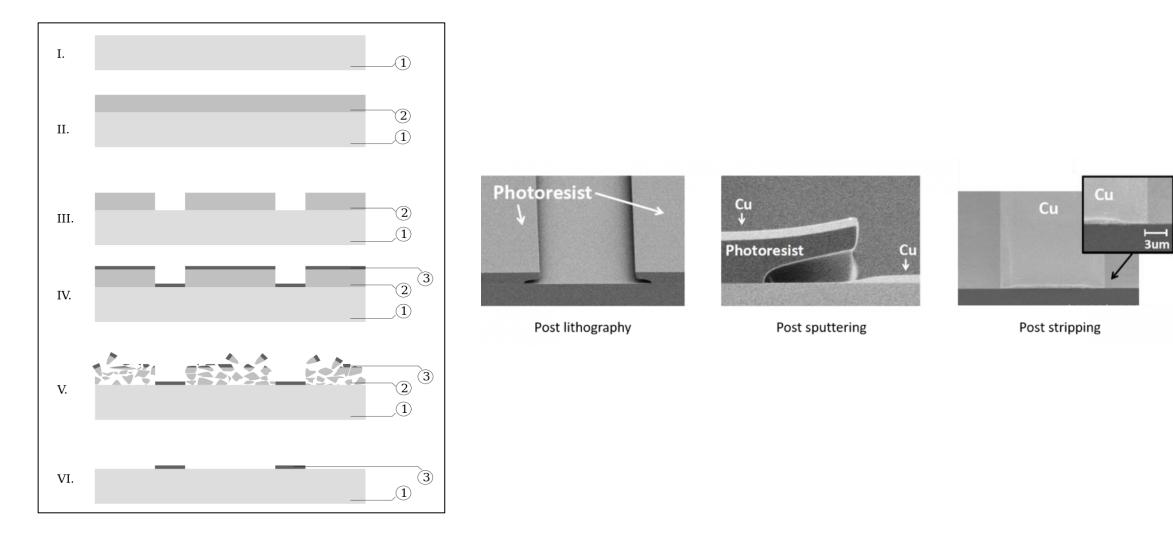
UV exposure through a mask

Resist development (negative resist)

Copper Etching

**Resist Stripping** 

## Lift Off patterning (additive)



## Photo-imageable materials

- Liquid resist (sacrificial)
  - Pattern lines
  - Thickness : 1um to 5um
  - L/S: sub micro-meter
- <u>Solid resist (sacrificial)</u>
  - Pattern lines
  - Thickness: 15um to 200um
  - L/S: 20-30um
- <u>Solder-mask (not sacrificial)</u>
  - Protect external lines on rigid boards
  - Typical thickness : 30um
  - L/S: 50um

- Photo-imageable Cover-layer (not sacrificial)
  - Protect lines on flexible boards
  - Thickness 30um
  - L/S: 50um
- Legend inks (not sacrificial)
- Liquid Polyimide (usually not sacrificial)
  - Make 3D shapes , protect Chips
  - Thickness : from 1 to 30um
  - L/S : 20um
- Liquid Epoxy (usually not sacrificial)
  - Make 3D shapes , used for MEMs , Gridpix
  - Thickness :1 to 100um
  - L/S: 20um

## **Resist deposition**

#### liquid

-<u>Spinning</u> - semiconductor production

-<u>Dip coating</u> - fine lines for PCBs or 3D objects

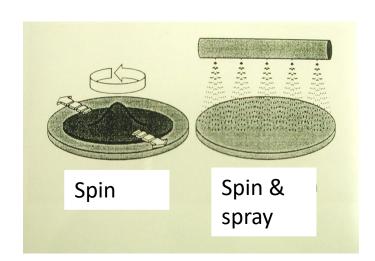
-<u>Curtain coating</u> - solder mask deposition

- -<u>Spray</u> solder mask- cover layer
- -<u>Screen printing</u> solder mask deposition

Solid

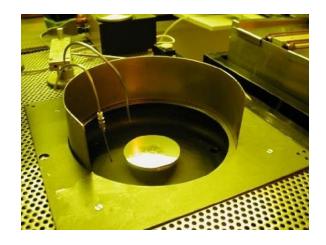
-Dry film lamination- PCB production

## Spin Coating

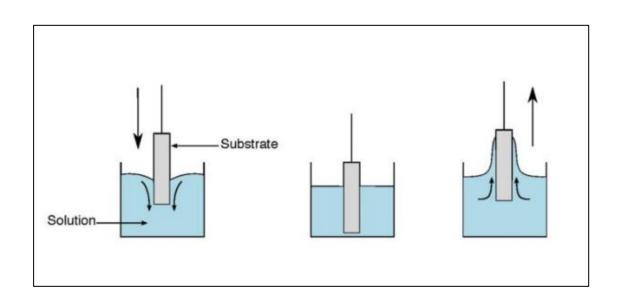




Deposition by centrifugation Uniform thickness Ultra-thin : down to 1 µm thickness Excellent repeatability Clean room mandatory

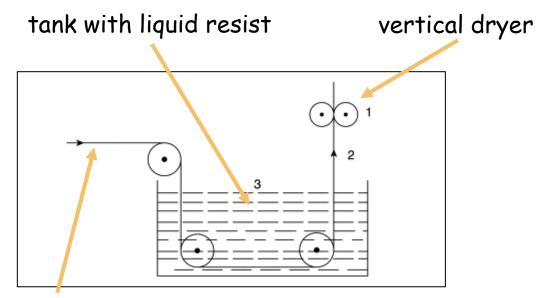


# Dip Coating



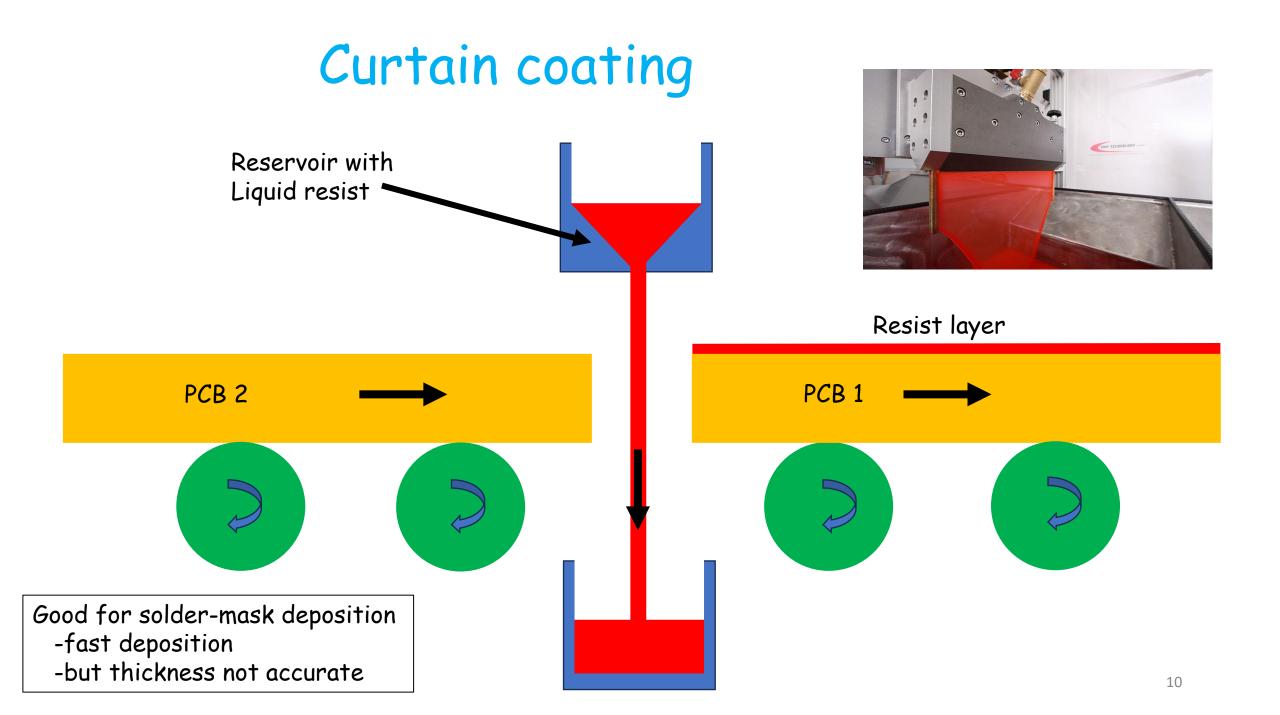
### Rigid board deep

- -Simple
- -Difficult to maintain repeatability
- -Thin depositions
- -Fine patterns

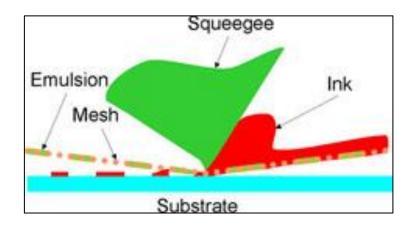


#### base material

Continuous flex mass production -Good repeatability -Fast deposition -Perfect uniformity



### Screen printing





Good for solder-mask deposition -fast deposition -thickness not really accurate -direct pattern printing



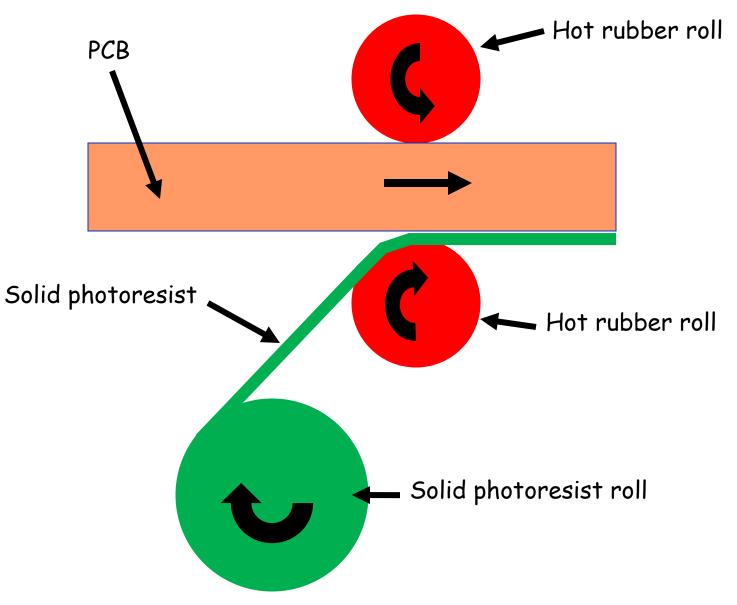
CERN precise machine 20cm x20cm deposition



CERN Semi automatic machine 1.5m x 2m General purpose

## Hot roll lamination

Dry process Medium class clean room Large size Precise thickness Easy to process Down to 20µm line/space Not compatible with 3D shapes



# Hot roll lamination deposition



Cu/Glass-Epoxy/Cu plate Or flex



Resist deposition by Lamination



Plate + Photoresist

## Masks

Precise mask : few mm of Glass or Quartz -Chromium on thick soda lime glass -Sub-micron resolution -up to 1m x 0.6m (15 000 CHF for a 1m x 0.6m mask)

Polyester mask : 100um thick -Minimum line and space around 20um -up to 2m × 1.5m (400 CHF for 2m × 1.5m mask)

Laser direct imaging : no mask -Minimum 20um line and space -up to 80cm x 60cm







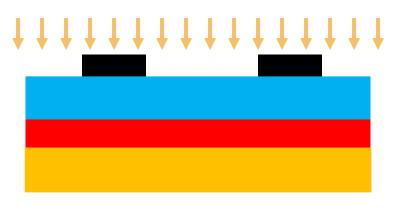
Figure 2 e 3 – The Paragon-8000 Laser Direct Imaging system (above) and 25 µm features exposed using this system (below)

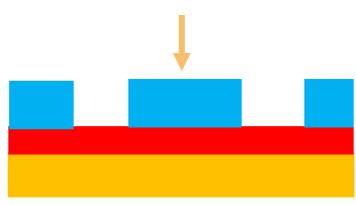
# Photolithography problems

- Light diffusion
- Light absorption
- Light diffraction

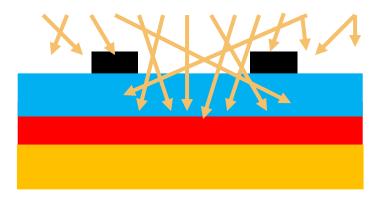
### light diffusion problem

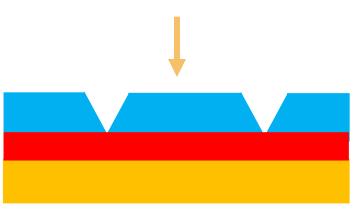
Collimated (polarized) UV light



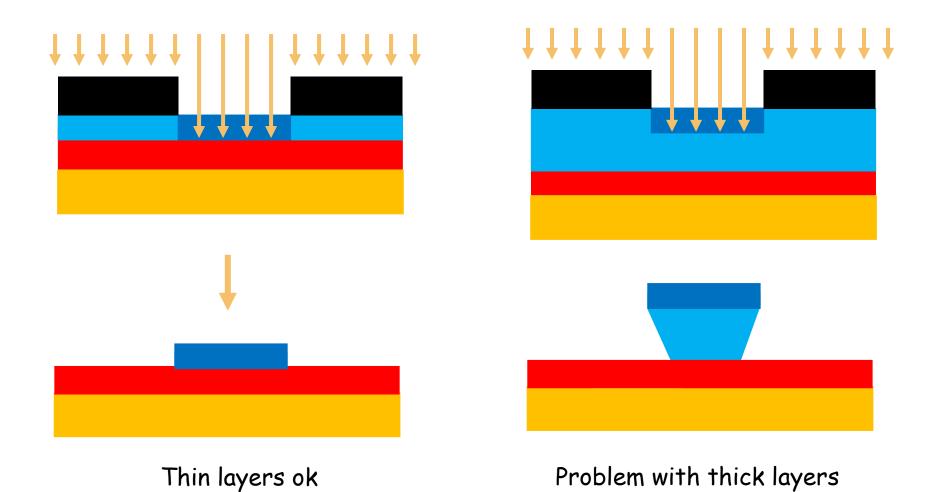


Diffuse UV light Ex: a neon tube

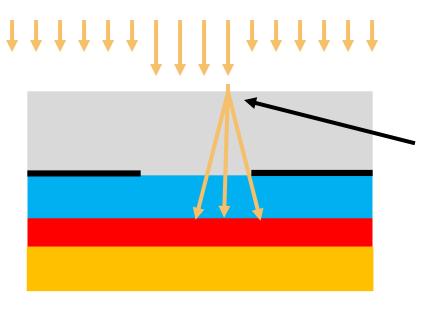




### light absorption problem

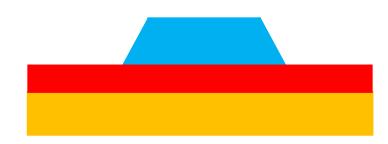


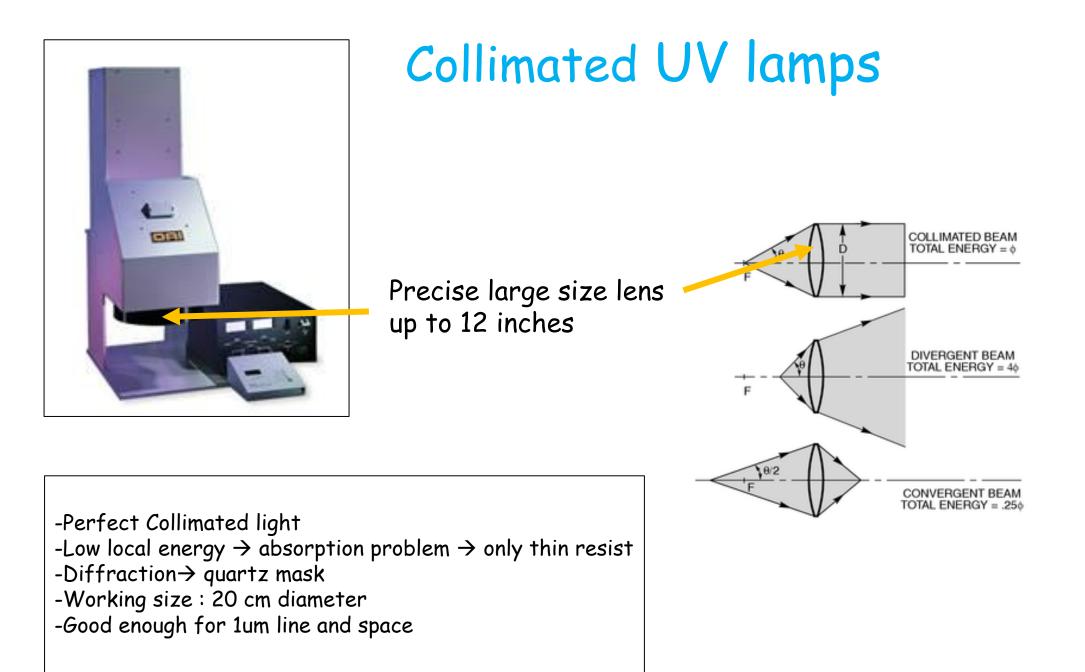
### light diffraction problem



Diffraction in glass or polyester Less with quartz

Problems appear with small pattern below the um





### Classical UV exposure machine for PCB production

-Semi Collimated light
-Low local energy → absorption pb
-Diffraction → thin polyester mask
-Working size up to 2.5m x 1.5m
-Good enough for 30um line and space

Two UV bulbs Vacuum Drawer (open) Sliding in the machine. Deflectors to Improve collimation UV bulbs

Stack up: -Polyester foil -Mask → manual alignment -PCB with resist to be patterned -Glass plate (Chassis)

## LDI: laser Direct Imaging

-Collimated light

-High local energy better for absorption but not perfect

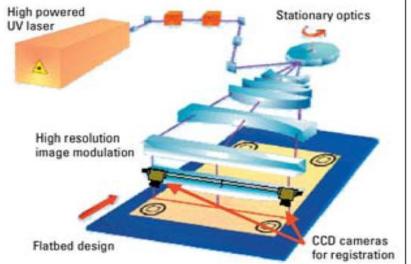
-No mask $\rightarrow$  no diffraction

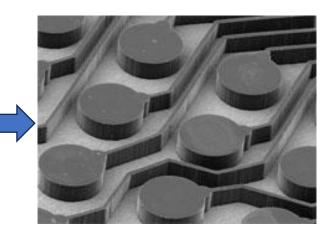
-Working size 80cm x 70cm

-Good enough for 20um line and space

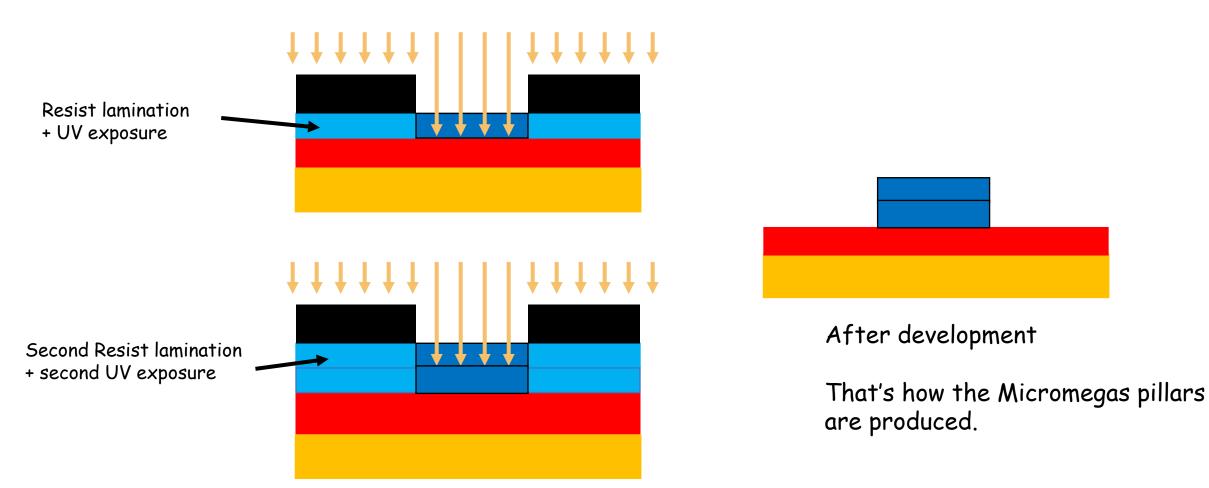
X Y steps: 0.5 µm to 2 µm (depending on machine type)







### Cheap way to avoid light absorption with thick patterns (above 100um)



### Wet development



### After Exposure:

UV exposure change slightly the resist color.

The UV light have initiated a crosslink in the polymer



Sodium carbonate NA2CO3 development With spray nozzles



### After Development:

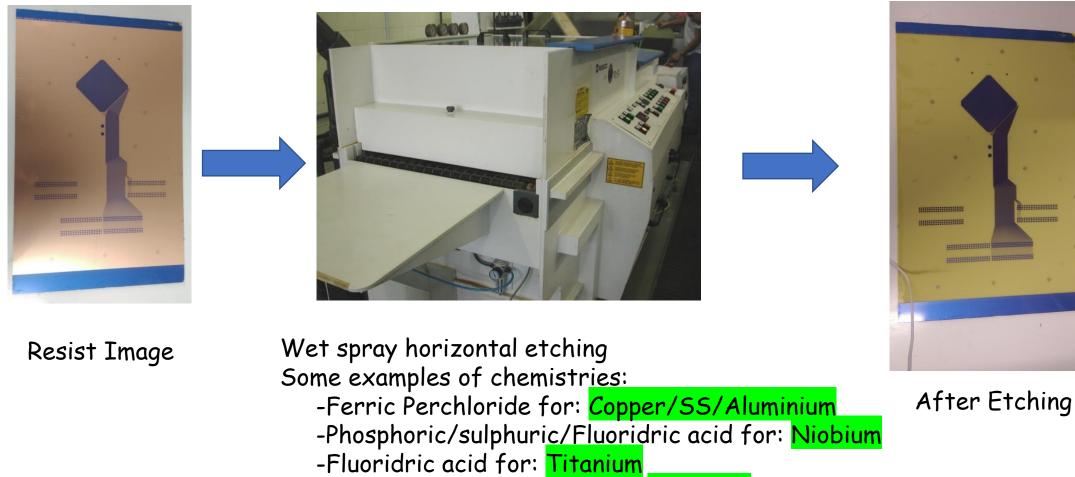
All the areas exposed to UV stays.

Everywhere else the resist is dissolved.

# Etching

- Wet etching
- Anisotropic /isotropic
- Laser ablation
- Electroforming to reach high aspect ratio patterns
- Resist stripping

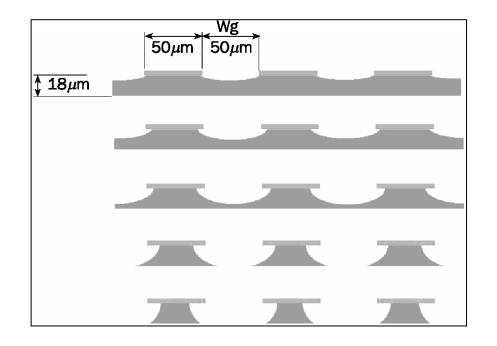
## Wet spray Etching



- -Potassium Ferrocyanide for: Tungsten
- -Iode/Iodine for: Gold
- -Iron nitrate for: Silver
- -etc.. etc..

## Wet etch , anisotropic or isotropic ?

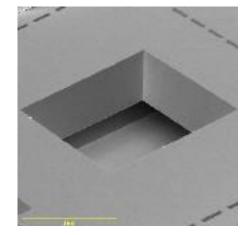
Wet etching is isotropic for nearly all materials



Minimum line spacing → 3 to 4 time the metal thickness. Big limitation for thick layers !

- Some exceptions
  - Silicon etching is anisotropic due to its crystalline structure
  - Polyimide etching can also be anisotropic

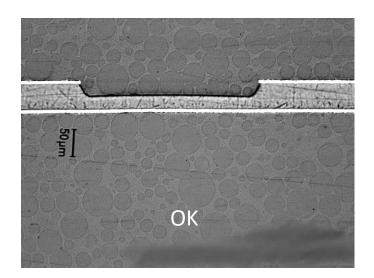


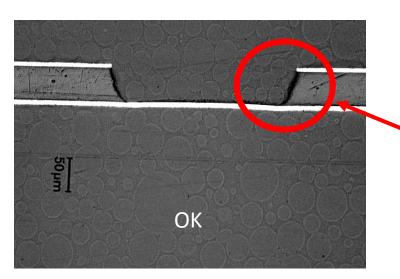


Patterned silicon

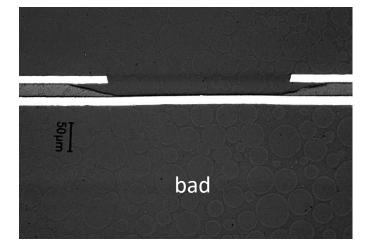
Anisotropic etching

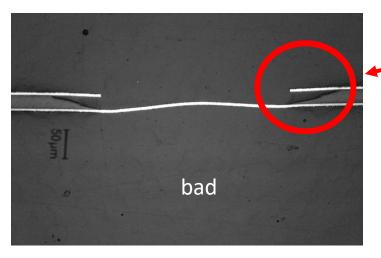
### Polyimide wet etching , isotropic and anisotropic





First type of polyimide: Perfect anisotropic etching No under etch Perfect to make small holes





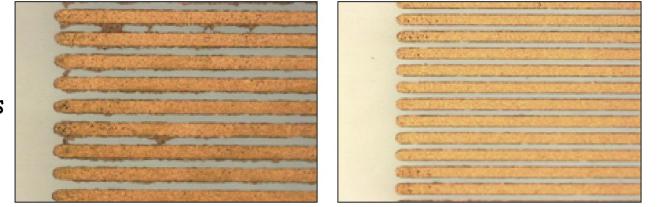
Second type of polyimide: Fully isotropic etching Not satisfactory

Les than 10% of PIs available on the market have an anisotropic behavior.



-Laser direct patterning is anisotropic , but not commonly used to make lines in PCBs.

-But lasers are nice for repairs .

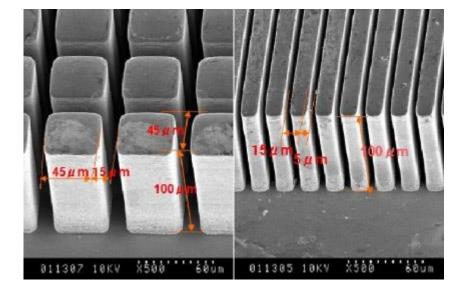


Full of shorts

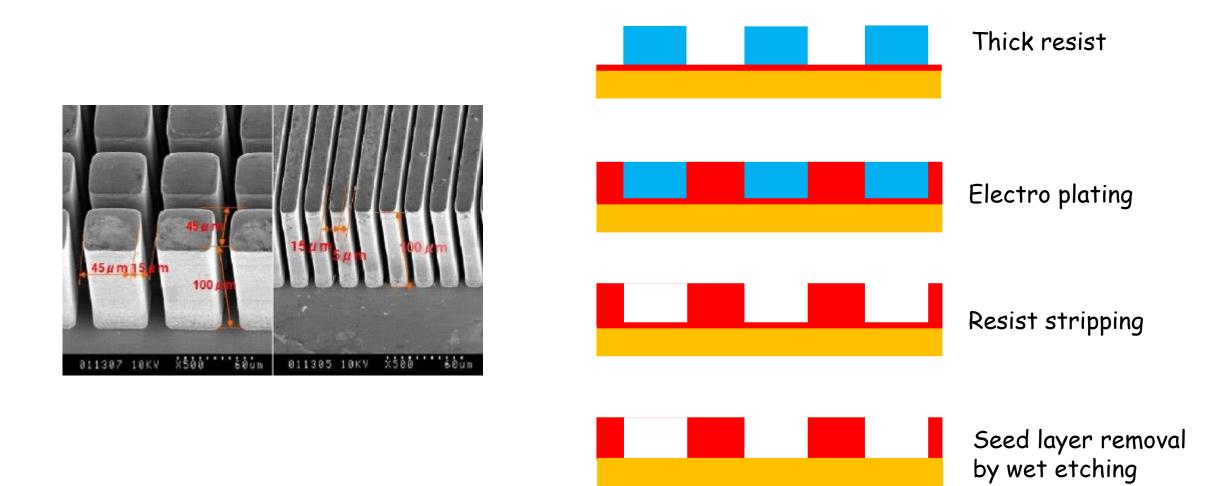
Seems simple but the process to get this result is highly complex , only lasers can achieve this result.

Cleaned up

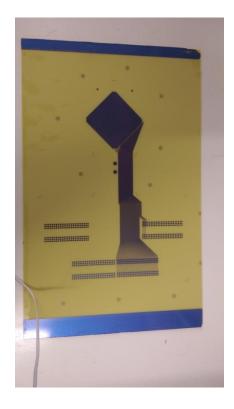
### Electroforming to beat wet etching isotropy



### Electroforming to beat wet etching isotropy



## After wet etching $\rightarrow$ wet resist Stripping



After copper Etching



Stripping with: NAOH KOH Solvents

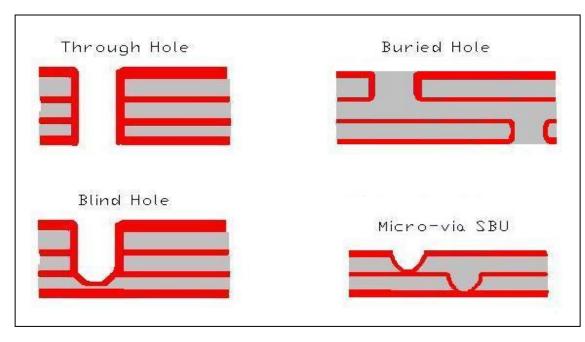


After stripping

# Multilayer structures

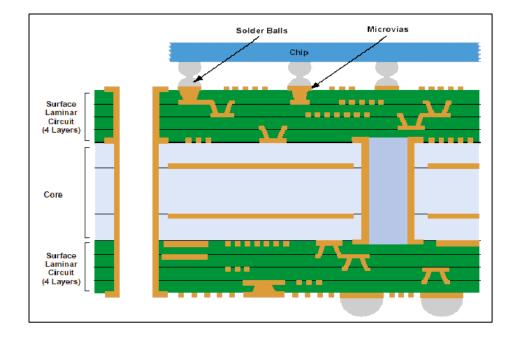
- Parallel layers production
  - Produce all the layers in parallel
  - Stack and glue them
  - Drill and plate the full stack
- Sequential build-up
  - Produce layer 1
  - Glue layer 2 on top
  - Drill and plate layer 2
  - Pattern layer 2
  - Etc..
- Mix structure
  - Core done with parallel production (power supplies)
  - Sequential build up for high density interconnect

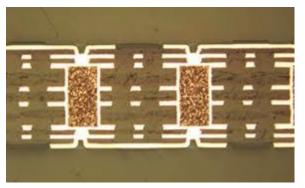
### Different interconnection strategies and corresponding holes



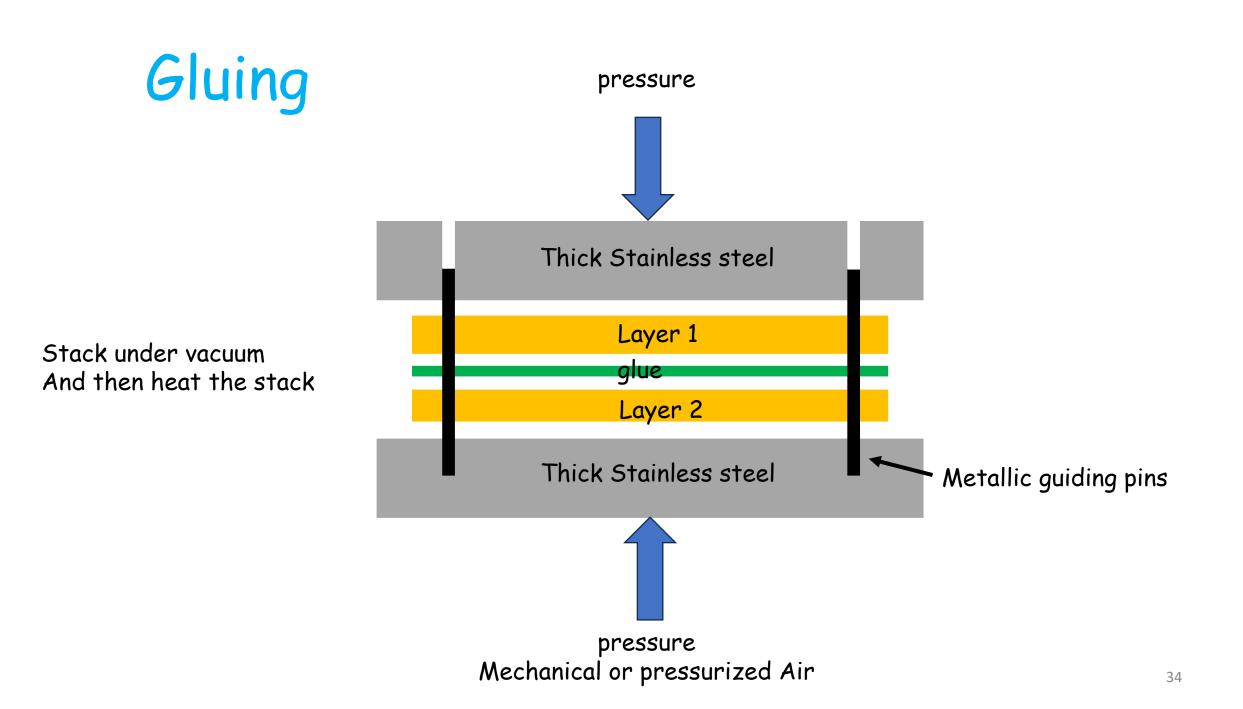


Staggered micro vias, buried and through holes





Buried holes and stacked micro vias



# Glue

### • Liquid

- Difficult to apply , impossible to clean , thicknesses not accurate
- Impossible to remove bubbles

### • Prepregs

- Woven glass impregnated with a bi-stage glue
- Few um precision thickness

### • Cast

- Thin solid bi-stage glue layer without glass fiber
- Epoxy, epoxy-acrylic
- Few um precision thickness

### • Bond-ply

- Polyimide foil with a cast layer on both sides
- Polyimide foil with a thin thermoplastic polyimide layer on both sides

Elite Mate	Technical Da	
Lead-fr	ee , Halogen-fr	ee Material
EI	M-370(5) / EM-3	37B(5)
Basic Available		
Type	Resin Content (%)	Unclad Laminate Thickness (mil)
4007	71.0±3.0	2.2
1037	75.0±3.0	2.5
100	71.0±3.0	2.2
106	75.0±3.0	2.5
1067	71.0±3.0	2.6
1007	75.0±3.0	3.1
1080	64.0±3.0	3.0
1060	67.0±3.0	3.3
1086	64.0±3.0	3.4
1080	67.0±3.0	3.9
2113	56.0±3.0	3.9
	53.0±3.0	4.8
2116	57.0±3.0	5.4
	49.0±3.0	6.7
1501	53.0±3.0	7.5
7000	45.5±3.0	7.9
7629	49.0±3.0	8.6

Lower resin content might be insufficient resin for lower copper residual or heavy copper of inner layer.

If you have any other requirement, please contact our sales or customer service representatives.

## Curing cycle

#### PrePreg

#### Cast

#### Processing AKAFLEX<sup>®</sup> KDF HT

The following pressing cycle is recommended for processing AKAFLEX KDF HT in heated-plate presses:

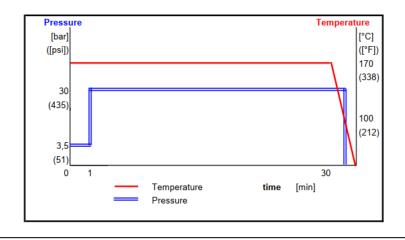
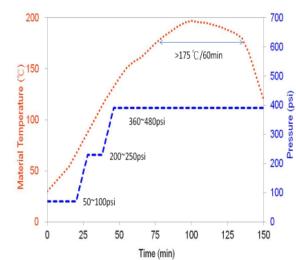


Plate temperature: 170 °C (338°F) Contact pressure: 3.5 bar (51psi)(1 min) Pressing pressure: 30 bar (435psi) Pressing time: 30 minutes Cooling: < 100 °C (212°F) under pressure Conformal layer: e.g. silicone

### Lead-free , Halogen-free Material EM-370(5) / EM-37B(5)

#### **Press Cycle**

Basic press cycle for normal construction of multilayer PWB:



	Kiss pressure: 50~100psi(3.5~7kgf/cm²)
	Middle pressure:200~250psi(14~18kgf/cm²) Apply at 70~90°C
	Heating rate:1.6~2.5°C/min(70~100°C)
essure (psi)	Full pressure:360~480psi(25~34kgf/cm²) Apply at 105~125°C Heating rate:1.6~2.5°C/min(100~130°C)
ĭ	Curing condition: >175°C / 60mins
	Peak temperature of material should be preferable achieved at 195°C

# Gluing equipment

Isostatic press

-Vacuum chamber
-5 heating platens , 4 daylights
-Mechanical Hydraulic pressure (170 Tons)
-Plate size 600mm x 700mm
-Presses with 3m x 1.5m platens exist
-Only used for flat circuits
-Perfect for mass production

Autoclave

- -The parts to be glued are in a vacuum bag -Pressure comes from compressed air (7 Bars) -STD size 2m x 1m
- -Machine with a length of 20m are existing

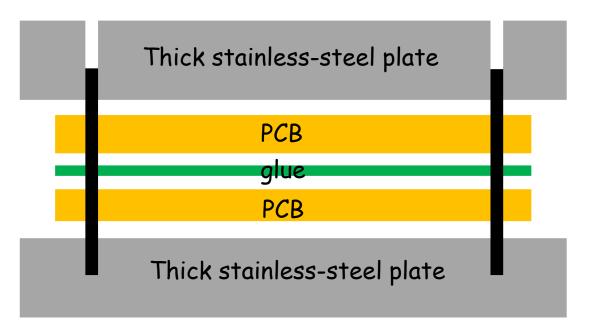
-Long cycles -3D objects

-Need more handling than Isostatic

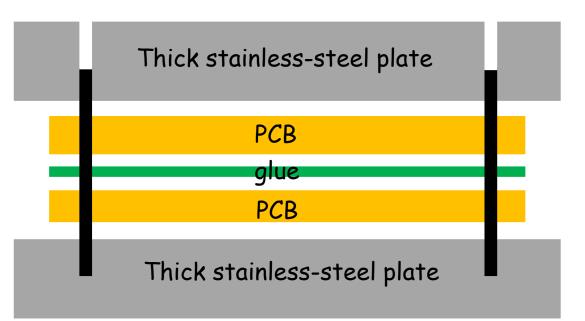




# Problem : nothing is flat

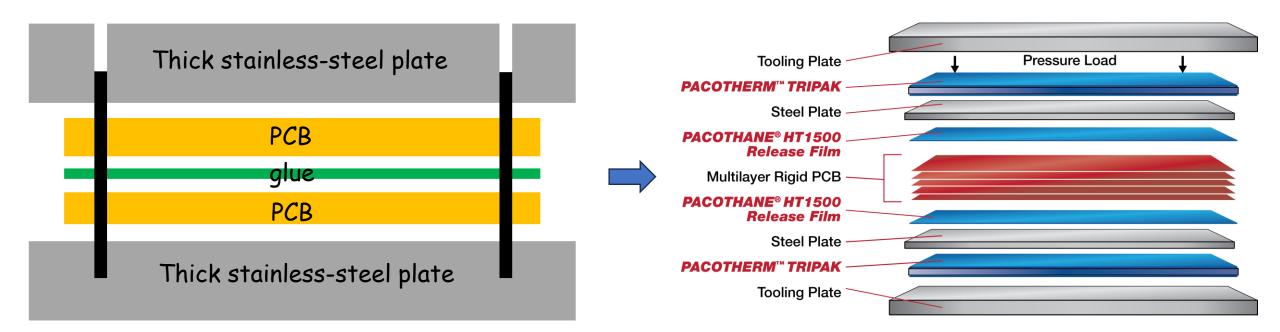


# Problem : nothing is flat



Machine platens  $\rightarrow$ +/-150um error SS plates  $\rightarrow$ +/- 50um PCB  $\rightarrow$ +/- 10% of the thickness Prepreg , Glues  $\rightarrow$ +/-10um

# Problem : nothing is flat

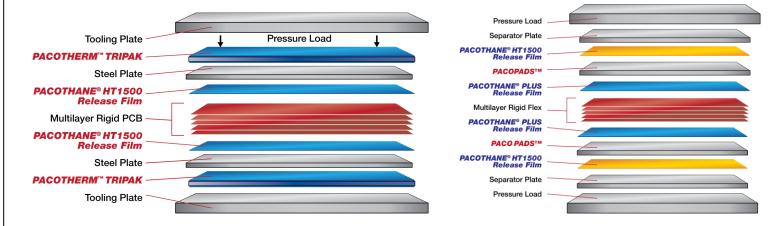


Machine platens →+/-150um error SS plates →+/- 50um PCB →+/- 10% of the thickness Prepreg , Glues →+/-20um

Introduction of conformal layers to correct all the errors

## Release and conformal layers

- Release sheets
  - Pacotane
  - Pacolon  $\rightarrow$  high temp
- Planarity correction
  - Pacopads  $\rightarrow$  200 um correction
  - Thermal activation
  - Pacoterms → 1mm correction
  - Mechanical activation
- Hermetic layer
  - Pacovia → Stop the glue flowing from buried holes
  - Thermal activation



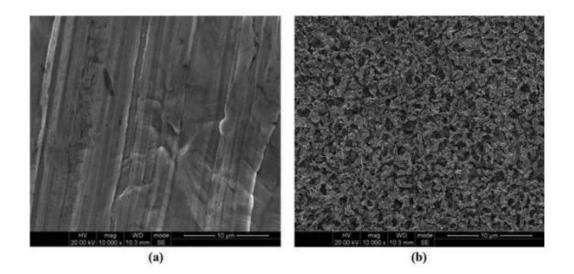
Typical 4-layer Rigid PCB stack

Typical 4-layer Flex stack

### And not forget to prepare metals before gluing



1/Detergent Cleaner2/Pre-conditioning3/Micro structuring Chemistry4/DI water Rinse5/Dry



#### Copper before and after treatment

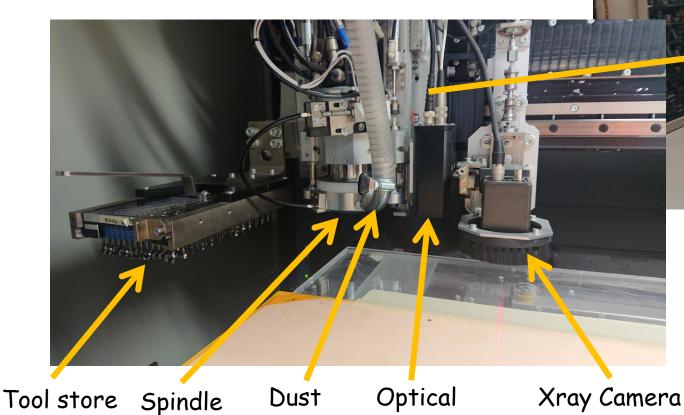
# Drilling / Milling

- Mechanical
- Chemical
- Laser
- Plasma
- Sand blast

# Mechanical drilling

#### At CERN

Minimum tool diameter : 0.15mm Spindle 180 000 RPM Max rate 3Holes/s 1.4m x 0.6m drilling area



exhaust

camera

**CERN** Machine

#### In industry

R-A15-01

Up to 4m x 2m drilling area Max rate up to 10 holes/s Min tool diameter 0.05mm Spindle 300 000 RPM

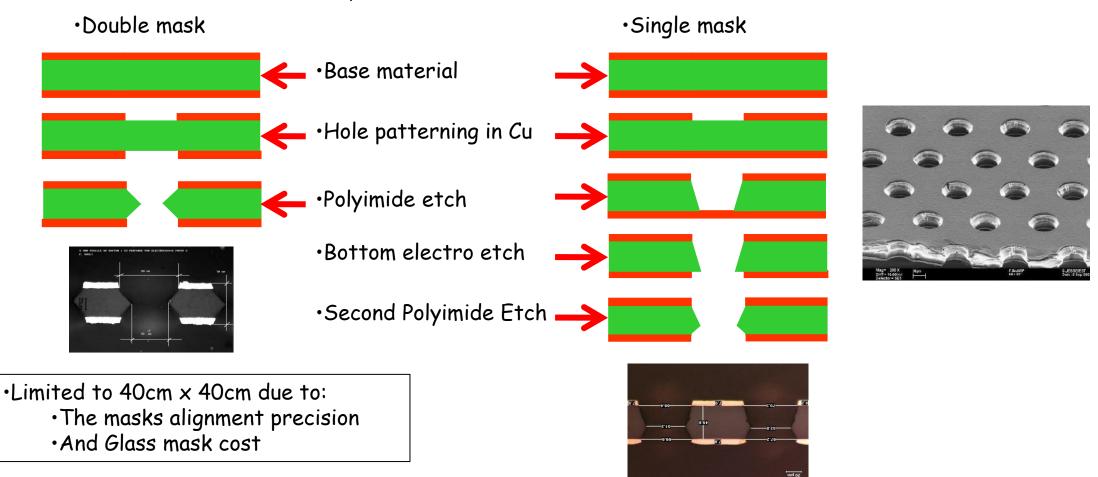
### Mechanical Milling

Spindle 80 000 RPM 0.5mm min milling tool



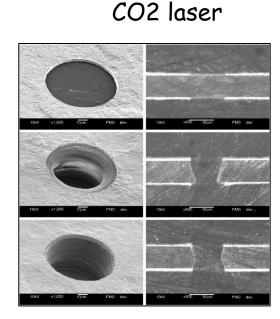
### Chemical drilling $\rightarrow$ GEM

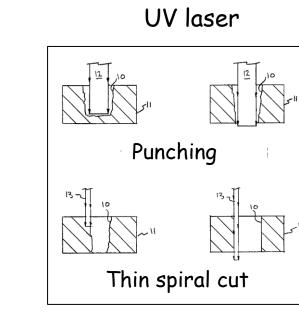
•Base material : Polyimide 50um + 5um on both sides

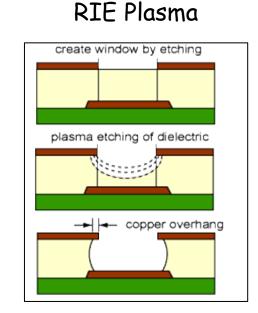


Limited to 2m x 60cm due to:
Base material
Equipment

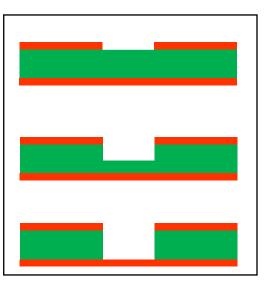
### Laser or plasma drilling







#### DRIE Plasma



-Many possible base Materials. -Holes perfectly clean. -Small patterns -30um holes

-Not competitive with Wet drilling

-Many possible base Materials. -Can drill both metals and polymers -20um holes

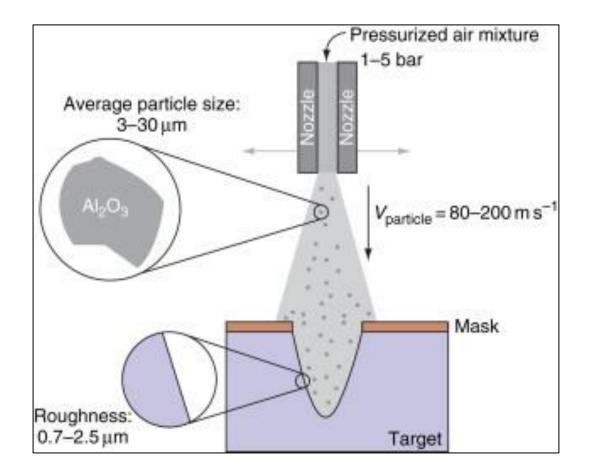
-Not selective enough -Not yet competitive with Wet drilling -Moderate machine cost . -Holes perfectly clean -50um holes

-Not uniform on large size. -Etching too Isotropic -Perfect cylindrical holes. -Holes perfectly clean -Ultra precise patterns -20um holes

-Complex process -limitation on size : dia 20cm max.

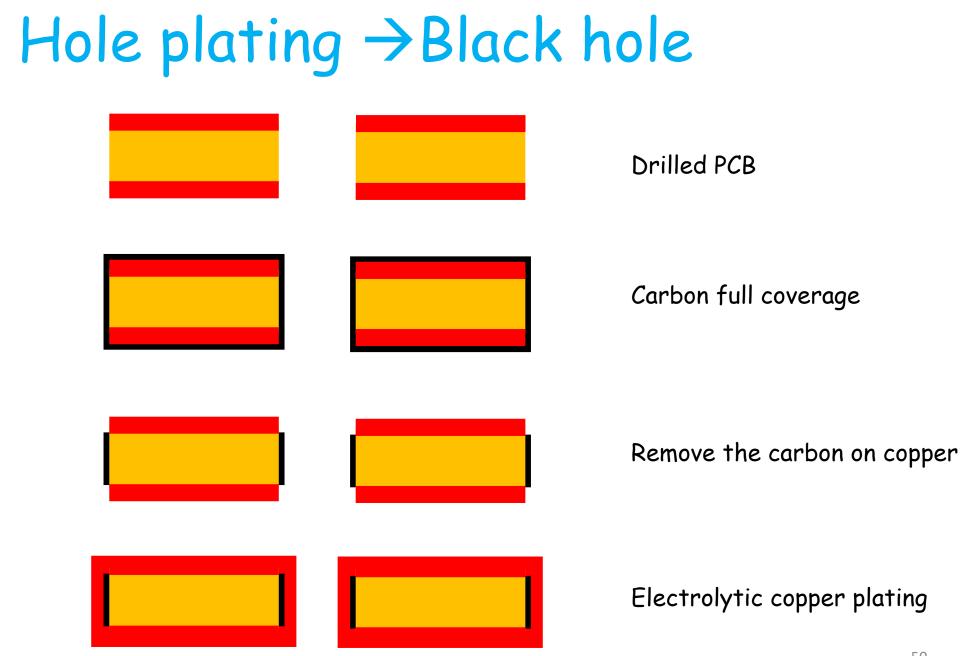
### Sand blast drilling

-Anisotropic drilling -Can drill nearly all materials -Smooth walls -Millimetric scale holes



# Wet plating

- Black hole
- Electroplating
- Electroless plating
- Vacuum plating



### Dielectric treatment before copper plating



### 1/Deasmear line:

- Remove material burnt during drilling
- Come back to fresh dielectric
- Sequence:
  - 1/ Sweller
  - 2/ Potassium permanganate etching
  - 3/ Neutralizer

### 2/Carbon line:

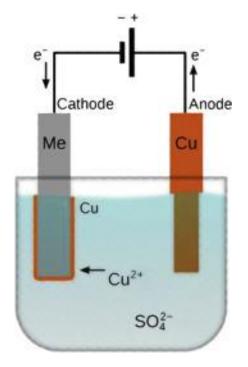
- Deposition of Nano Carbon on dielectric
- Sequence
  - 1/ Detergent cleaning
  - 2/ Nano carbon full coverage
  - 3/ Micro etch to remove carbon on copper

# Electro plating

• CU

• NI

• Au



Anode Basket with copper balls Copper plating Bath 2m x 0.6m panels

Cathode

Filter/pump

# Electro-less plating

- Protect copper from oxidation
- Ease the components soldering
- Allow Aluminum or Gold wire Bonding

Immersion Au

Electroless Ni

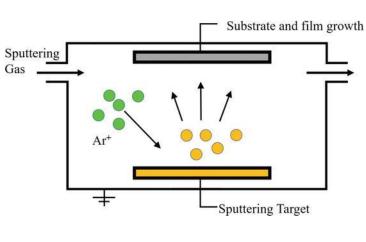
ENIG : Electroless nickel (5um) , Immersion Gold (0.07um) ENEPIG: Electroless nickel , electroless Palladium , Immersion Gold Ag: Chemical silver less than 1um Tin: Chemical Tin



# Vacuum deposition



Pulsed DC Magnetron vacuum deposition machine





Cr

Cu

Al

Ti

Nb

B4C

DLC

ITO

70cm targets

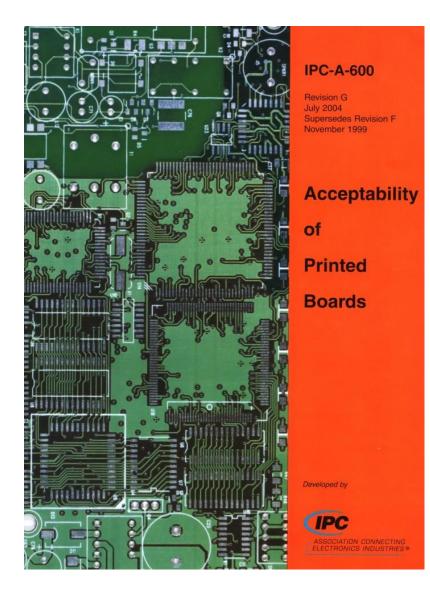




Drum unloading after processing

## Tests

- QC
- AOI
- Flying probe tester
- XRF





IPC standard define the parameters to check and set 3 levels of quality.

Class 1: Consumer products -It can fail without consequences -exchange sometimes not necessary → radio , toy , microphone etc..

Class 2: Industrial products -uninterrupted service is desired -exchange is not critical. → CNC machines, Heat pumps etc..

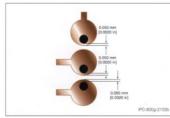
Class 3: High reliability -No possibility to exchange the product without serious problems. → Airplanes, trains , cars → President microphone.

#### 2.10 PATTERN DEFINITION - DIMENSIONAL

#### 2.10.3 External Annular Ring - Supported Holes

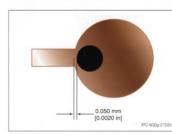


Target Condition - Class 1, 2, 3 · Holes are centered in the lands.



#### Acceptable - Class 3

. Holes are not centered in the lands, but the annular ring measures 0.050 mm [0.0020 in] or more. The minimum external annular ring may have 20% reduction of the minimum annular ring at the measurement area due to defects such as pits, dents, nicks, pinholes, or splay.



#### Acceptable - Class 2 · 90° breakout or less. (A)

. If breakout occurs at the conductor to land junction area, the conductor is not reduced by more than 20% of the minimum conductor width specified on the engineering drawing or the production master nominal. The conductor junction should never be less than 0.050 mm [0.0020 in] or the minimum line width, whichever is smaller. (C)

· Minimum lateral spacing between conductors is maintained.

#### Acceptable - Class 1 • 180° breakout or less. (B)

. If breakout occurs at the conductor to land junction area, the conductor is not reduced by more than 30% of the minimum conductor width specified on the production master nominal. (D) . Form, fit and function are not affected. Minimum lateral spacing between conductors is maintained.

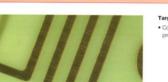
#### Nonconforming - Class 1, 2, 3

PC-600o-21

· Defects either do not meet or exceed above criteria.

	RESIST (Solder Mask) on to Holes (All Finishes)	
Lion Regionatio		
	No solder resist misregistration. The solder resist is centered around the lands within the nominal registration spacings.	
20	Acceptable - Class 1, 2, 3	
	Misregistration of the resist to the land pattern but the resist does not violate minimum annula ring requirements.	s r
	<ul> <li>No solder resist in plated-through holes, excepthose not intended for soldering.</li> </ul>	it.
	Adjacent, electrically isolated lands or conductors are not exposed.	100 A
	Nonconforming - Class 1, 2, 3	8-5815
	Deficits either do not meet or exceed abow criteria.	

50



2.10 PATTERN DEFINITION - DIMENSIONAL

2.10.1.2 Conductor Spacing

Target Condition - Class 1, 2, 3 · Conductor spacing meets dimensional requirements of the procurement documentation.

• Any combination of edge roughness, copper spikes, etc., that does not reduce the specified minimum conductor spacing by more than 20% in isolated areas.

### Acceptable - Class 1, 2 · Any combination of edge roughness, copper spikes, etc., that does not reduce the specified minimum conductor spacing by more than 30% in isolated areas.

July 2004

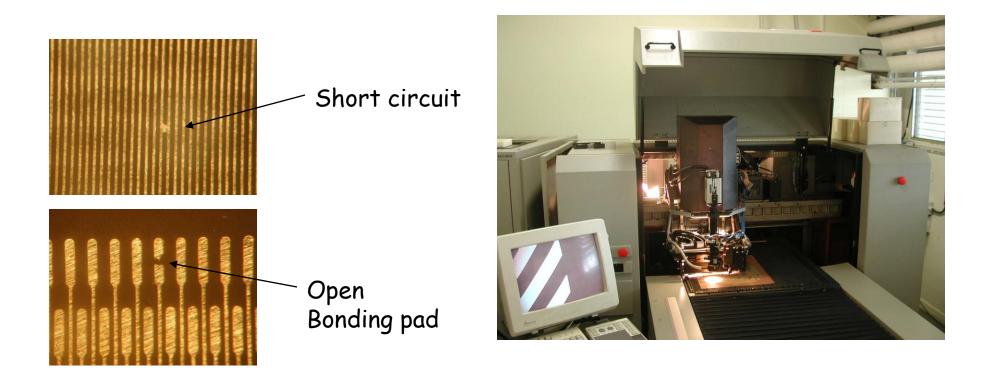
Nonconforming - Class 1, 2, 3 · Defects either do not meet or exceed above criteria.

Acceptable - Class 3

3 examples of parameters to be checked. There are more than 150 parameters to check in IPC-A-600. IPC-A-600G

### Automatic Optical Inspection (AOI)

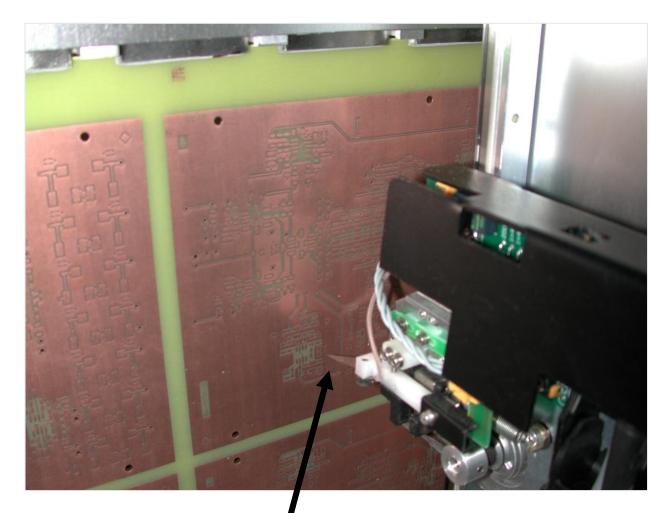
- -Compare the scanned image with the CAD file
- -Minimum track or space 20um
- -Minimum detectable defect around 5 um (pixel 2.5um)
- -Cannot detect defect in holes



# Electrical test



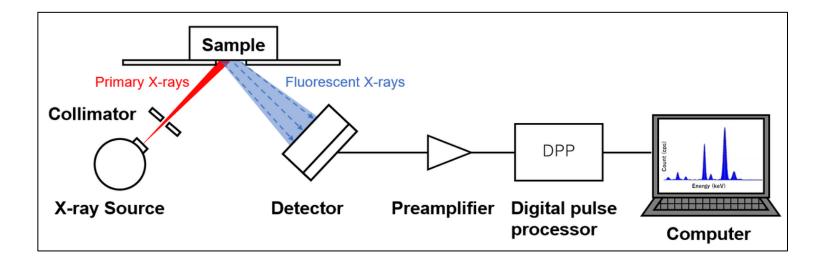
Control netlist integrity Including PTH connections Conductive / dielectric mode Capacitive mode



Flying probe

# XRF, X Ray Fluorescence

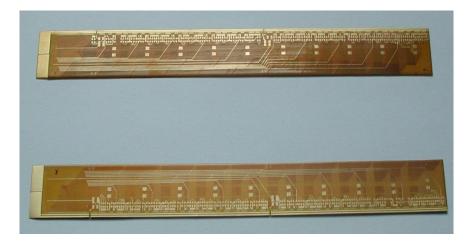




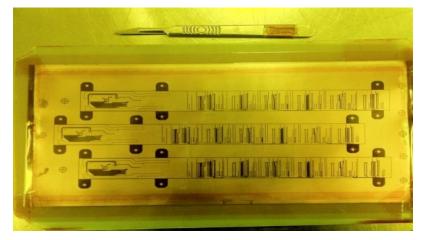
Can measure different metals nature and thicknesses Used for ENIG and ENEPIG control

## Production examples

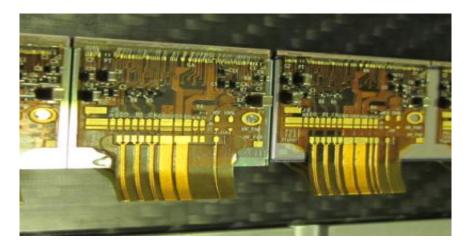
### Inner Trackers $\rightarrow$ Low Mass Aluminium circuits



ALICE inner tracker BUS (5 Aluminum layers)



Double-sided flex for ILC Vertex sensor



ATLAS IBL Low Mass 8 layers



ALICE inner tracker

### Magnets calibration $\rightarrow$ ultra precise field sensors



- -1.5m long
- -up to 40 layers
- -30um maximum registration error
- -Line and space down to 50um/50um

-Printed Coils are 10 time more accurate than wounded structures

1 .

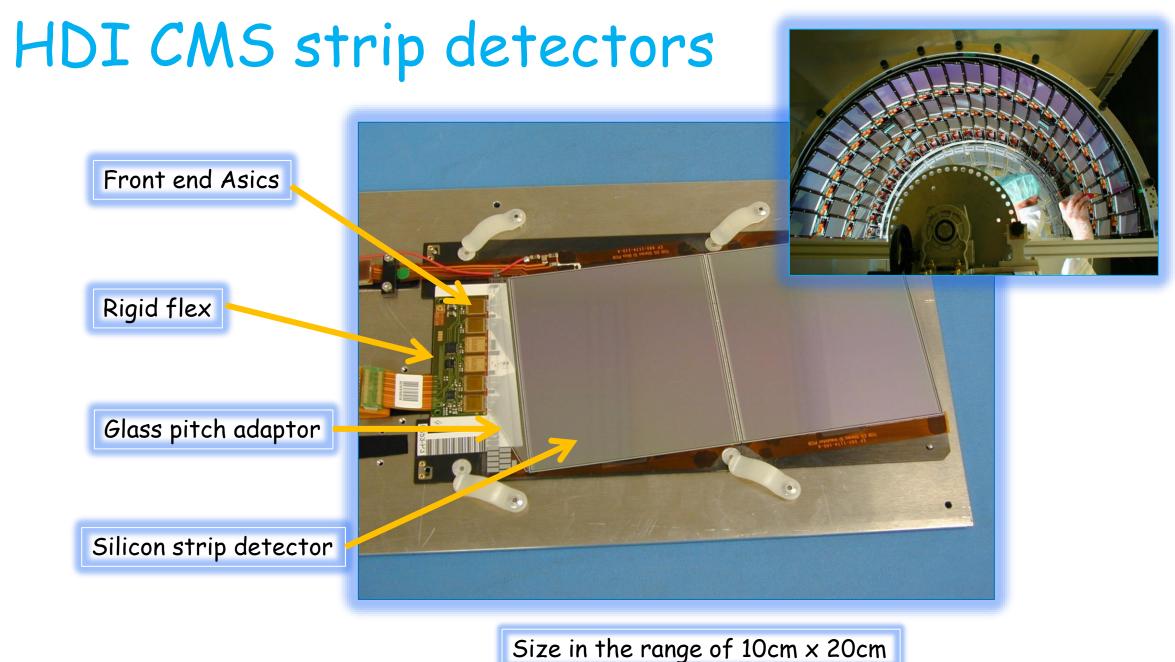
21225

### LHC machine protection





Stainless steel or SS/Cu mix Quench Heaters Long flexes up to 15m x 0.6m Others: -Heating foils -High power resistors -Optical targets compatible with UHV -etc..

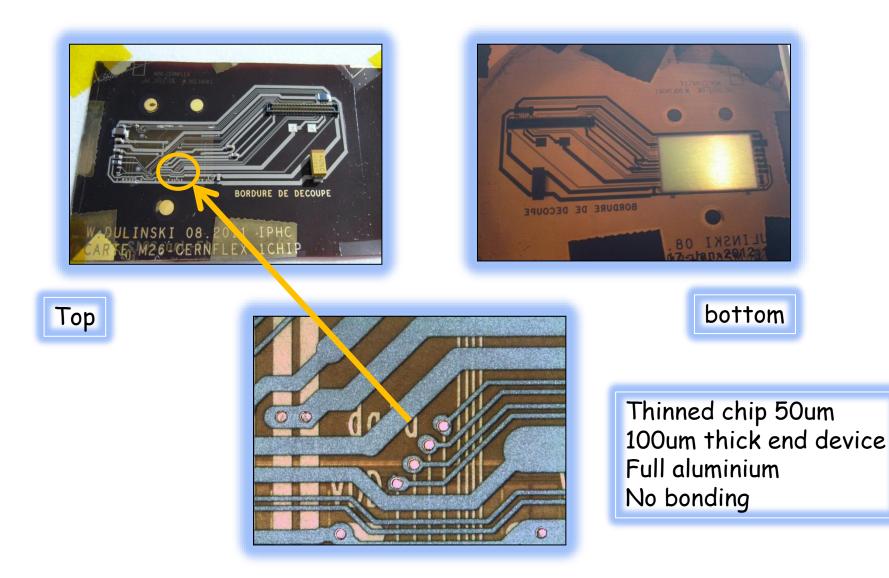


## PCB for CMS Pre-shower

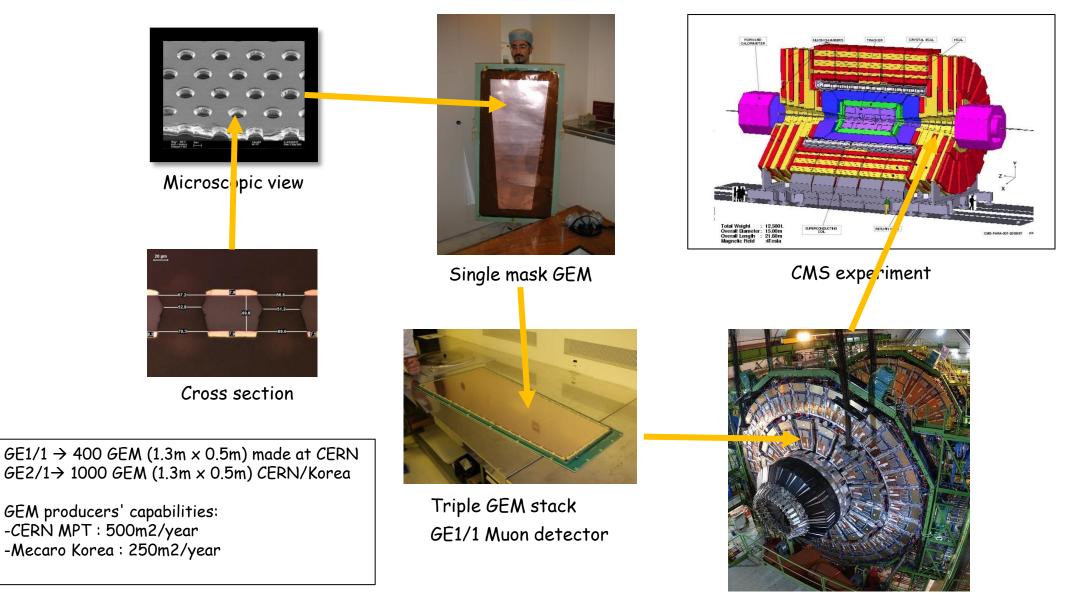




# Full Aluminium flex with embedded silicon detector (R&D)



### CMS GEM Single Mask



CMS nose

### Other detectors with GEM single mask technique



 ${\rm CMS}\;{\rm ME0} \rightarrow 1000\;{\rm GEMs}$ 

KLOE - Cylindrical Detector

ALICE TPC  $\rightarrow$  700 GEM

### And many more

-BM@N in Dubna (1.6m x 0.5m) -SBS tracker Jefferson lab -CBM at Fair -BESIII China

-SOLID -BONUS 12 -P-RAD -S-Phenix TPC -COMPASS upgrade

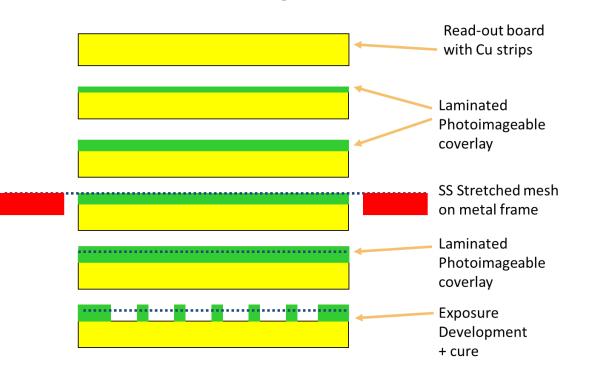
-GEM for nuclear physics TPCs

-ESS for neutron detectors

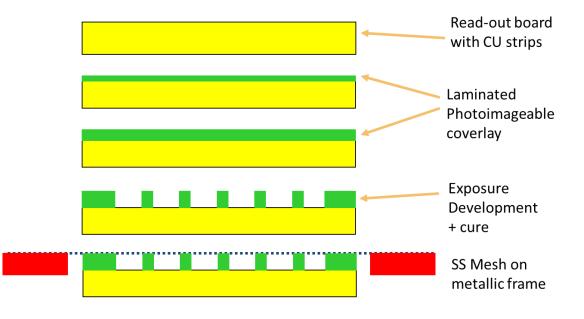
-and lot of small GEMs for academic purpose

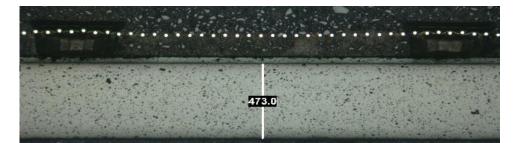
### BULK and Floating mesh Micromegas detectors

#### **BULK Micromegas**



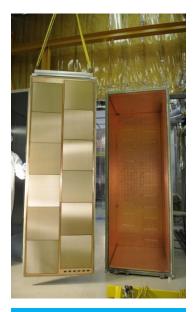
#### Floating mesh Micromegas



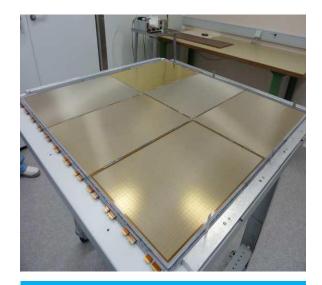


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### **BULK Micromegas detectors**



T2K TPC ,J.Beucher 1.8m × 0.8m plane With 12 detectors



ILC DHCAL , M.Chefdeville 1m x 1m plane With 6 detectors

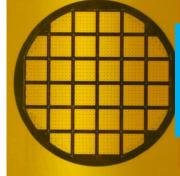






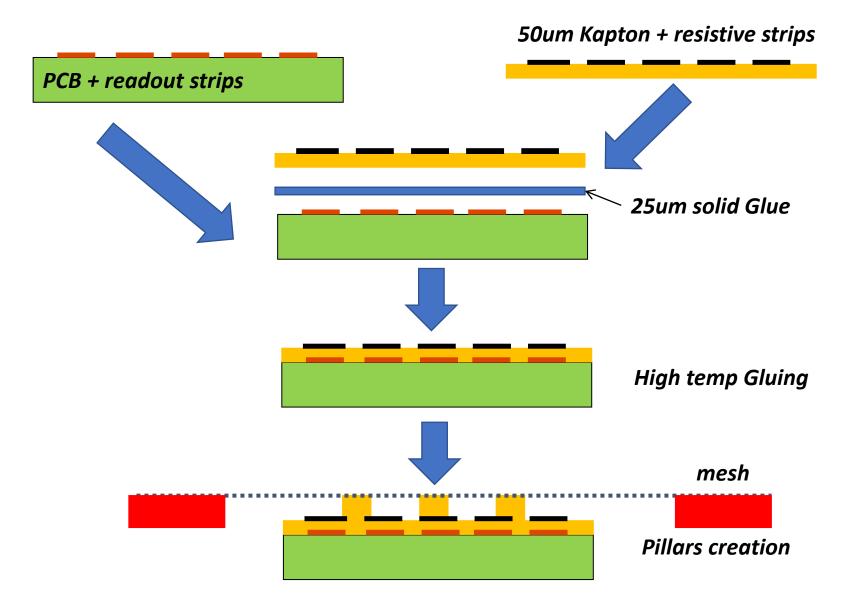
Early ATLAS NSW R&D Joerg Wotschack 1.5m x 0.5m plane Single panel

CLAS 12 Cylindrical Micromegas bulk Thin substrate



33 sectors , 12cm diameterdetector2.5mm dead space for sectorizing1mm hole for HV connection

### Introduction of resistive layers in floating MM



### Atlas NSW

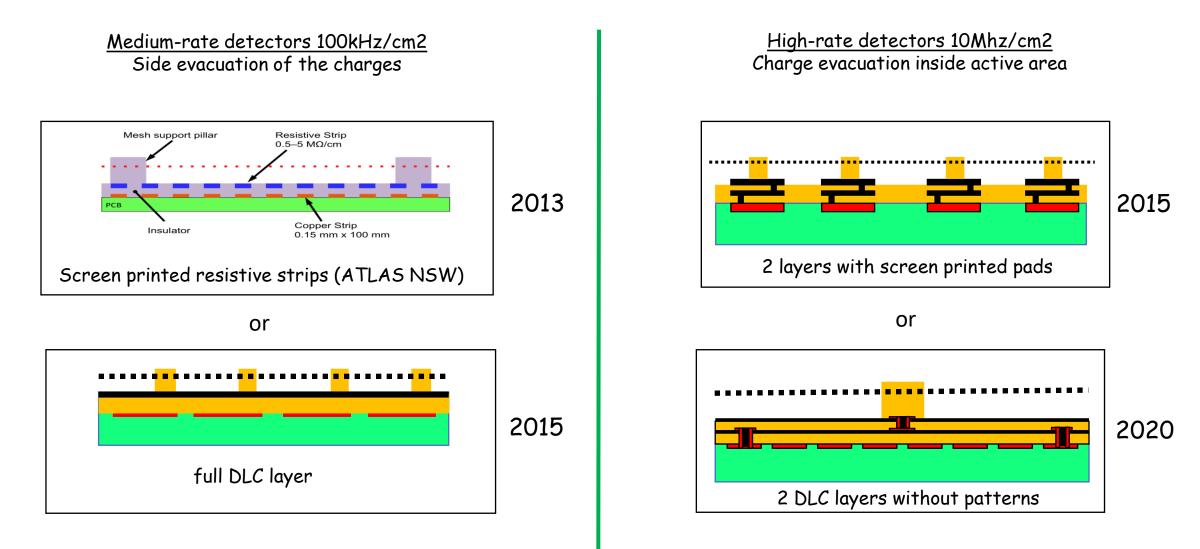


Close to 2000 Micromegas detectors produced with modules sizes up to  $2m \times 0.5m$ 

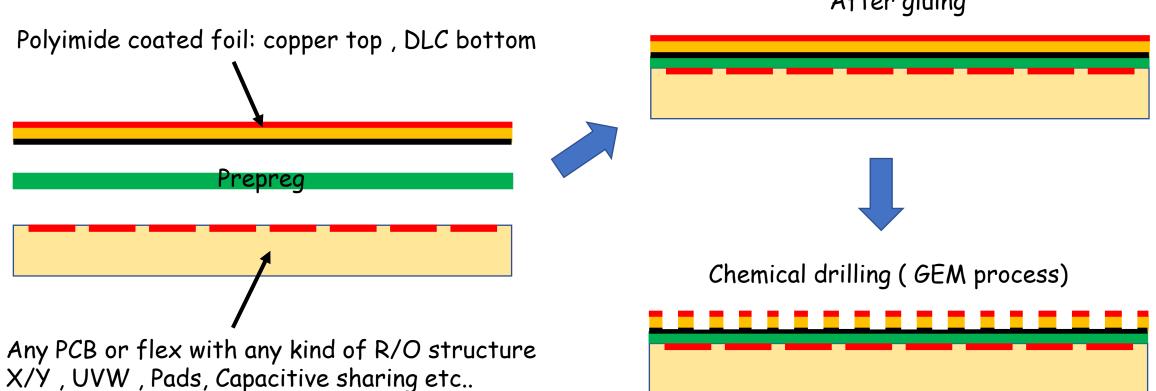
PCBs with pillars built at ELTOS (IT) and ELVIA (FR) Panels construction and detector Assy :

-Dubna -INFN Frascati -CEA Saclay -LMU Munich MPT participated to the R&D and was also involved in the mass production with industry -Specification -Companies selection -Technology transfer

### **Resistive MM structures**



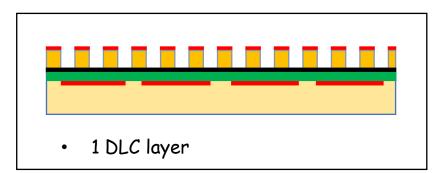
### Micro Resistive well detectors

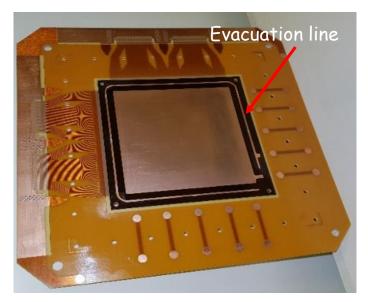


#### After gluing

#### <u>Medium rate µRwell</u>

Lateral evacuation of charges





 $10 \text{cm} \times 10 \text{cm} \mu \text{Rwell detector}$ 

#### <u>High rate uRwell</u>

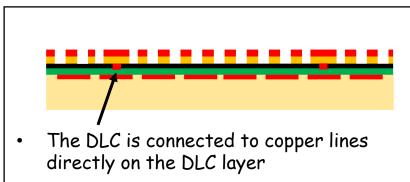
Charge evacuation in the active area

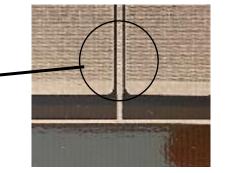
#### $PEP \rightarrow Pattern Etch Plate$

## 

• The DLC is connected by plated grooves or plated holes from the top layer

#### $SG \rightarrow Silver Grid$

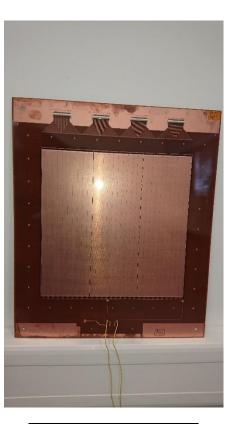




### uRwell examples



Frascati R&D 1D PEP uRwell Active area: 40cm x 5cm



Frascati R&D 1D PEP uRwell Active area: 30cm x 30cm



CLAS12 R&D 2D PEP uRwell Active area: 150cm x 50cm



CLAS12 uRwell rolled in an oven for E-cleaning

# Thank you for your attention