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OVERVIEW: We present accurate and extensive studies on 3D-column silicon sensors, aimed at high-resolution space-time (4D) tracking for future collider experiments. Such studies are a follow-up of the TimeSPOT-project and AIDAInnova initiative, where we developed timing optimized 3D-trench silicon sensors [1], which have been proved to reach a time resolution around 10 ps rms up to extreme fluences (above 10^{17} new/cm²) [2] [3]. In this study, three configurations, 1E, 2E, and 3E, based on a parallel electrode configuration are designed, simulated, and compared with the established parallel trench geometry. Additionally, the impact of varying pitch sizes (45 μ m, 50 μ m, and 55 μ m) on performance are investigated. These sensors will use columnar electrodes with a smaller pitch and pixel matrices, along with a refined production process for improved hybridization yield. They will be compatible with future readout chips, such as the IGNITE32/64 and INFN IgniteER, respectively [4].

Sensor design: Columnar electrode geometries with single, double and triple readout electrodes are proposed.

Devices were simulated based on their properties such as weighting field, bulk capacitance and intrinsic current induction (Ramo Map) \rightarrow

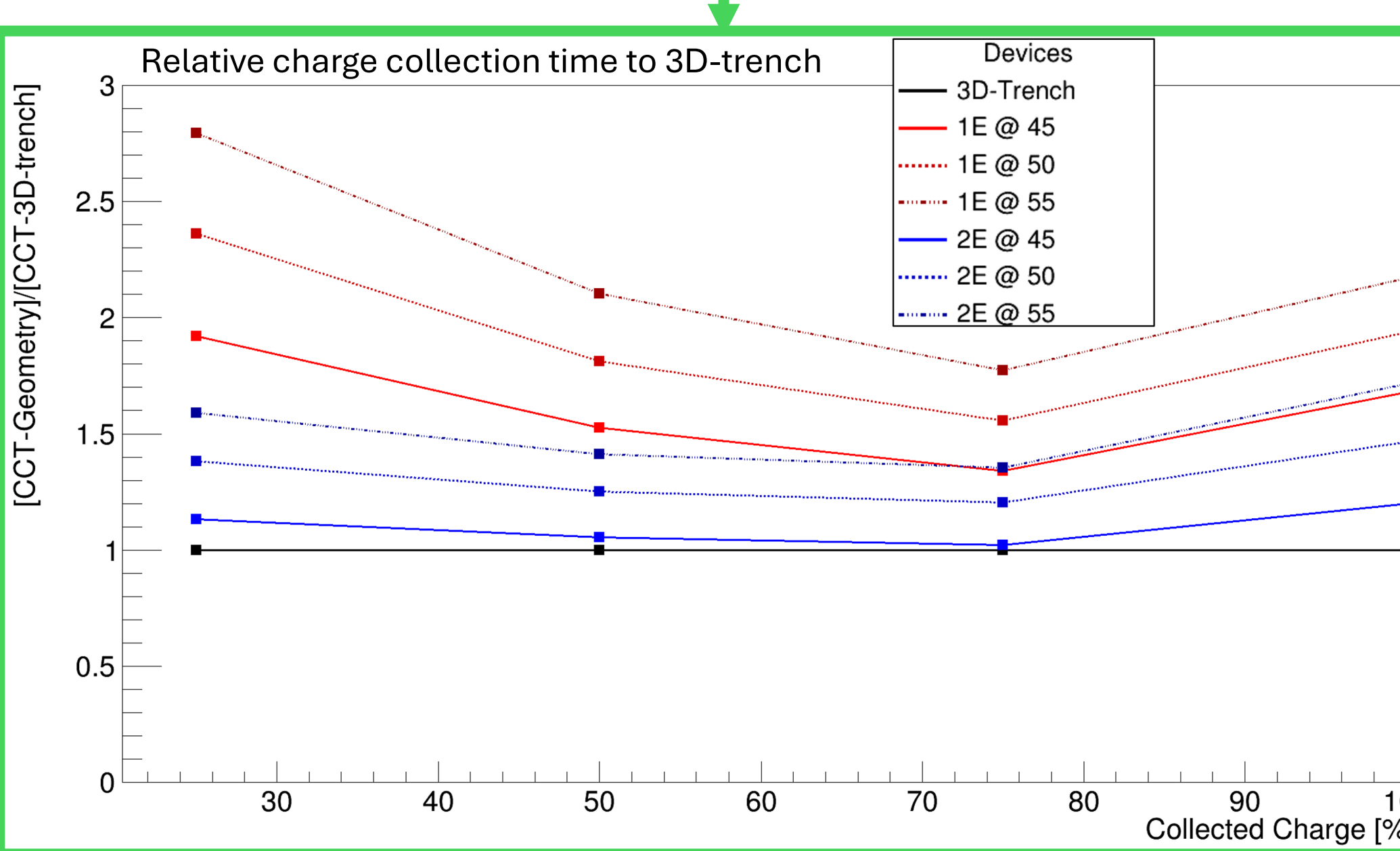
3D-trench is the reference device, due to its already demonstrated performance of time resolution below 20 ps, even after being irradiated above 10^{16} 1 MeV neq/cm² [1][2].

3E excluded due to large capacitance and complicate fabrication process.

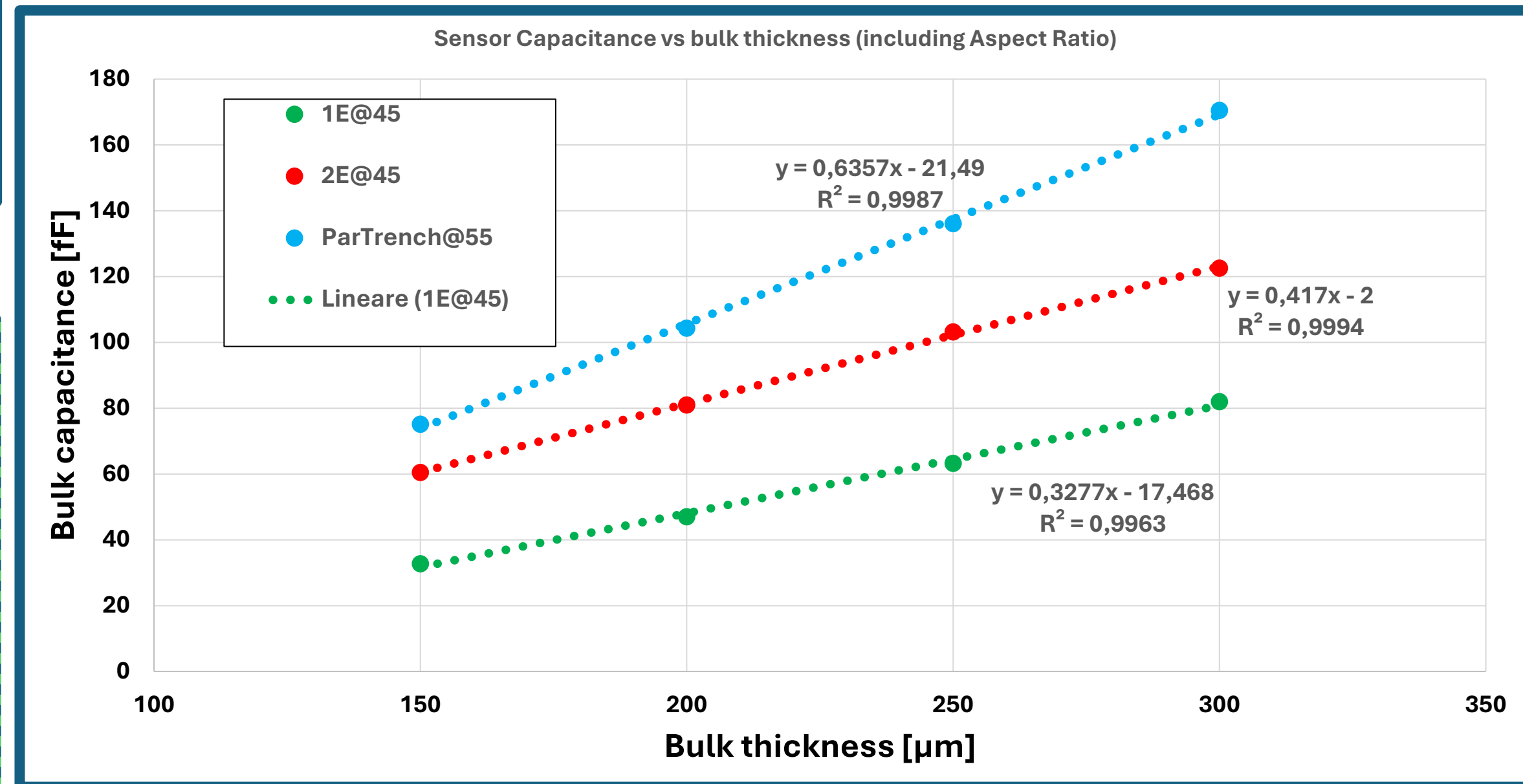
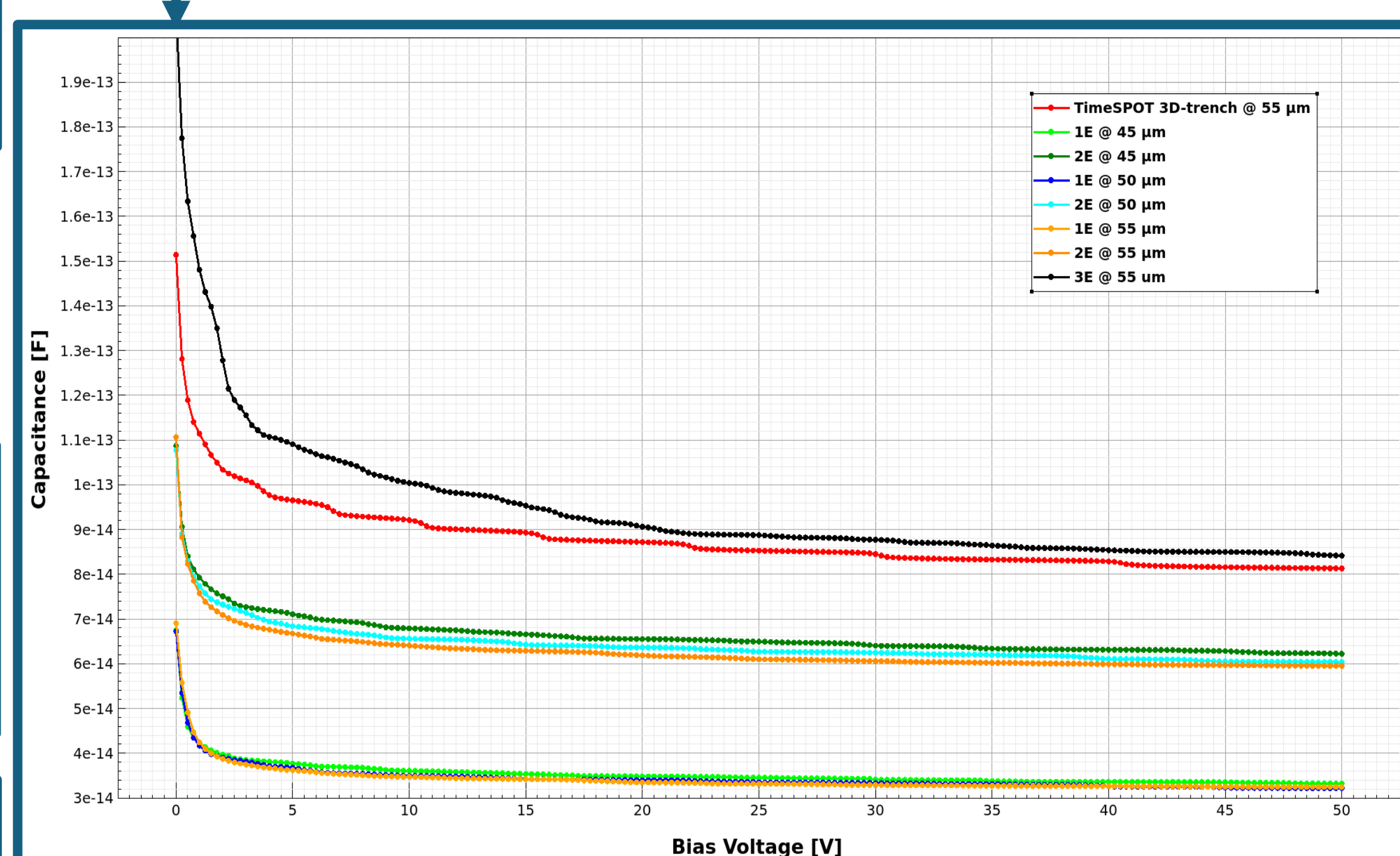
Transient simulation: @150 μ m thickness, the capacitance increases proportional to the number of readout columns used (40 fF for every single RO column). \rightarrow

\leftarrow Better weighting field confinement in parallel electrode geometries

Sensor geometries simulated at 3 different angulations (0°, 10° and 20°). Collected charge as well as Charge Collection time at thresholds of 25 %, 50 %, 75% and 100 %, has been analyzed and compared to 3D-trench device.

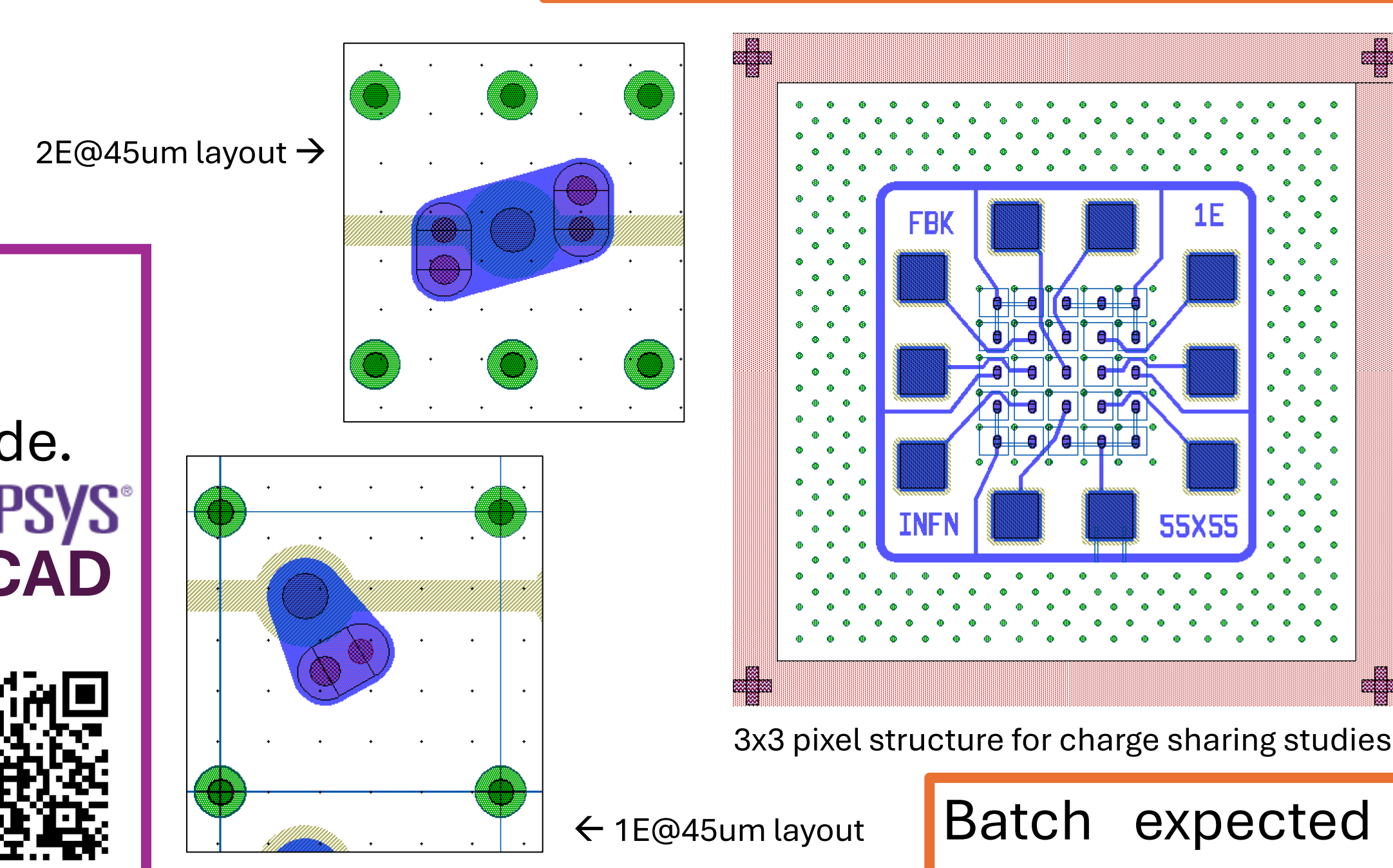


A parallel study considered also capacitance related to wafer thickness, showing an increment of 0.5 fF/ μ m including aspect ration. \rightarrow



\leftarrow The performances of the 2E design @ 45 μ m pitch are the closest to the 3D-trench. The smaller pitch allows faster charge collection time and the columnar shape electrodes reduces capacitance by 25%. \rightarrow

Layout: Layout has been defined based on choices made after the design study and the development of the incoming IGNITE-ER and PicoPix ROCs.



50 μ m and 45 μ m pitch matrices are included in the layout with sizes of 64x64 up to 256x256 pixel.

\leftarrow Pixel geometries will include 1E and 2E geometries to compare the best tradeoff capacitance/CCT. Dedicated 3x3 pixel matrices have been designed.

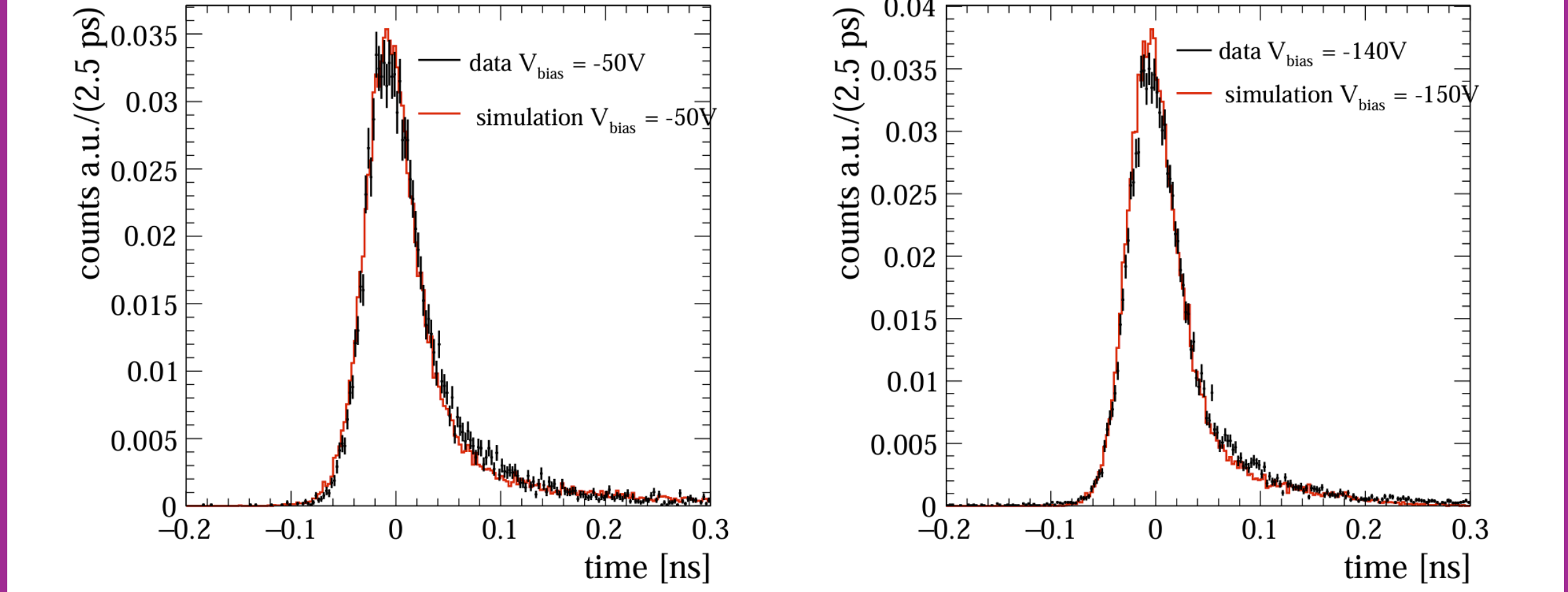
Batch expected back at latest for 2026. Future activities involving charge sharing studies and beam test comparison are currently under preparation

Simulation tools used: The simulation framework developed for these studies involve different tools, including commercial, open source and custom made. Device modelling and stationary simulation \rightarrow SYNOPSIS TCAD

Particle-matter interaction with: Geant4

Charge drift, diffusion and signal generation with TCoDe.

The simulation framework has been used and validated for the first time in 2021, comparing test beam data with equivalent simulation [5]



References

- [1] TimeSPOT 3D trench sensor
- [2] results after irradiation
- [3] TimeSPOT test-beam results
- [4] Recent developments in the IGNITE project
- [5] Accurate simulation vs measurement