

Future e+e- Silicon Detectors & Optimization for LLPs

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ECFA Focus Topic: LLPs

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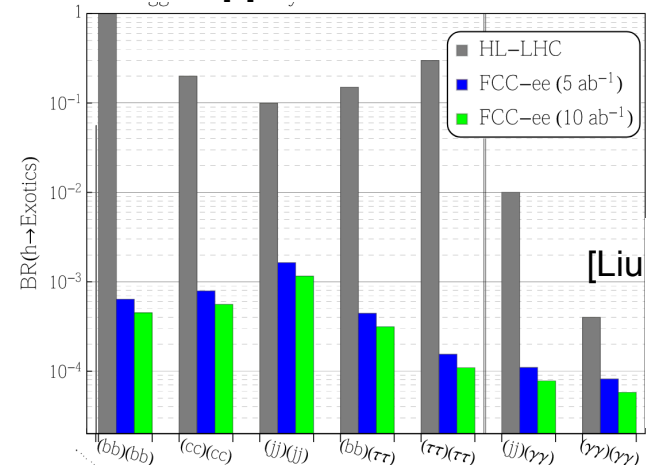
Future e+e- Silicon Detectors

- Physics drivers

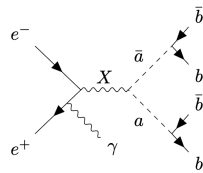
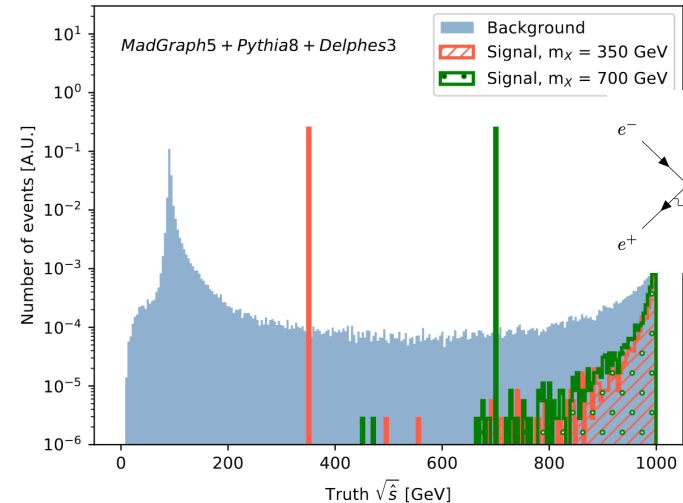
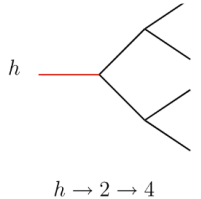
- ▶ Higgs physics: vertexing resolution, strange tagging..
- ▶ Vector-like leptons
- ▶ Exotics Higgs decays, axion-like particles: displaced vertices
- ▶ Resonances/anomaly detection?

- → detector specifications

- ▶ High granularity, spatial constraints (low material budget, low power)
- ▶ Vertexing detector before silicon tracker with $25 \times 25 \mu\text{m}^2$ pixels, $3 \mu\text{m}$ single point resolution, power density $< 30 \text{ mW/cm}^2$



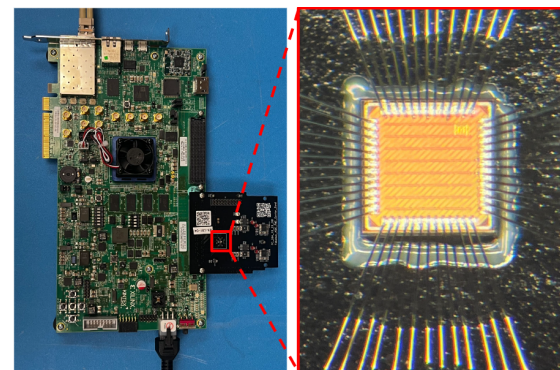
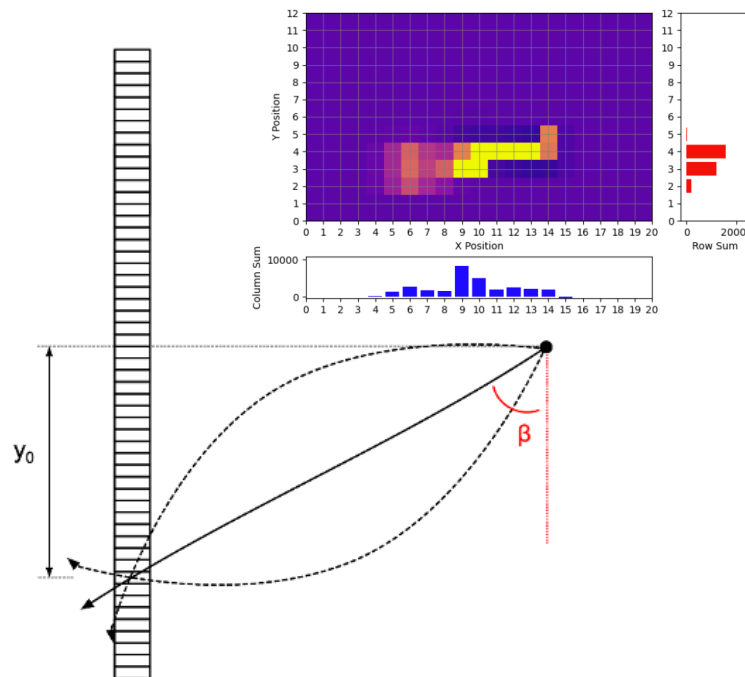
Y. Wang



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Ideas for ML-based Readout

- Working with “smart pixel” datasets [1, 2, 3]: futuristic pixel sensor with datasets of CMS tracks
 - Allows for ML-based compression at source: classification (eg removal of pileup), regression of cluster features
- Studying embedded FPGAs (eFPGAs) as technology to perform *reconfigurable logic on-chip*
 - Taped out 28nm proof-of-concept eFPGA at SLAC and get perfect closure of simple BDT on hardware with respect to golden software (quantized) result



Summary & Next Steps

- eFPGAs for on-detector silicon readout: open source [FABulous](#) framework development, radiation tolerance, etc.
- Study BSM models and specific detector simulations to estimate sensitivity gain for different on-detector compression schemes
 - Eg. Higgs exotic decays, vector-like leptons, axion-like particles, resonances, etc.
- Potential connection of smart readout with [MAPS](#)?

