

DMAPS development at PSI

Monday 17 June 2024 10:15 (15 minutes)

PSI is developing DMAPS prototype chips with time of arrival measurements for potential use in small scale physics experiments at PSI and also for detectors used outside the field of particle physics. After the production of a first basic prototype in the TSI 180nm process node we have recently switched to LF 150 and just submitted a design in a MPW run. We will present the design of this first LF15 DMAPS chip and included test structures.

Type of presentation (in-person/online)

in-person presentation

Type of presentation (scientific results or project proposal)

other

Primary author: KAESTLI, Hans-Christian (Paul Scherrer Institute (CH))

Presenter: KAESTLI, Hans-Christian (Paul Scherrer Institute (CH))

Session Classification: WG/WP1 - CMOS technologies