

1st DRD3 week on Solid State Detectors R&D

Monday, June 17, 2024

WG/WP1 - CMOS technologies: WG1 talks - 500/1-001 - Main Auditorium (9:10 AM - 1:05 PM)

-Conveners: Heinz Pernegger; Jerome Baudot; Eva Vilella Figueras

time	[id] title	presenter
9:25 AM	[100] Introduction to WG1/WP1 session	VILELLA FIGUERAS, Eva PERNEGGER, Heinz BAUDOT, Jerome
9:30 AM	[17] Applying DMAPS technology to the Upgrade of the Belle II Vertex Detector	BABELUK, Maximilian
9:45 AM	[60] DMAPS for measuring energy depositions and tracks of Galactic Cosmic Ray and Solar Energetic Particles	LAMBROPOULOS, Haris
10:00 AM	[11] R&D of MAPS for the Super Tau-Charm Facility(STCF)	XU, Lailin
10:15 AM	[9] DMAPS development at PSI	KAESTLI, Hans-Christian
10:30 AM	Coffee Break	
11:00 AM	[31] Research on CMOS MAPS at GSI/FAIR – Status and Next Step	DEVEAUX, Michael
11:15 AM	[75] The ATLASPIX3 CMOS pixel sensor and module performance	Prof. ANDREAZZA, Attilio USTUNER, Fuat
11:30 AM	[4] Characterization of the RD50-MPW4 HV-CMOS pixel sensor	PILSL, Bernhard
11:45 AM	[71] Adaptation and Modularization of MPW4 Firmware for Integration into the Caribou Boreal Architecture: A Pilot Project	JIMENEZ SANCHEZ, Jorge PALOMO PINTO, Francisco Rogelio
12:00 PM	[30] Results and perspectives of the Monopix2 depleted monolithic active pixel sensors	SCHALL, Lars Philip
12:15 PM	[73] Radiation hardness and timing performance of MALTA monolithic Pixel sensors in Tower 180 nm	FASSELT, Lucian
12:30 PM	[113] Development of MAPS using 55nm HVCMOS process for future tracking detectors	LI, Yiming

WG/WP1 - CMOS technologies: WP1 review of project proposals - 500/1-001 - Main Auditorium (1:40 PM - 6:05 PM)

-Conveners: Eva Vilella Figueras; Jerome Baudot; Heinz Pernegger

time	[id] title	presenter
2:00 PM	[58] First measurements on the CASSIA Sensor (CMOS Active SenSor with Internal Amplification)	PERNEGGER, Heinz
2:15 PM	[57] All-silicon ladder concept for CMOS monolithic pixel detectors	VOGT, Marco
2:30 PM	[67] Innovations in CMOS Pixel Sensor Technology at IPHC: Projects and Future Prospects	MOREL, Frederic
2:45 PM	[46] The H2M project: Porting the functionality of a hybrid readout chip into a monolithic 65 nm CMOS imaging process	GADOW, Philipp
3:00 PM	[28] Fine-pitch CMOS pixel sensors with precision timing for vertex detectors at future Lepton-Collider experiments	SPANNAGEL, Simon

3:20 PM	[51] A versatile pixel matrix in TPSCo 65 nm for future trackers	BAUDOT, Jerome
3:40 PM	Coffee Break	
4:10 PM	[77] Large area low-power Monolithic CMOS Tracking Detectors for future particle physics experiments	Prof. ANDREAZZA, Attilio GAO, Yanyan
4:30 PM	[21] Large electrode sensors with intrinsic amplification for ultimate timing performance	Prof. SCHWEMLING, Philippe
4:50 PM	[72] CMOS Active SenSor with Internal Amplification – CASSIA	SULIGOJ, Tomislav
5:10 PM	[99] DRD7 - Technology Access	SNOEYS, Walter
5:30 PM	[117] An OpenPDKs/OpenSource approach to DRD3 CMOS sensors	MUENSTERMANN, Daniel

Tuesday, June 18, 2024

WG/WP1 - CMOS technologies - 500/1-001 - Main Auditorium (8:40 AM - 9:45 AM)

-Conveners: Eva Vilella Figueras

time	[id] title	presenter
8:40 AM	[70] Thin monolithic High Voltage CMOS sensors with excellent radiation tolerance	VILELLA FIGUERAS, Eva
9:00 AM	[35] Radiation hard read-out architectures	SOLANS SANCHEZ, Carlos
9:20 AM	[111] Monolithic CMOS Strip Sensors for large area detectors	WEINGARTEN, Jens
9:40 AM	[118] Next steps in WG1 (September zoom meeting)	VILELLA FIGUERAS, Eva PERNEGGER, Heinz BAUDOT, Jerome