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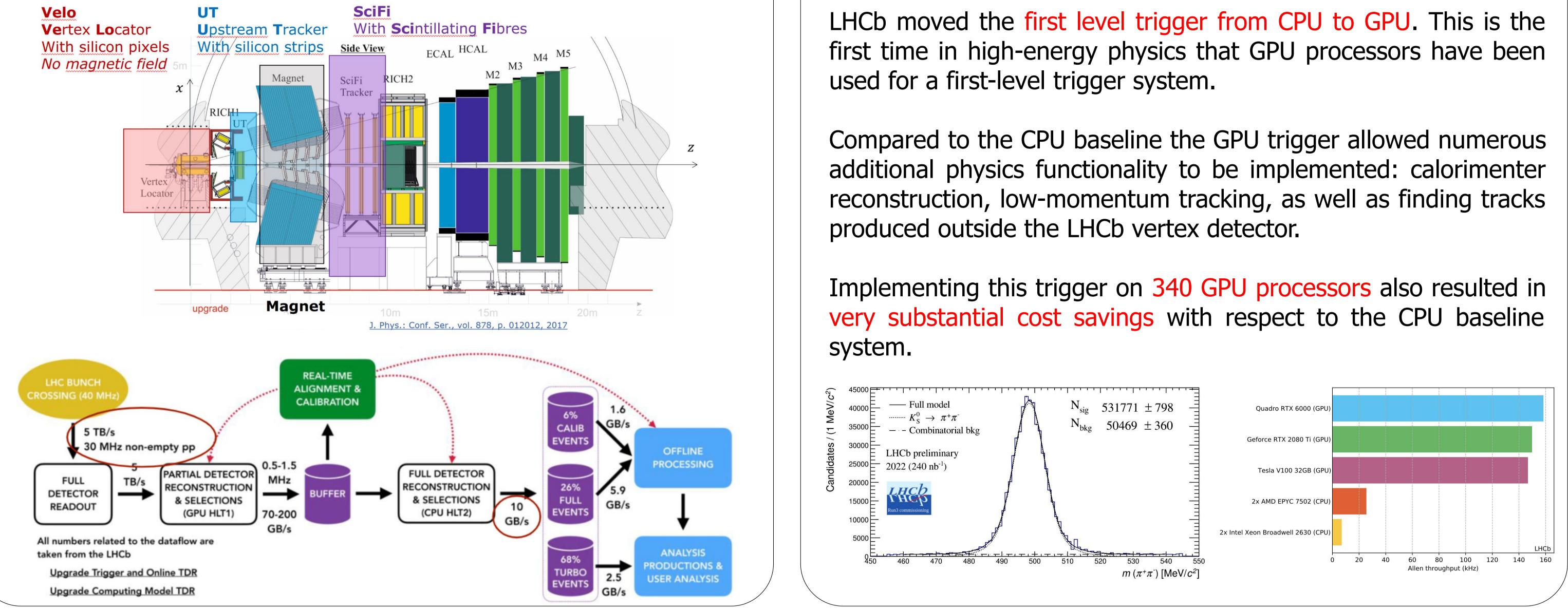
Track Reconstruction with Graph Neural Networks on Heterogeneous Architectures





Fotis Giasemis on behalf of the **SMARTHEP/ANN4EUROPE** team SMARTHEP Edge ML School, CERN, September 2024

LHCb Detector and Dataflow



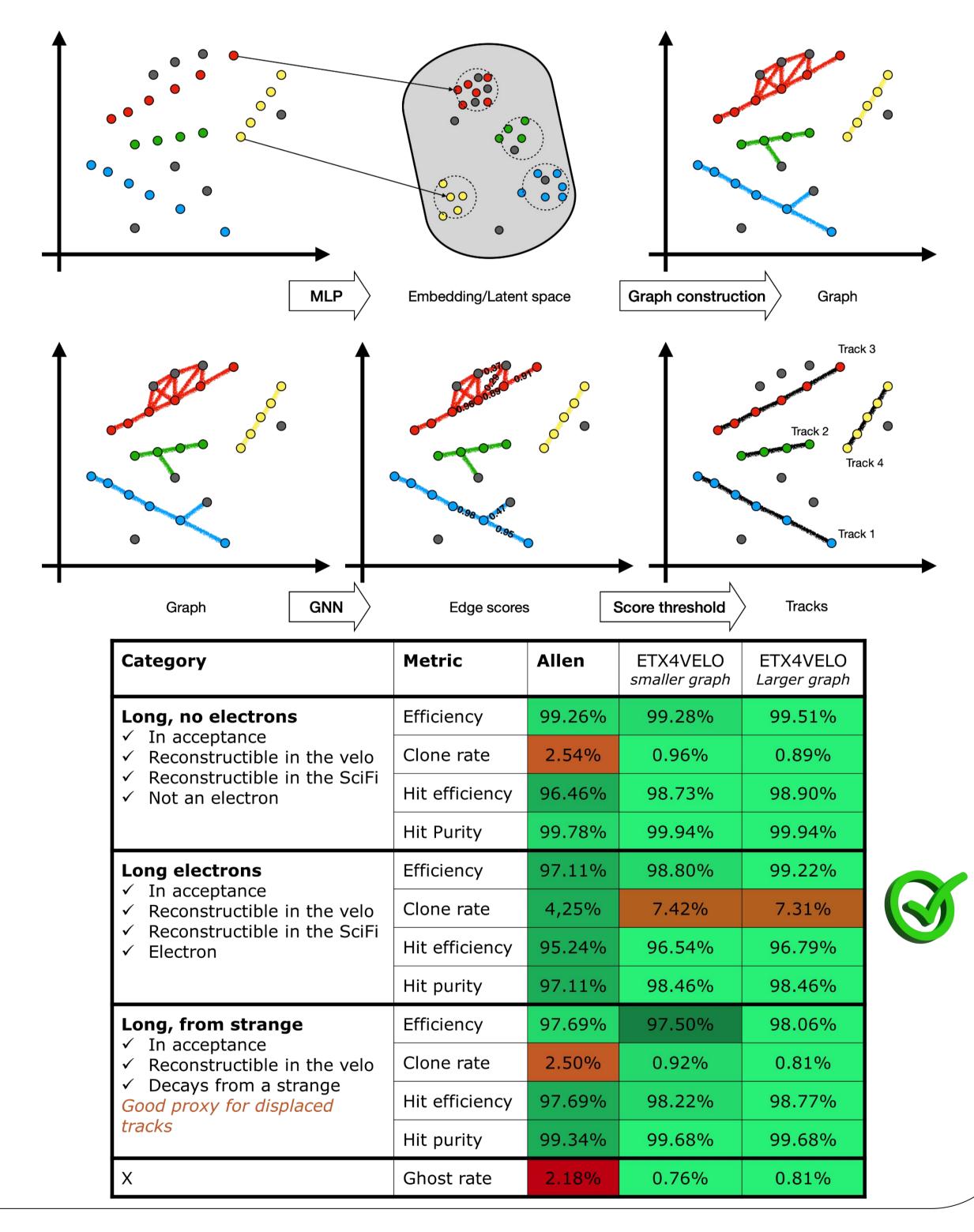
First Level Trigger (HLT1)

ETX4VELO: Graph Neural Network-Based Pipeline for Track Finding in the Velo at LHCb

The focus is to evaluate deep-learning algorithms performance for **EFFICIENCY and THROUGHPUT**, and estimate how these models scale up with the increase of data rate.

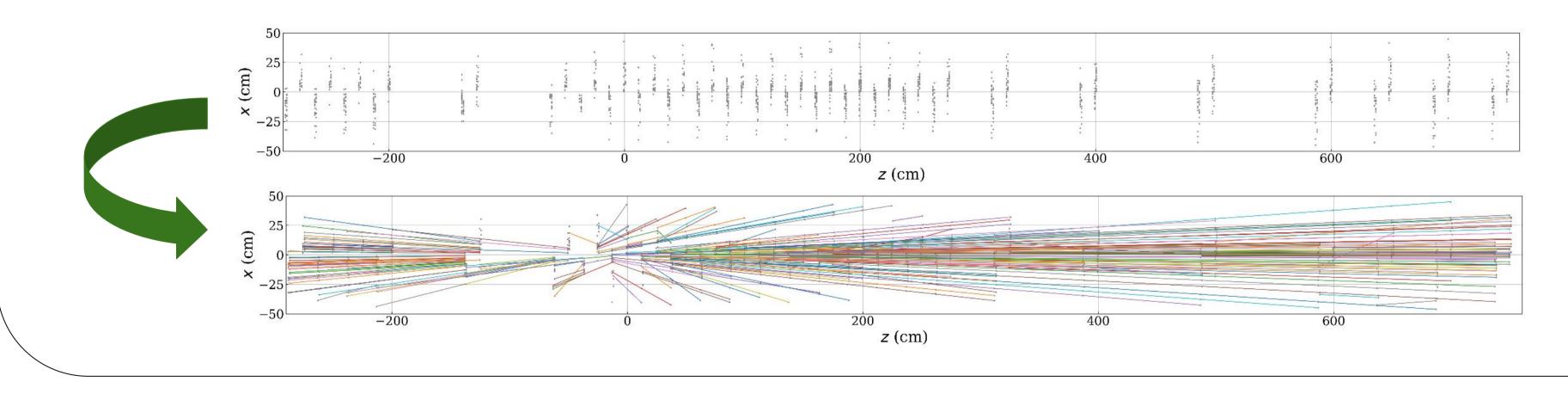
For this purpose we developed the ETX4VELO pipeline which focuses on developing Graph Neural Networks (GNNs) algorithms for track reconstruction.

Steps of the pipeline:



- Embedding
- Graph Construction
- GNN
- Triplets (not shown in the diagram)
- Score cut

The efficiency reached at this level is higher than Allen. The ETX4VELO pipeline is based on edge and triplet GNN model that can reconstruct harsh cases of shared hits between tracks.



Inference of the ETX4VELO Models on GPUs and FPGAs

Then we focused on running these algorithms on architectures with more potential for performance. • Integration inside the LHCb's first-level GPU trigger (HLT1) • Inference of the MLP and GNN on GPU • Inference of the MLP on FPGA NVIDIA GeForce RTX 3090 https://arxiv.org/abs/2407.12119 10 (ents/s) ₅01 Ighput 104 ETX4VELO ONNX Runtime FP32 ETX4VELO TensorRT FP32 ETX4VELO TensorRT INT8 Allen VELO decoding Embedding VELO tracks k-NN GNN Steps

