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Type: **Poster and Flash talk**

Nanosecond AI for anomaly detection with decision trees on FPGA

Monday 23 September 2024 16:52 (3 minutes)

We present an interpretable implementation of the autoencoding algorithm, used as an anomaly detector, built with a forest of deep decision trees on FPGA, field programmable gate arrays. Scenarios at the Large Hadron Collider are considered for which the autoencoder is trained using the Standard Model. The design is then deployed for anomaly detection of unknown processes. The inference is made with a latency value of 30 ns at percent-level resource usage using the Xilinx Virtex UltraScale+ VU9P FPGA. The work is documented at <https://arxiv.org/abs/2304.03836>

What of the following keywords match your abstract best?

Real-time algorithms

Please tick if you are a PhD student and wish to take part to the poster prize competition!

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Session Classification: Flash talks / poster session