

High Efficiency Klystrons for MuCol

G. Burt for HE project team at CERN & Lancaster:

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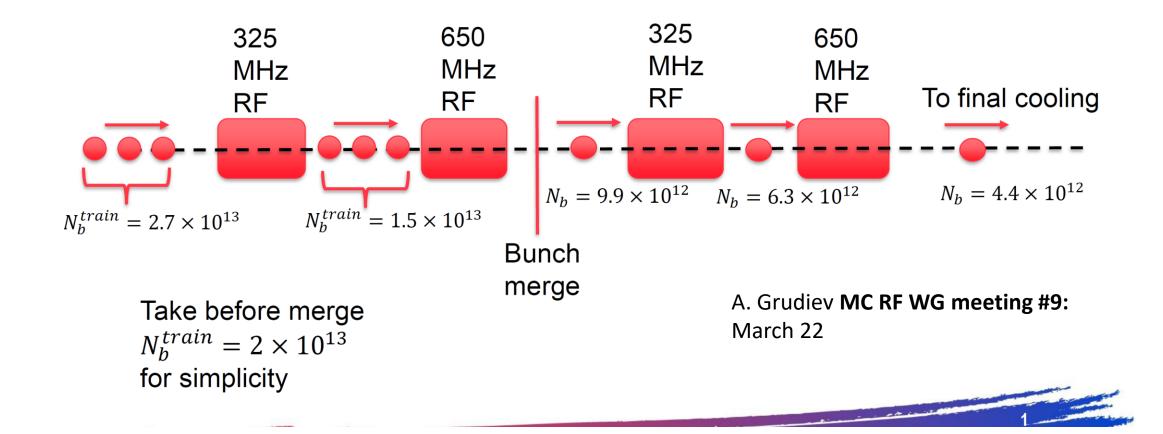
Nuria Catalan

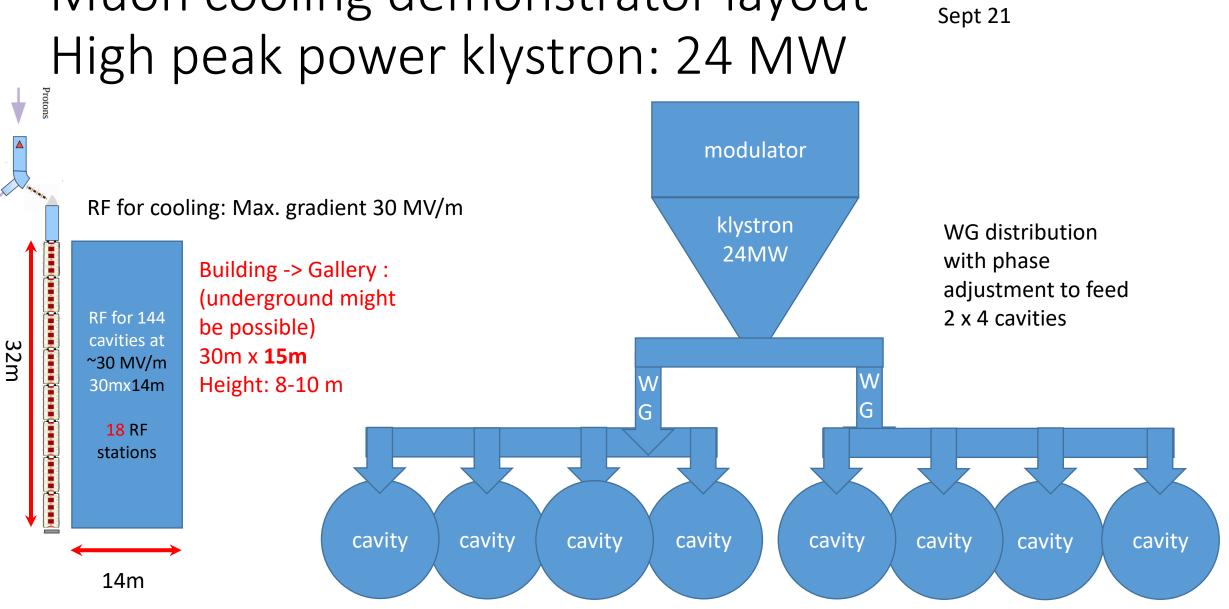
Zain Un Nisa

Anis Baig



Cooling channel: beam parameters



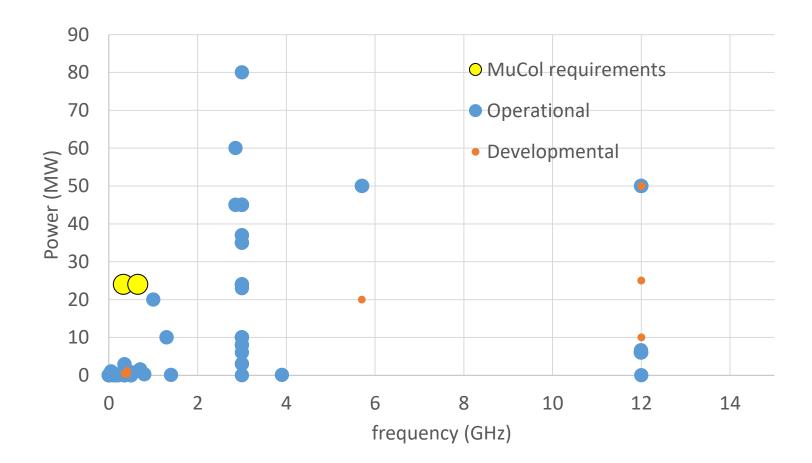


Muon cooling demonstrator layout

A. Grudiev and R. Losito MC RF WG meeting #5:

Existing tubes

While tubes >24 MW exist they are all above 3 GHz There are 10-20 MW tubes developed for CLIC drive beam and ILC at 1-1.3 GHz Nothing of this power exists at 325 or 650 MHz Issue is typically that to get high power means high voltage which makes the tubes longer For scaling at low frequency this used to not be feasible as length is inversely proportional to frequency for constant beam New developments may change that.....



High power L-band Multi Beam Klystrons (MBK). Commercial tubes.

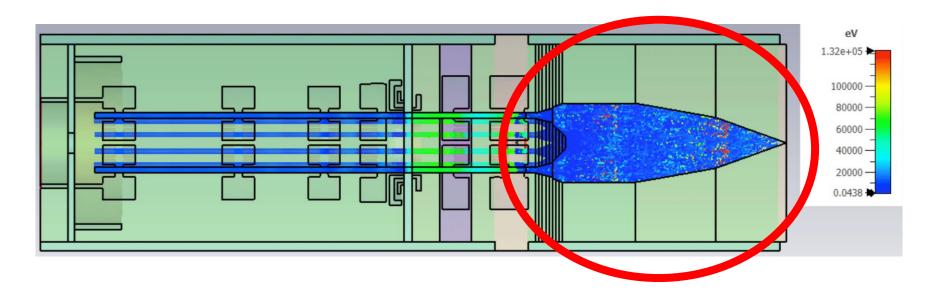




Frequency: **1.0 GHz** Peak RF power: 20 MW Efficiency: 70% Frequency: **1.3 GHz** Peak RF power: 10 MW Efficiency: 65%

Collector

- A large chunk of a klystron's length is the collector
- In DC mode the collector must absorb the entire beam power, and dissipate the heat load without heating up too much
- The Canon E37503 had a max heat load of 300 kW and was 1.5m long (ie half the tubes length)
- The DC heat load for the muon collider klystron is sub-5 kW and hence a much shorter collector can be used (30 cm may be fine).



Scaling Klystrons (PSP)



Scaling Procedures and Post-Optimization for the Design of High-Efficiency Klystrons

Jinchi Cai, Igor Syratchev[®], and Zening Liu

 Key numbers in klystron scaling is the electron wavelength, Beta, and the bunching parameter, A,

$$(\beta_e = \omega/v_e). \qquad A = \frac{I_0}{U_0^{\frac{3}{2}}} \frac{\eta_0 U_e^{1/2}/\pi}{\gamma^2(\gamma+1)^{\frac{3}{2}}} \sum_{n=1}^{\infty} \frac{r_c^2}{r_b^2} \left[\frac{2}{\mu_{0n}^2} \frac{J_1(\mu_{0n} \frac{r_b}{r_c})}{J_1(\mu_{0n})} \right]^2.$$

 If we scale a klystrons voltage, current, or frequency certain relationships need be maintained

$$U_0 I_0 = \text{constant}$$

$$f(\beta_e z) = \text{constant}, \quad \sqrt{A}\beta_e L_{\text{drift}} = \text{constant}$$

$$\frac{1}{\rho Q} \frac{1}{|M(\beta_{e0})|} \frac{1}{N_b I_0} = \text{constant}$$

$$\frac{n\omega - \omega_0}{\rho\omega_0} \frac{1}{|M(\beta_{e0})|} \frac{1}{N_b I_0} = \text{constant}.$$
(12)

 In our case scaling from 1 GHz to 350 MHz, while keeping A constant would mean the length increases by a factor of 3

Scaling the Canon tube to 0.7GHz, 24MW and 30 $\mu sec.$

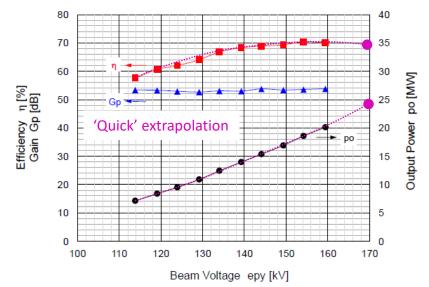
	Canon E37503	>	tube, 0.7 GHz ams MBK			
F=	999,5 MHz	F=	700 MHz			
P max=	20.2 MW	P max=	24 MW			
T =	150 µsec	T =	30 µsec			
V=	159.4 kV	V=	171 kV			
I total =	180 A	I total =	200 A			
Eff.=	70.5 %	Eff.=	70.0 %			
uP=	0.47 µAxV ^{-3/2} /beam	uP=	0.47 µAxV ^{-3/2} /beam			
Gain =	53.9 dB	Gain =	53.9 dB			
P average (5	50Hz)= <mark>150kW</mark>	P _{average} (5Hz) = 3.6kW				

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To our experience such a scaling is a 'low' risk development:

- For the fixed micro perveance, the tube length is proportional to the frequency
- Lower cathode current density (55%) and increased life time.
- Much lower average power (simpler collector)
- Marginal (~10%) increase of the modulator voltage and current.



Cost and schedule:

- The CLIC tube prototypes were designed/built about 10 years ago; Canon: iiiii and Thales : iiiiii. Mu-tube cost will be within this range, as the companies shall do it not from scratch, but could scale it from exiting ones. Though, today there is no market for such devices, thus the cost of 'unique' prototype could be even higher.
- Similar to the CLIC tubes, it will take about 24 month to design, built and test the first Mu-tube prototype. Additional budget will be needed for the testing infrastructure (like RF loads etc.).

How do we reduce the size?

- Drift scales proportional to the bunching parameter, A, and the beam velocity. $\sqrt{A}\beta_e L_{drift} = constant$
 - We can use a higher beam voltage
- Bunching parameter is given by

$$A = \frac{I_0}{U_0^{\frac{3}{2}}} \frac{\eta_0 U_e^{1/2} / \pi}{\gamma^2 (\gamma + 1)^{\frac{3}{2}}} \sum_{n=1}^{\infty} \frac{r_c^2}{r_b^2} \left[\frac{2}{\mu_{0n}^2} \frac{J_1(\mu_{0n} \frac{r_b}{r_c})}{J_1(\mu_{0n})} \right]^2.$$

- We could increase beamlet current (use less beams) but then the voltage needs to decrease
- Or the filling factor can be changed but this is only a 25% change.

Two-Stage Multi Beam Klystron (TS MBK) technology.

Specific features

- 1. Bunching at a low voltage (high perveance). Very compact RF bunching circuit.
- 2. Bunched beam acceleration and cooling (reducing $\Delta p/p$) along the short DC voltage post-accelerating gap.
- 3. Final power extraction from high voltage (low perveance) beam. **High efficiency.**

Additional advantages:

- For pulsed tubes, the second HV stage can be operated in DC mode. Thus simplifying the modulator topology. (cost/volume) and increasing the modulator efficiency.
- 2. Simplified feedback for the first stage pulsed voltage. Improved klystron RF phase and amplitude stability.

Drawbacks:

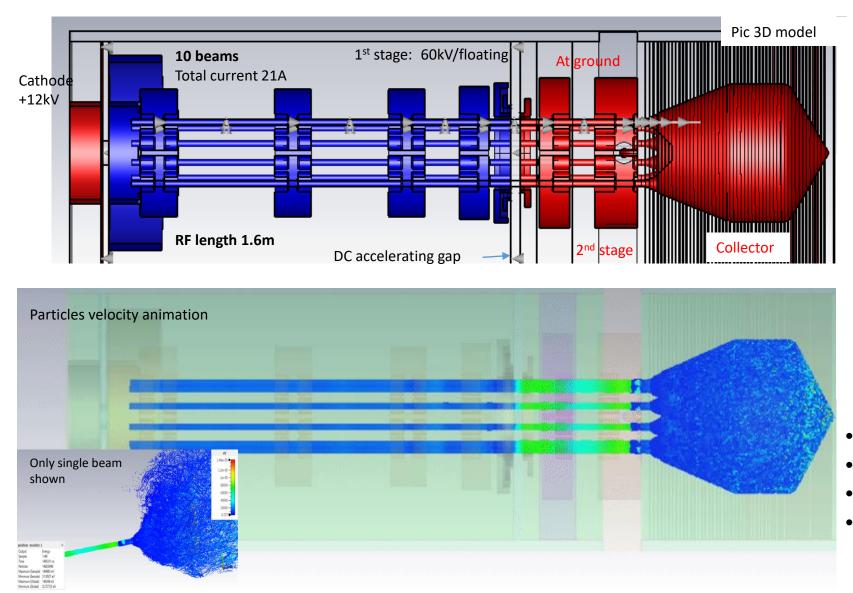
- 1. Reflected electrons from the output cavity and collector shall be **avoided at any cost**.
- 2. RF radiation into DC gap has to be sealed.
- 3. Requires special HV isolated RF feedthrough to inject RF signal into input cavity.

GOOD FOR: UON Collider ollaboration **HV insulators** TS HE MBK Efficiency 85% Stage ' V₁ , Post accelerating gap Stage V_2

Commercial HE MBK Efficiency 70%

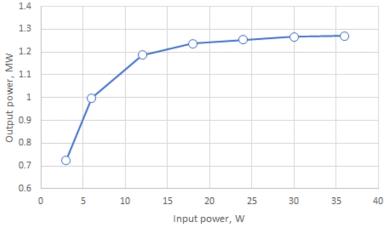
 $V_1 + V_2$

FCC Two-stage MBK klystron: CW, 400MHz, 1.28MW.





Power gain curve at nominal voltage (PIC results)



- Very Efficient: 84%
- Compact: ~2.5m length in total
- Low voltage: 60kV+12kV
- High saturated power gain: 46dB

CLIC Two-stage MBK klystron: Pulsed, 1.0 GHz, 24 MW

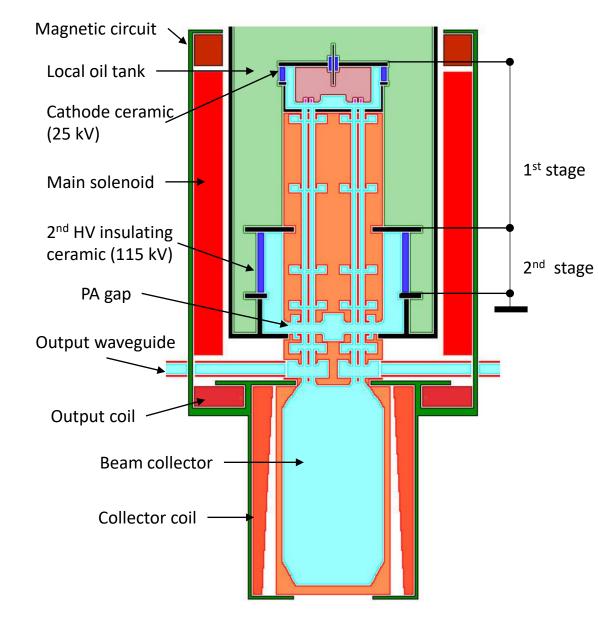
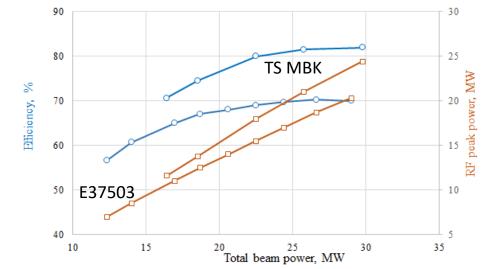


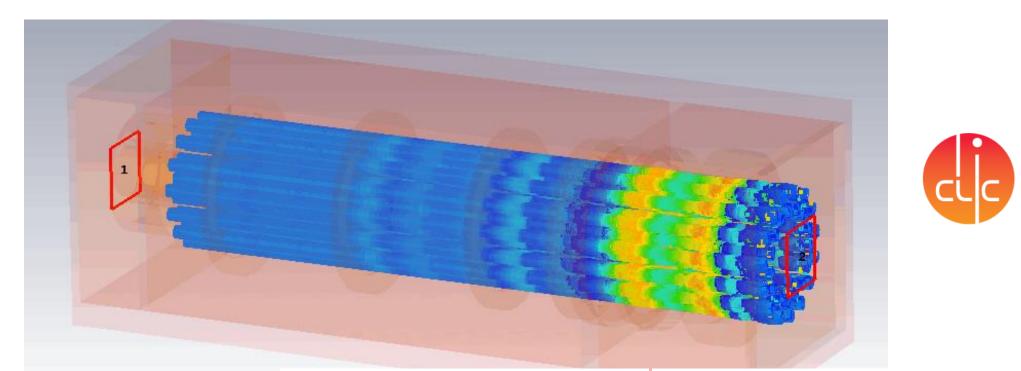
TABLE I. DESIGN AND SIMULATED PARAMETERS (CST/3D) OF THE CLIC TS MBK AND CANON MBK E3750 CATALOGUE DATA

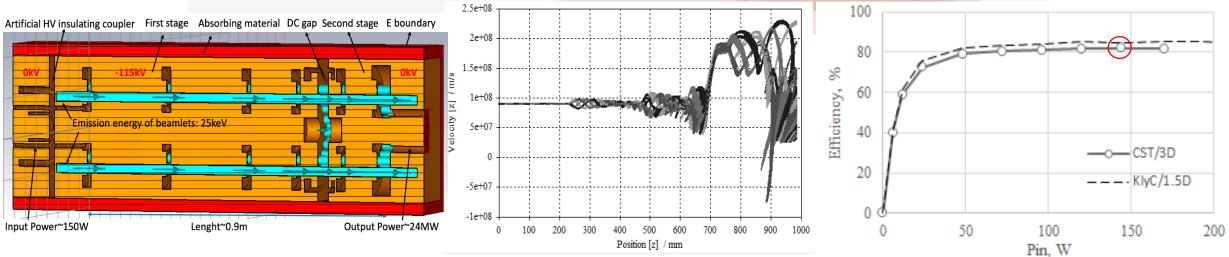


Parameter	TS MBK	E37503	Unit
Operating frequency	1000	1000	MHz
Voltage at the 1 st stage	25	160	kV
Voltage at the 2 nd stage	140		
Total beam current	212	180	А
Number of beamlets	30	6	
Number of cavities	6	6	
Perveance at the 1 st stage	1.77	0.47	$\mu A/V^{3/2}$
Perveance at the 2 nd stage	0.133		
Output RF power	24.1	20	MW
Saturated power gain	52	54	dB
Saturated efficiency	82	70	%
Length of RF circuit	900	1500	mm



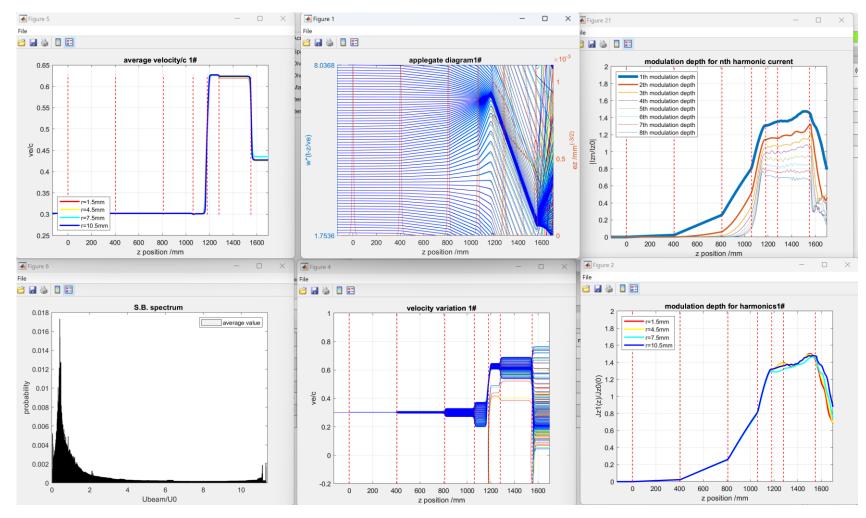
CLIC Two-stage MBK klystron: Pulsed, 1.0 GHz, 24 MW





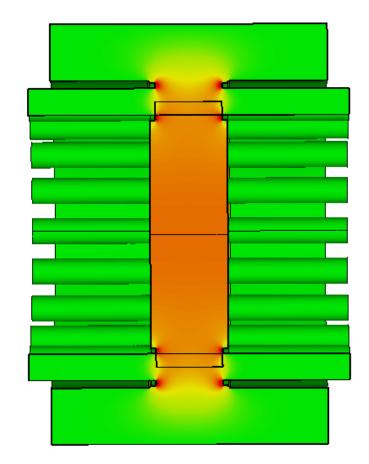
Example 352 MHz

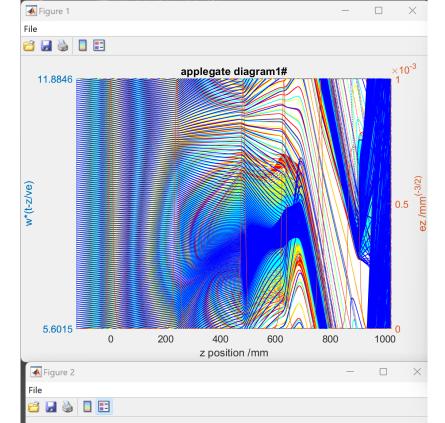
- (Incomplete) design of a 12 MW 352 MHz klystron.
- RF circuit is 1.55 meters long, total length inc. collector will be around 2 m
- Further work needed to get to 24 MW by adding more beamlets (CLIC was 30 this is only 20) and improving efficiency



Scaling CLIC Klystron to 400 MHz

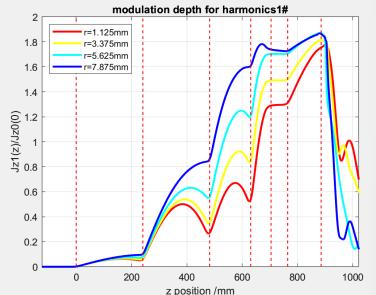
- High efficiency klystrons always have long gaps to allow bouncing electrons. This allows a larger gap voltage to flatten the develeration.
- At 400 MHz a comparable long gap would have 2.5 times more voltage at the same field.
- We will require low external Q's on the output gap to keep the voltage low.



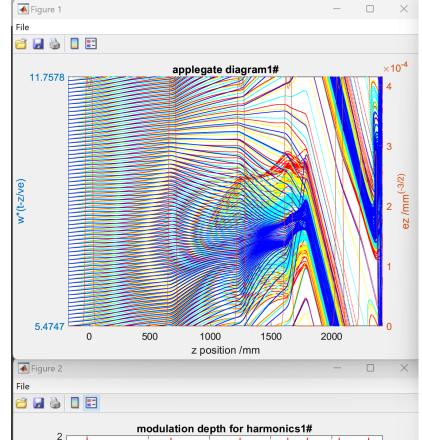


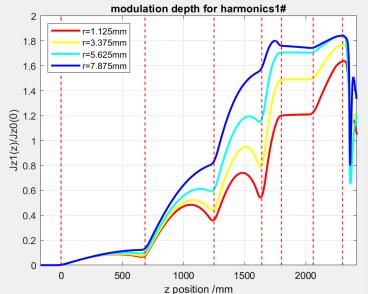
20 beam version

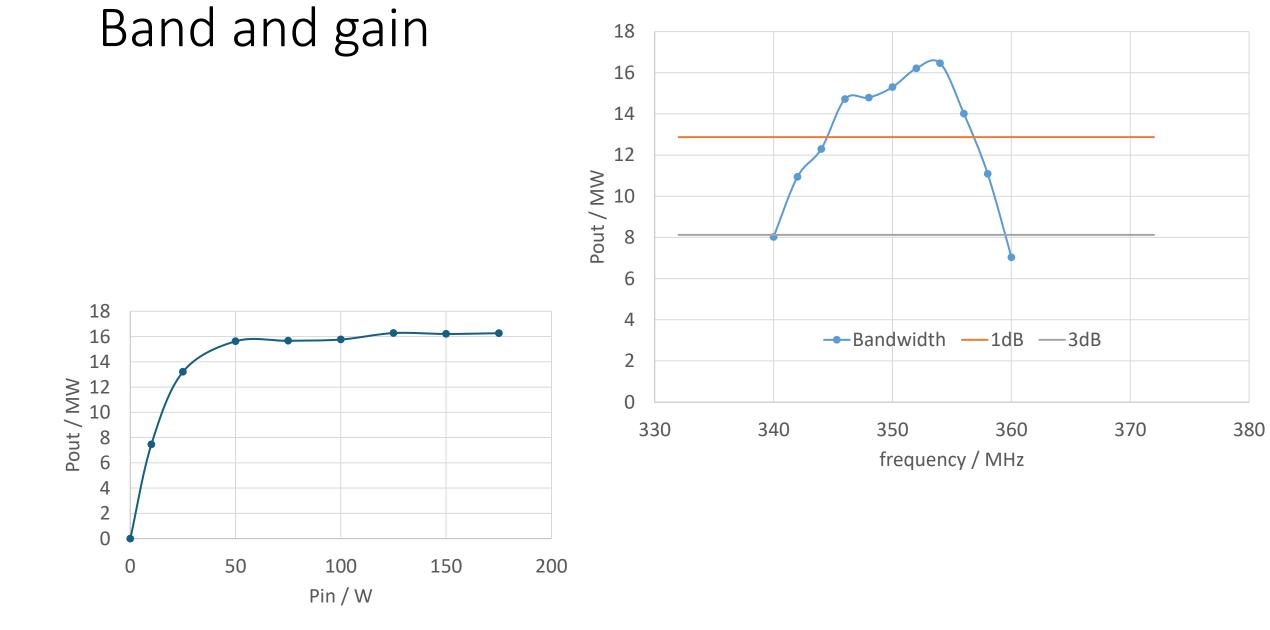
- CLIC is on left, Muon on right
- Not identical but very similar



 Needed some tweaking of frequencies and spacing as gap profile is different

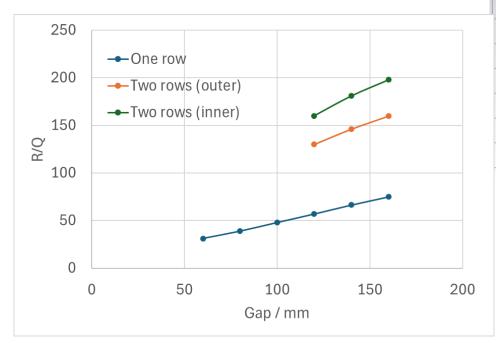






30 beams: 1 or 2 rows of beams?

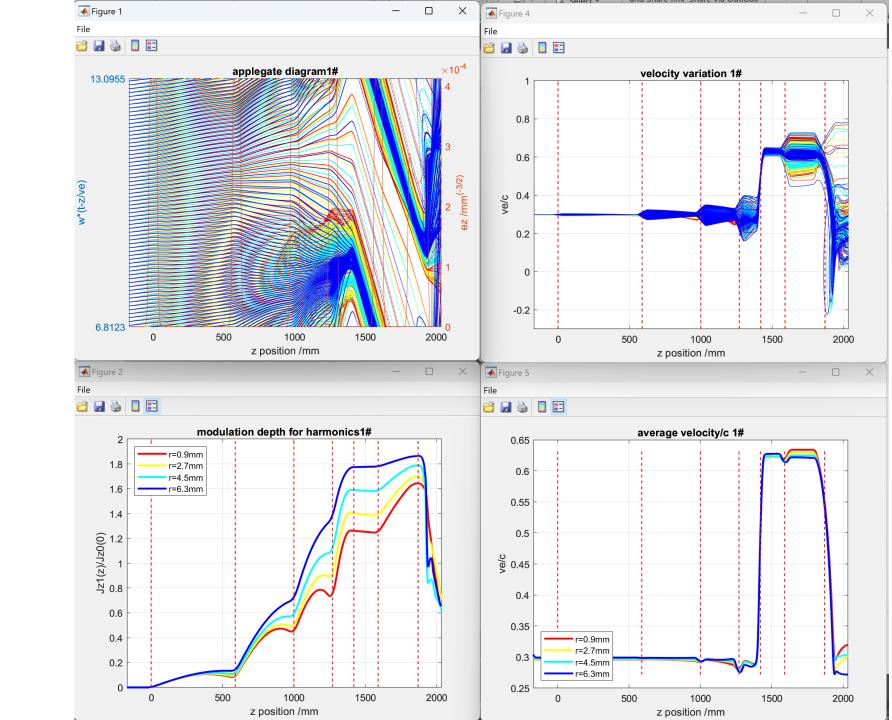
 To fit in 30 beams we can have two rows of 15 or one row of 30



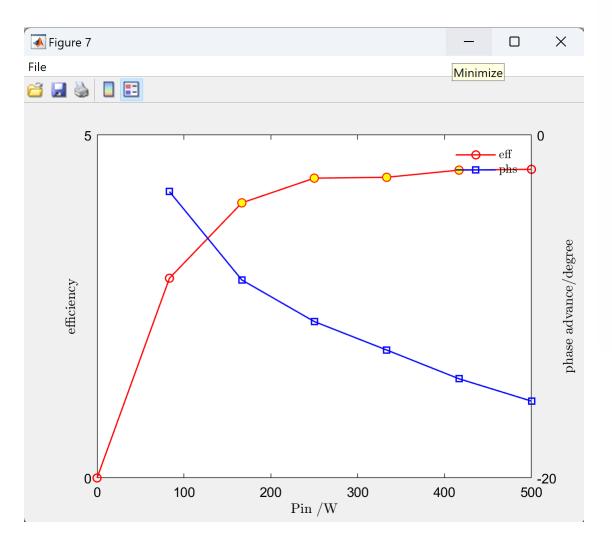
- One row needs a large aperture which lowers the impedance.
- Two rows means a different impedance

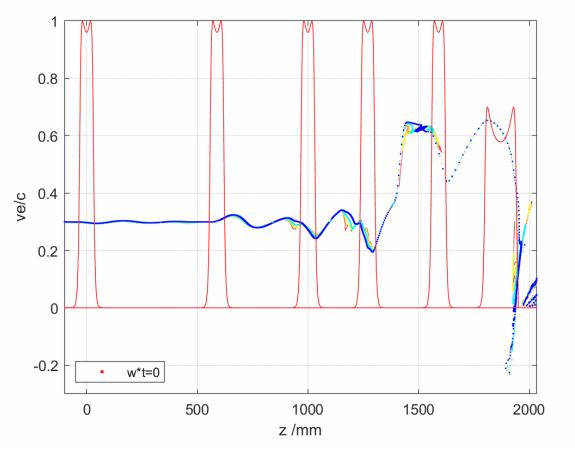
30 beam version

- Two rows high impedance
- 24.1 MW 83% efficiency



Reflected electrons





At lower input powers the beam is decelerated for the bounce, but doesn't get reaccelerated enough to escape leading to reflected electrons. Need to vary the gap

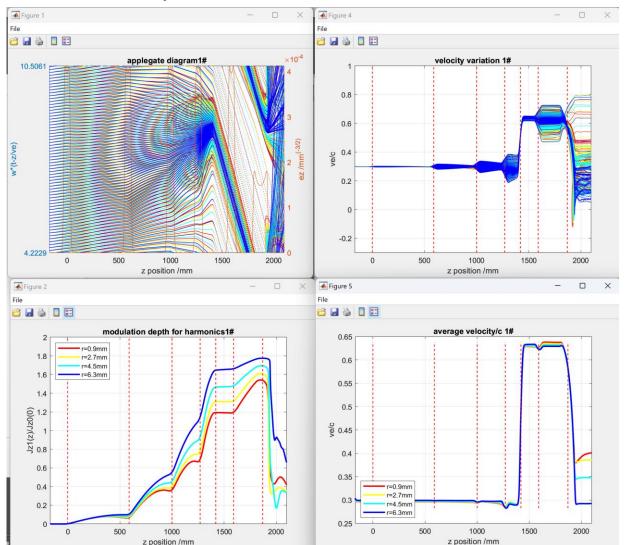
Shorter output gap (100->80 mm) big drop in power

- A shorter gap gets rid of the reflected electrons but the bounce is required for HE so efficiency drops
- Only 20 MW

KlyC		· · · · · ·		Beam Para.	Eff. optimiz	er		Accuracy Setting	g Plot s	seturig			Conv. OL FigOff	FigOn GIF on		•	Txt outpu	rt C	Cores 4		•
N	lew			Beam Voltage	(kV)		25.400	Space Charge Fi	ield Order			8	Simulation results summa	ary							
Or	pen			Beam Current	(A)		7.070	Division Number	r in λ_e-			128	Pout=	2.02e+04 kW	Gain=	50	0.04 dB Vg	(1.1.0)	_	p (d)/E kV/mm	_
Si	ave			Outer Radii/ xt	o (mm)		7.200	Division Number	r in RF			256	Eff.RF=	429 %			74.9 %	(KV)	0.637		0.2384
Sav	ve as			Inner Radii/ yb	(mm)		0.000	Max Iterations				200	Re.RF=	9.104e-05	Re.El=	9.495e-			3.634		1.3589
Sim	nulate			Tube R. (m	12.00	00 yd	0	Iteration Residua	al Limit			0.0001				5.4556			6.872		2.5695
	EM			Beam Number			30	Iteration Relaxation	tion			0.1	IJ1/J0 .i=		IJ1/J0 .o=				14.822		1.9658
				Layer Number			4											_	114.999		0
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																			169.048		8.2895
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	Ramp	S.C		Reflection from (autput			Pin (W)		degree	chirp		Reflected electrons	No	Тсри=	22					
n.C				Reflection from a	output 0 de	>gree	0			degree 360.000	chirp 0.000		Reflected electrons	No	Тсри=	33	min 8.36				
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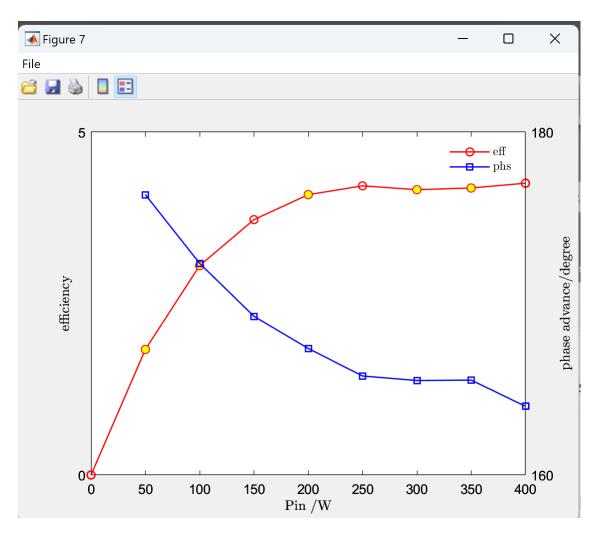
Larger gap 100->140 mm but also went to single row to decrease R/Q and increase Q

 Efficiency has improved and slowest electron velocity has increased

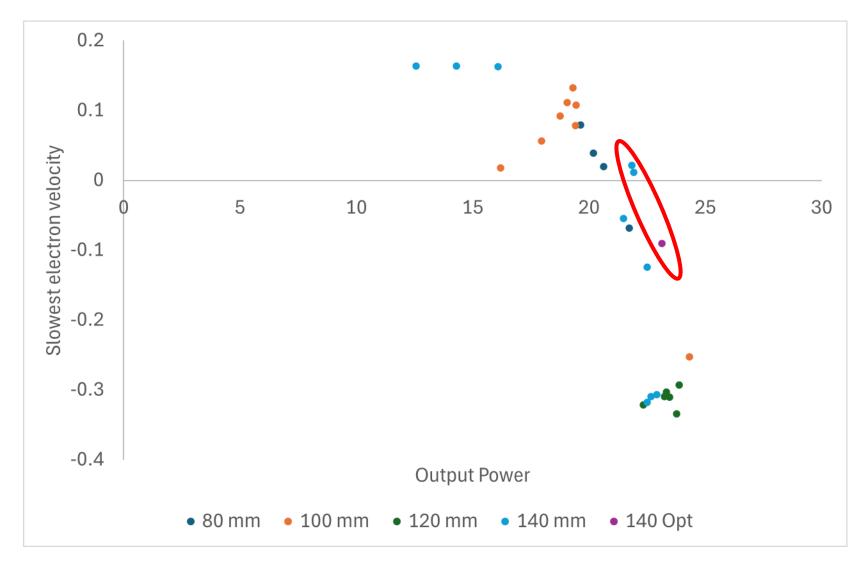


Simulation re	sults summary			
Pout=	2.25e+04	kW Gain=	49.54	d
Eff.RF=	477	% Eff.BI=	417.6	9
Re.RF=	9.789e-05	Re.El=	0.007348	
IJ1/J0 .i=		IJ1/J0 .o=		
ve/c.min=	1.542 -0.1238	Gama =	1.775 0.4487	

Still getting reflected electrons



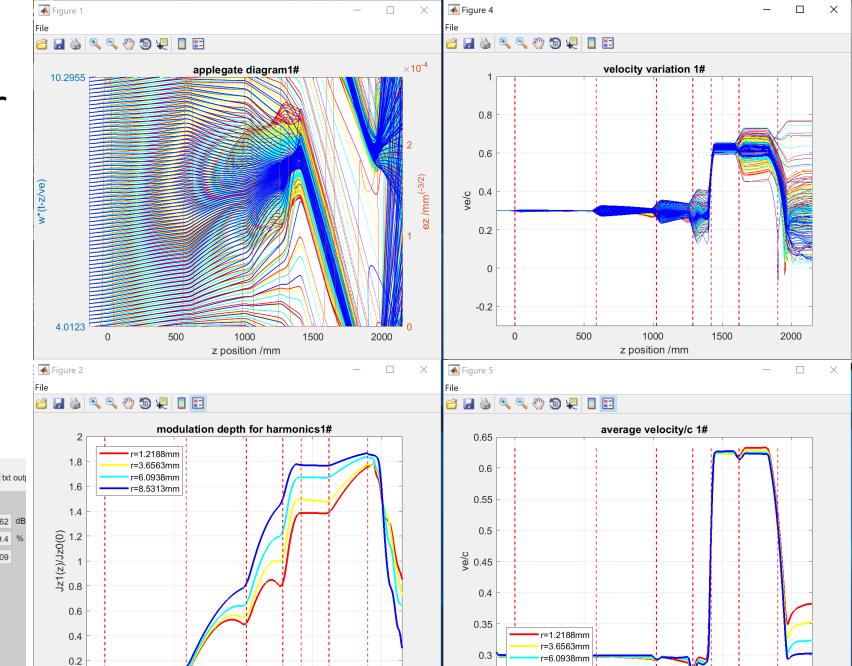
Pareto front Power versus min vel.



- Optimizing the output cavity tends to keep a linear relationship between power and electron velocity
- Two outliers seem to break that
- Let KLyC optimizer play with the bunch hing circuit

KlyC optimiser

- KlyC optimizer has got it back to 78% efficiency and 23.13 MW
- Min electron velocity increased to -10%c
- Significantly less stratification and not currently at saturation (ran out of time)



r=8.5313mm

500

1000

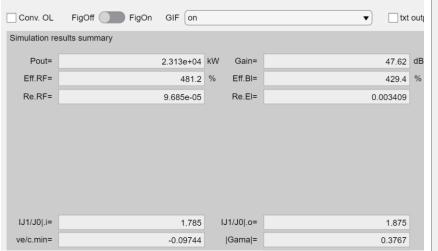
z position /mm

2000

1500

0.25

0



Ω

0

500

1000

z position /mm

1500

2000

Conclusion

- Current 352 and 704 MHz klystrons are limited to 2.9 and 1.5 MW respectively, requiring a lot of klystrons for a muon collider.
- 704 MHz tube may well be within scaling range of the CLIC tubes at 24 MW
- The 352 MHz tube would be very long if scaled from the same tube
- Two-stage technologies significantly shorten low frequency, high power tubes and is suitable for 352 MHz solution
- KlyC simulations show that 12 MW klystron is possible in under 2 m, and further work should push this to 24 MW.
- Reflected electrons is an ongoing issue