

MAPS detectors technologies for the FCC-ee vertex detector

Monday 1 July 2024

Readout Architectures and data handling for low power - 4/3-006 - TH Conference Room (16:10 - 18:35)

-Conveners: Grzegorz Deptuch

| time | [id] title | presenter |
|-------|---|---------------------------|
| 16:10 | [24] MOSAIX as a case study: overview of the stitched chip for ITS3 | AGLIERI RINELLA, Gianluca |
| 16:40 | [25] Electronics challenges and considerations including triggered vs data push architectures | SNOEYS, Walter |
| 17:00 | [29] Silicon Photonics and high speed links | TROSKA, Jan |
| 17:30 | [27] Discussion | |