



HL-LHC Crab-Cavities Low Level RF (LLRF)

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Overview

- 1) LLRF Overview
- 2) Infrastructure
- 3) Architecture
- 4) LLRF Cavity Controller
- 5) RF noise & CC noise feedback
- 7) RF conditioning
- 9) Conclusion

HL-LHC LLRF overview

Great challenges for LHC LLRF systems

- Synchronize a distributed RF system in multiple points
 - Point 4 Surface (SR4): Beam-Control¹, ADT
 - Point 4 underground (UX45): ACS Cavities
 - Point 1 underground (ATLAS) : RFD CC
 - Point 5 underground (CMS) : DQW CC
- Very low CC RF noise to limit EGR
- RF phase stability to keep the luminosity
- Crabbing voltage closure for machine protection
- Large and distributed LLRF infrastructure

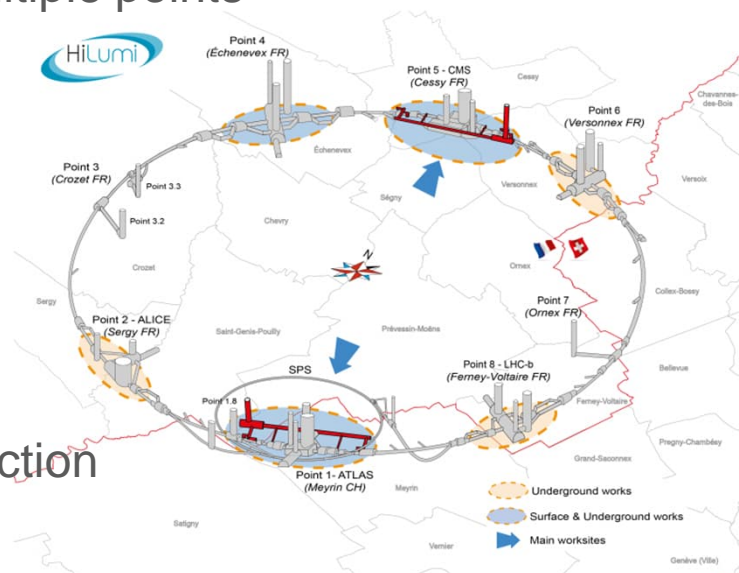


Fig – HL-LHC layout [2]

LLRF Infrastructure (1/2)

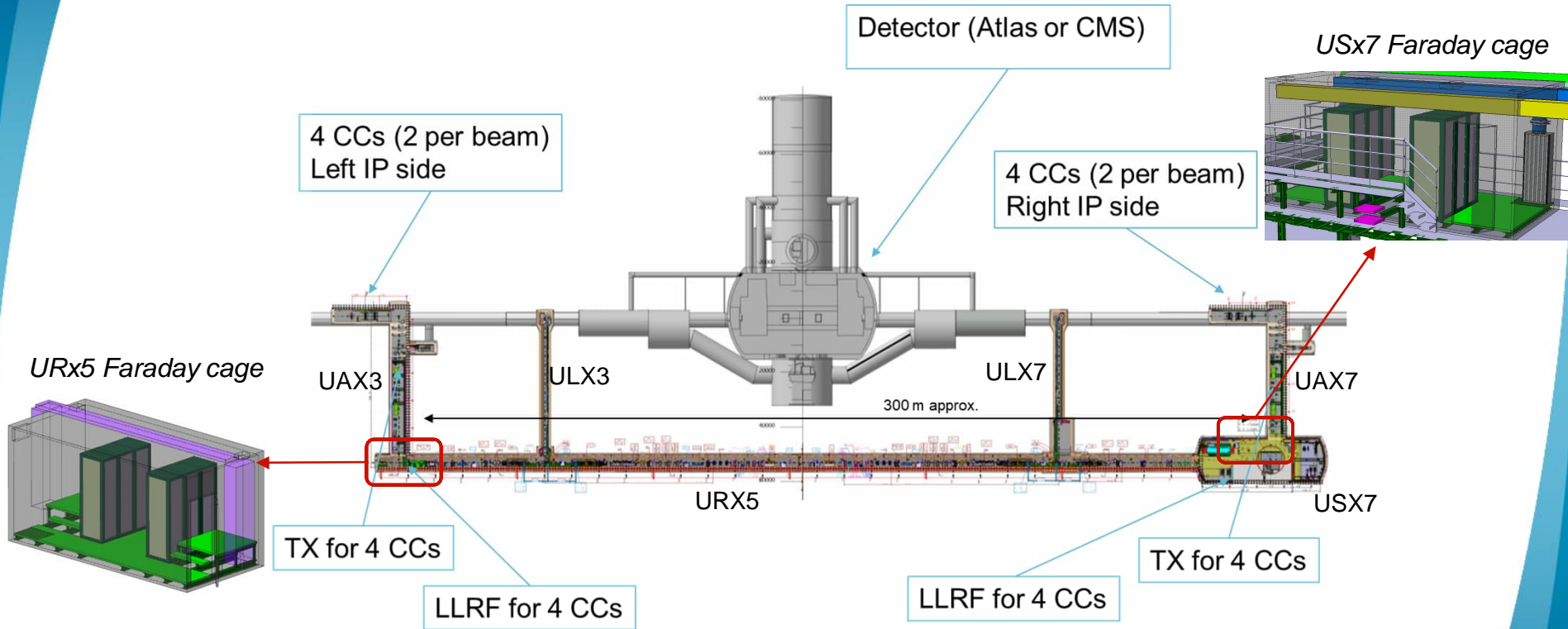


Fig – HL-LHC point 1 or 5 underground layout. Top view,
Courtesy S. Maridor

LLRF Infrastructure (2/2)

- RF integration being finalized
 - FC & Cabling layout finalized
 - Technical details Q4 2024
- Four Faraday-cages
 - Technical spec by Oct 2024, Ordering Q1 2025
- Cabling & Fibers
 - Requests & routing finalized (682 cables / ~100 fibers)
- Cable temperature sensitivity ($\Delta T=8K$, 7/8")
 - Phase shift of ~ 3.1 RFdeg, Budget 2 RFdeg
 - **Beam phase tracking**
- Joint GMT & RF White-Rabbit network

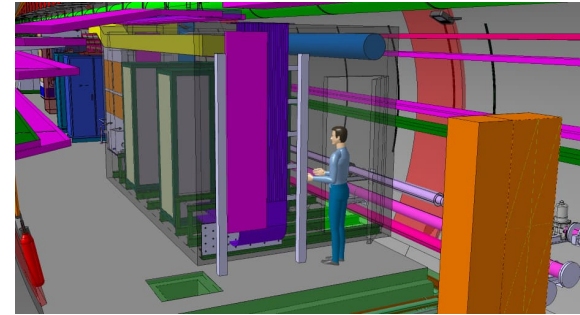


Fig – HL-LHC URx5 Faraday cage, Courtesy S. Maridor

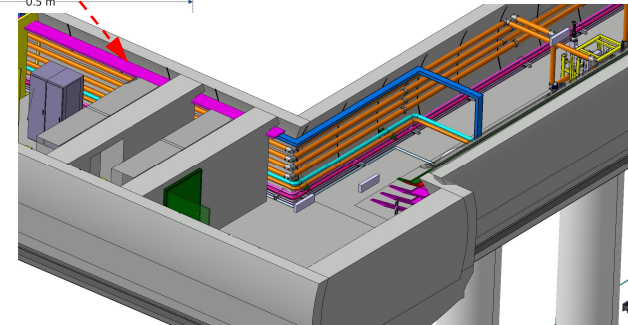
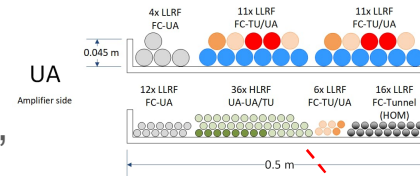


Fig – HL-LHC US galleries, Courtesy L. Ciampo

LLRF architecture

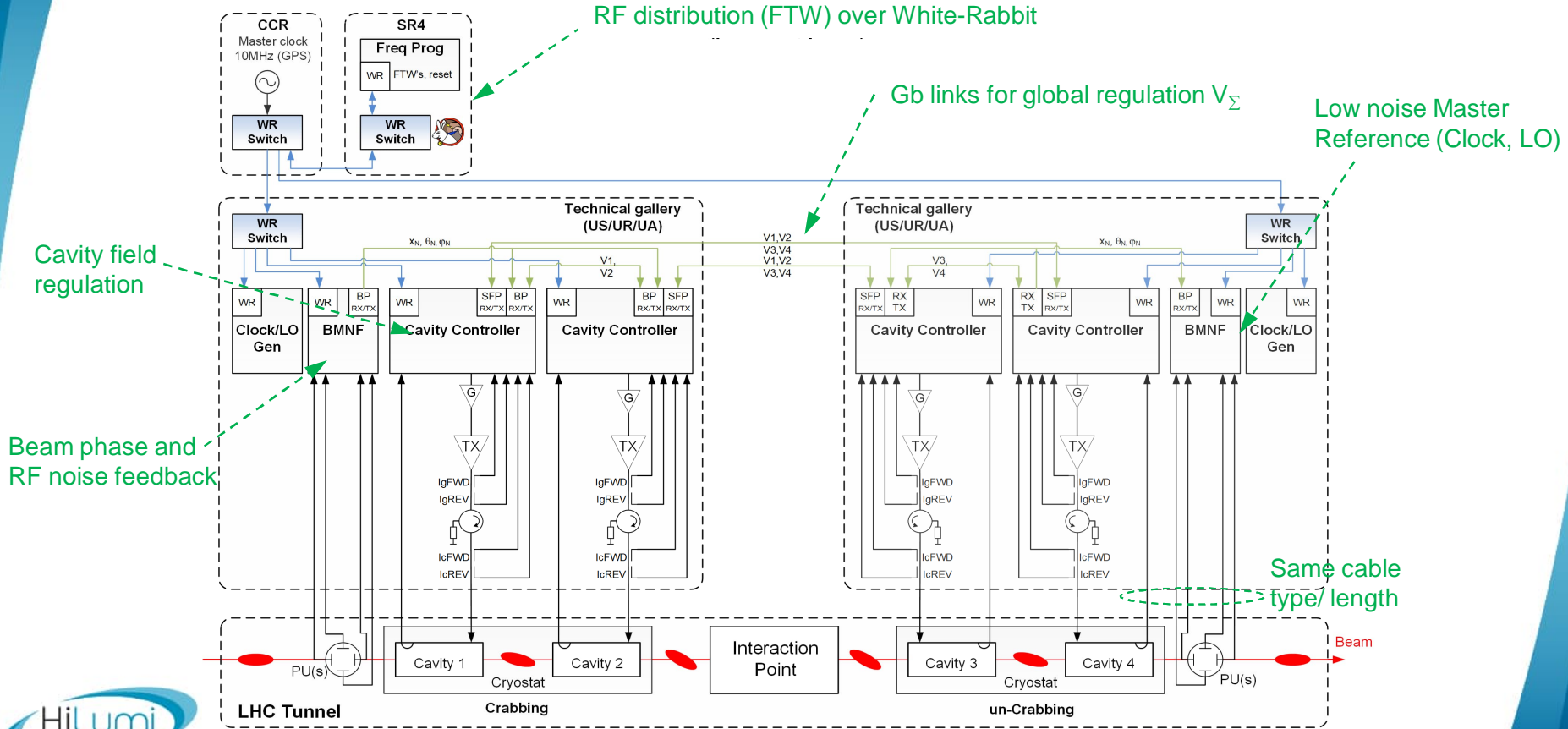


Fig – HL-LHC Crab cavities LLRF architecture, per IP, per beam

LLRF architecture - Hardware

- Inspired by SPS LLRF upgrade
- White-Rabbit network
 - RF over WR
 - Master REF locked on WR
- MTCA platform
- Cavity-Controller
 - RF feedback (direct + Comb)
 - Global control for crabbing closure
- Beam Measurement
 - Bunch phase & tilt
 - Crab-cavity noise feedback
- COTS and in-house designs (baseline scenario)

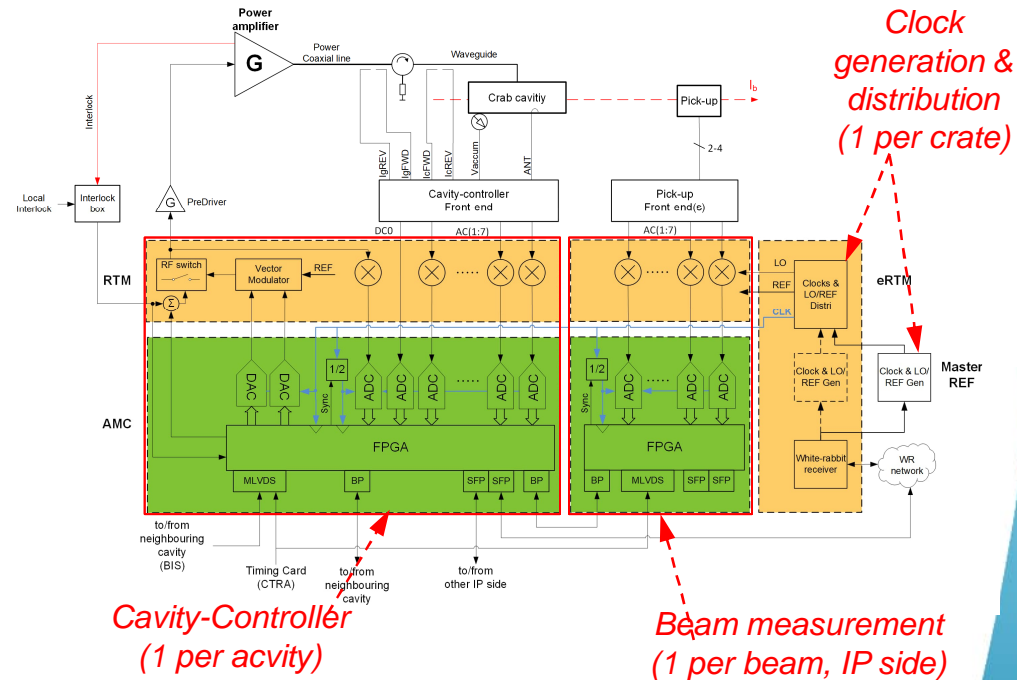


Fig – HL-LHC Crab cavities controller HW diagram

LLRF Cavity-Controller

The Cavity-Controller is in charge of

- Self-Excitation Loop (SEL)
- Tuning loop
 - keep the cavity on-tune the entire LHC fill (filling/ramping/collision)
- Polar-loop
 - Slow regulation around the amplifier (Gain&phase drift, reduce amplifier noise)
- RF feedback
 - Control cavity field + Impedance reduction
 - Fast loop around cavity-amplifier
 - Slow global loop regulating the vector sum: crabbing-uncrabbing voltages
- RF conditioning

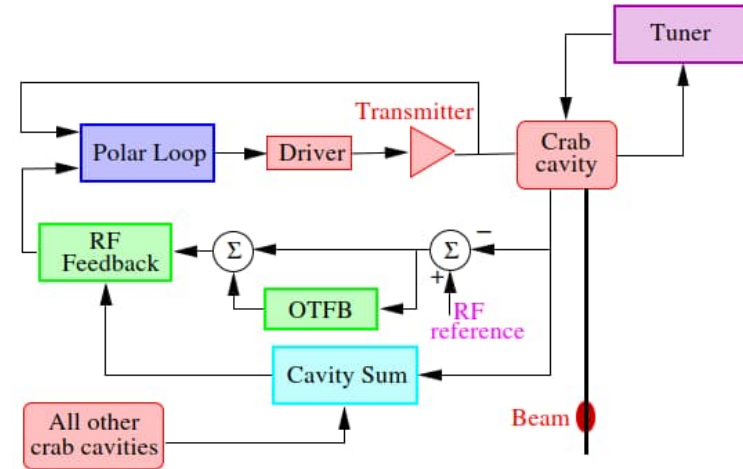


Fig – Crab Cavity Low-Level RF block diagram [11]

LLRF Cavity-Controller

- One Cavity-Controller per Cavity → 16 total
 - Pair of AMC and RTM cards:
 - AMC: Digital processing (ADC, DAC; FPGA)
 - RTM: Analog processing (RF mixer, etc.)
- Hardware currently under evaluation in lab
 - RF receiver noise is the most critical issue
 - Baseline scenario: COTS similar to SPS 200MHz
 - Reusing same AMC from SPS LLRF
 - **New RTM now supported** (COTS, RF mixer)
 - Additional FPGA Firmware+Gateway developed in 2024
 - Detailed noise measurement on-going → Q4 2024

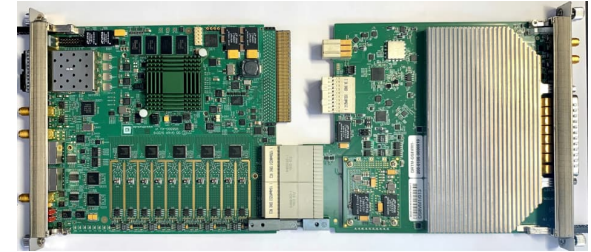


Fig – Example of the SPS Cavity-Controller, AMC (left), RTM (right) [20]



Fig – LLRF lab test

LLRF Cavity-Controller

- Scenarios for MTCA development
 - **A) Baseline:** COTS similar to SPS
 - **B)** If required study possibility for HW modifications with manufacturer (only if minimal)
 - **C)** Else evaluation of different COTS or **D)** in-house design → new cost and resources evaluation will be required.
 - **Strategy:**
 - (in)validation of the baseline scenario by Q4 2024
 - pre-series only with HL-LHC performances achieved
- Integration in SM18 or SPS (BA6)
 - With baseline scenario A), feasible end 2025
 - With B) tentative in 2026
 - With C) or D) late 2026 or beyond

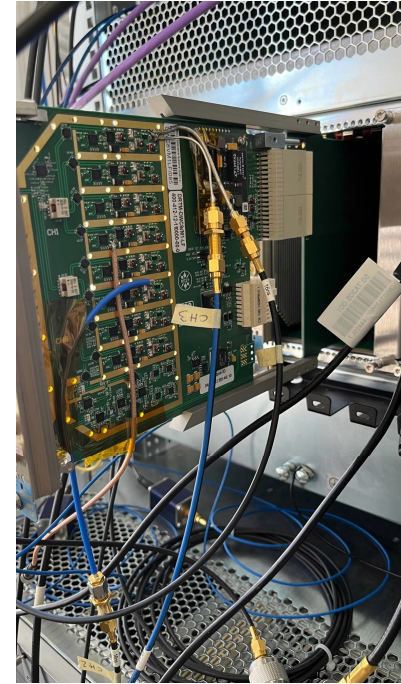


Fig – LLRF lab test stand for HL-LHC LLRF Cavity-Controller [20]

LLRF Cavity-Controller: RF Feedback ♣

- Direct feedback (proportional)
 - Gain of 150 (43 dB) limited by the latency ($<1.3\mu\text{s}$).
- Betatron comb filter (OTFB)
 - The cavity impedance requires evaluating it only a discrete set of frequencies \rightarrow betatron frequencies [5].
 - Adding narrow band gain peaks of 10 (20dB) at these frequencies with the betatron comb filter [11].

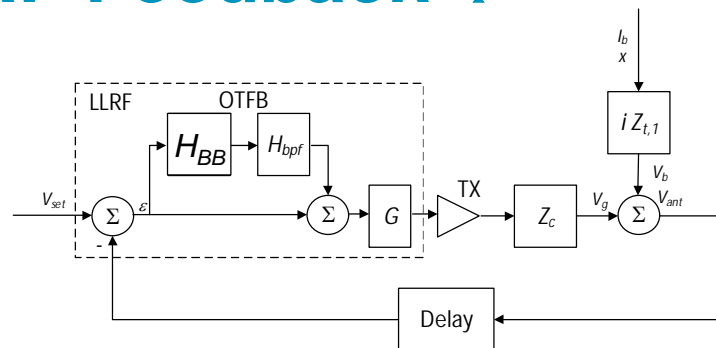


Fig – RF feedback and OTFB block diagram [11]

$$f_p^s = (p + \nu_*)f_0 \quad f_p^d = (p + (1 - \nu_*))f_0, \quad \forall p \in \mathbb{N}$$

$$H_{BB}(\omega) = K(1-a) \left[\frac{e^{i2\pi Q} e^{-i T_{rev} \omega}}{1 - a e^{i2\pi Q} e^{-i T_{rev} \omega}} + \frac{e^{-i2\pi Q} e^{-i T_{rev} \omega}}{1 - a e^{-i2\pi Q} e^{-i T_{rev} \omega}} \right]$$

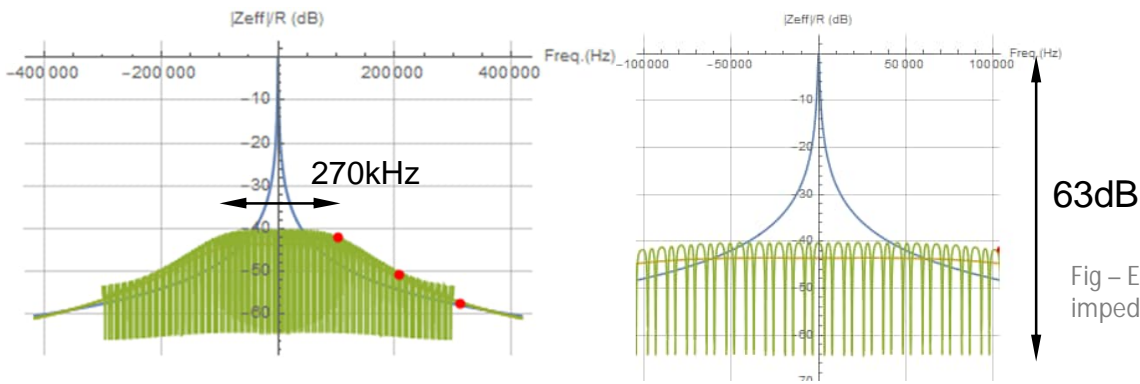


Fig – Effective transverse cavity impedance with and w/o feedback [11]

Cavity-Controller: RF Feedback ♣ [11]

- The optimal OTFB gain is inversely proportional to the bandwidth.

$$G \simeq \frac{f_{rev}}{6 \pi \Delta f}$$

- For the nominal OTFB gain of 10 linear, the 3dB bandwidth would be 50 Hz.
- This bandwidth has to cover the tune spread and tune shifts.
- Any required increase in bandwidth would come at the expense of impedance reduction, as shown below → **keep highest gain.**

- Implementation plan:
 - Design based on SPS LLRF
 - Only on the working pre-series (MTCA platform)
 - Tentative end 2025 for baseline scenario (A)

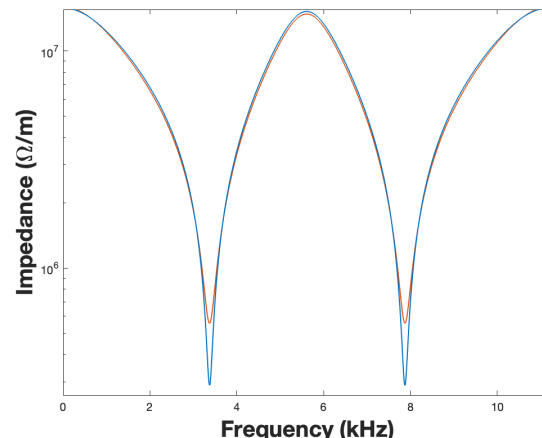


Fig – Impedance reduction at the betatron frequencies wrt to comb bandwidth, Courtesy T. Mastoridis

RF noise: Recap (1/2) ♣

- The CC deflecting field will suffer from small phase and amplitude fluctuations (PM and AM) called RF noise
- The RF noise depends on the LLRF and HLRF architecture
- The RF noise will therefore be dominated by the LLRF receiver noise (demodulation, ADC) of the CC voltage
 - Identical PM and AM noise spectra are expected identical (I/Q demodulation).
- The CC LLRF includes a strong RF feedback [11] to reduce the cavity impedance at fundamental, required to prevent CBI¹ [5]
 - Expecting flat RF noise spectrum from ~1 kHz to the RF feedback BW (136 kHz) [11]

RF noise: Recap (2/2) ♣

- Emittance growth is due to the excitation that overlaps the betatron tunespread (red asterisks)
- With a realistic improvements planned for the CC LLRF compared to the ACS system:
 - noise plateau at $5 \cdot 10^{-3} (\mu\text{ rad})^2/\text{Hz}$, that is $-143 \text{ dBc}/\text{Hz}$
- Using analytical expressions [6] we can calculate the emittance growth, in physics conditions (at lowest $\beta^*=15 \text{ cm}$), caused by the red noise spectrum. We assume $\beta_{\text{CC}}=3620 \text{ m}$
 - 23.7 %/h EGR due to PM and 9.0 %/h due to AM with the four cavities per beam and plane [7]
 - Budget is 1% integrated luminosity loss caused by CCs. That corresponds to 2 %/h EGR at flat top and lowest β^* [14]
- We must therefore **find a mitigation to further reduce the emittance growth by factor 25 for phase and 10 for amplitude**

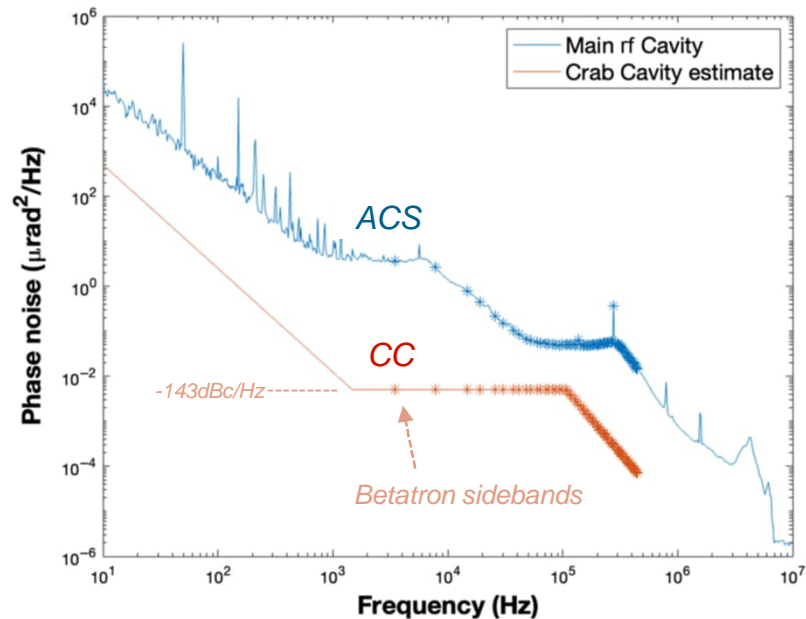


Fig – LHC ACS cavities and Crab-cavities SSB phase noise [11]

RF noise feedback (1/3) ♣

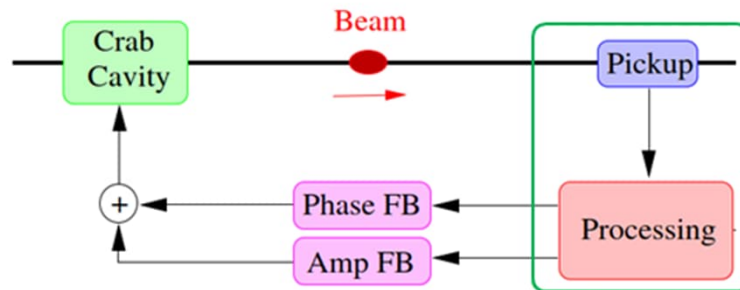


Fig – Crab-cavity noise feedback diagram [10]

- Emittance growth caused by crab cavity RF noise is a two-step process:
 - 1) Noise excites a bunch oscillation.
 - 2) This oscillation results in emittance growth through decoherence due to the betatron tunespread
- A feedback system can mitigate this degradation:
 - Damping the oscillation before decoherence has significantly impacted the emittance
 - Using Crab cavities as kickers to mitigate rf noise effects
 - Extracting both the dipole (mode 0) and head-tail (mode 1) signals from Pickup + LLRF
 - Feeding back signals on the CC phase and the head-tail (tilt) signal to modulate the CC amplitude in quadrature with the measurement, thereby achieving damping [7].

RF noise feedback (2/3) ♣

- Feedback limitations:
 - Gain α_0 limited by the latency (processing) (expecting <5 turns, $\alpha_0 < 5.3$)
 - Measurement noise
- Requirements emittance growth reduction:
 - $R_0 = 1/25$ for phase
 - $R_1 = 1/10$ for amplitude
- The intersection with an analytical curve indicates the maximum measurement noise over RF noise ratio $\sigma_0/\sigma_{\Delta\phi}$ ($R_0 = 1/25$).
- With lower measurement noise level (yellow curve), we can get higher reduction ($R_0 = 0.03$, mode 0 case plotted here) with same gain.

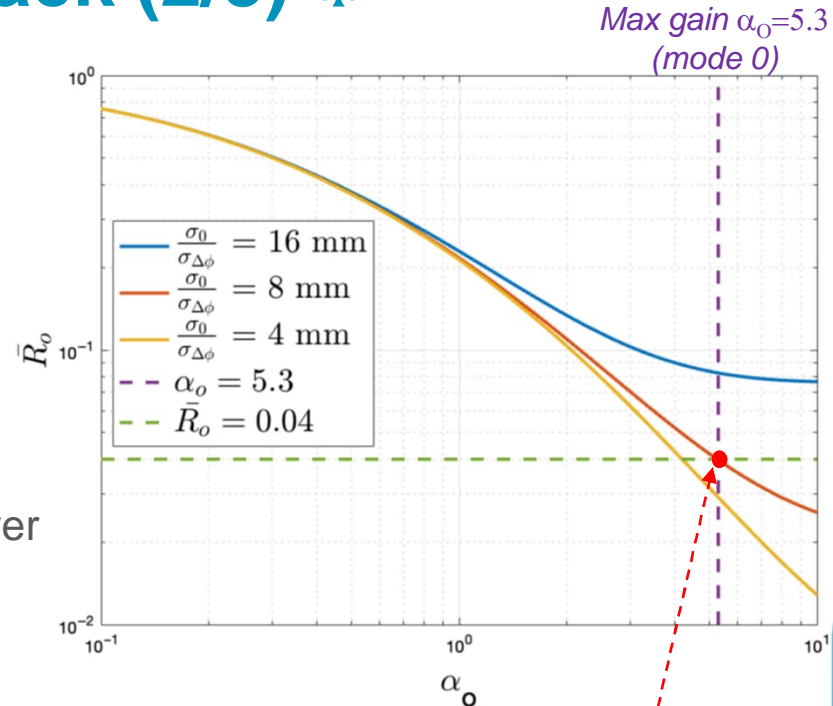


Fig – Emittance growth reduction factor with varying mode 0 measurement error to phase noise ratios. HL-LHC case ($N_{cc} = 4$) [7]. s

Max allowed
measurement noise

RF noise feedback (3/3) ♣

- The acceptable RF noise levels are now defined, setting the LLRF specifications [7].
- To achieve the luminosity goal (reduction of total e-growth to 2 %/h), a dedicated noise feedback is required.
- The noise feedback specifications call for a pickup single bunch measurement noise rms below $\sigma_0 < 320$ nm and $\sigma_1 < 8.3$ μ rad [7] → **Challenging!**
- Fortunately, the CC RF noise spectrum is limited to 136 kHz, so we could average over multiple bunches.
- The requirement for bunch-per-bunch measurement can be relaxed by 12 (linear) to **$\sigma_0 < 3.9$ μ m rms and $\sigma_1 < 100$ μ rad rms** [7], assuming:
 - 25 ns bunch spacing
 - independent measurement noise from bunch to bunch
 - Averaging the measurements over 144 bunches (approx.) = 3.6 μ s

RF Noise feedback – Design status ♠

- Beam interaction side – PICKUP
 - Conceptual specification of the pickup now finalized [18]
 - Joint Pickup analysis and simulation between BI and RF (set of position and angular displacements) → almost completed
 - Existing LHC Pickup measurement foreseen end run 2024 → validation of BI simulation and RF analytic model
 - Preliminary BI & RF analysis indicates the need of the stripline pickup for crab-cavity noise feedback.
- LLRF Hardware - Signal processing
 - Analysis of expected signal at LLRF input finalized (Pickup, Cables)
 - Further analysis with real RF device parameters (cables, hybrids, etc) required
 - Analysis on-going of the beam motion sensitivity to be measured (mode 0 and mode 1). Current models confirm it will be very challenging to measure (very carefully electronics design and cabling required)
 - Similar hardware architecture with MTCA AMC+RTM
 - AMC: On-going analysis with available digitizers (COTS from SPS LLRF)
 - RTM: In-house design required → design starting Q4 2024, for tests in SPS in 2025

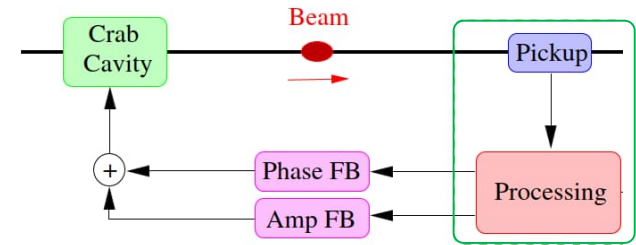


Fig – Crab-cavity noise feedback diagram [10]

Clock generation & distribution

- Clocks generation synchronized on WR
 - Fixed frequency sampling & processing clocks for noise optimization (thanks to SPS experience)
- Low noise Master Reference
 - Generates clocks and LO for RF receivers
 - New standalone module (3U, 19")
 - Based on very low noise OCXO, locked on WR
 - Development outsourced to industry
- enhanced RTM (eRTM)
 - Distributes clocks and LO in the MTCA shelf
 - Control the Master Reference (phase lock)
 - Collaboration with BE-CEM for Développement (eRTM15)
- Design schedule (Master REF, eRTM)
 - Specification by Q1 2025
 - Prototyping end 2025

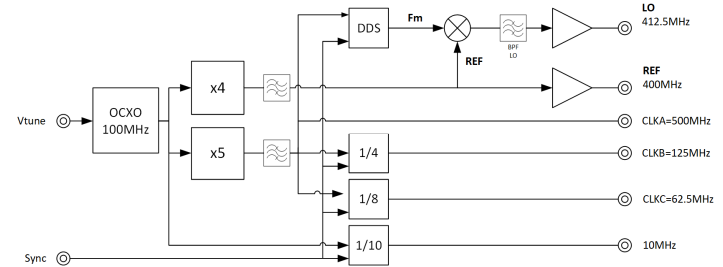


Fig – Master Reference block diagram



Fig – SPS eRTM (dual module eRTM14, eRTM15)

RF conditioning

- Obsolescence of the current RF conditioning system
 - SPS/CC: Limited nb of systems (2-3)
 - LHC: ~20years old, VME
- Migration of the RF conditioning inside the LLRF
 - Same HW than the Cavity-Controller
 - New FPGA firmware (IP core) being developed (GRAD), → by Q2 2025
 - Inspired from SPS and LHC algorithms
- Vacuum measurement required by the LLRF
 - Dedicated fiber optic link (from UAs to UR/US)
 - New HW interface being developed (GRAD) → series by Q4 2025
- Migration plan
 - Only on the working pre-series (MTCA platform)
 - late 2025 for the baseline scenario (A)

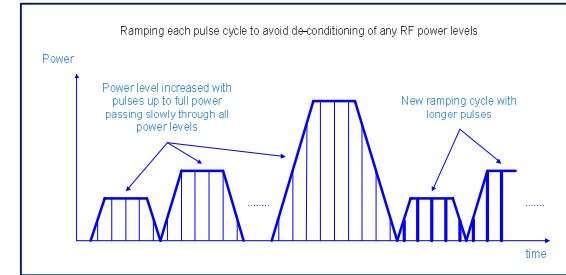


Fig – SPS Conditioning in pulse mode Courtesy E. Montesinos [12]

Conclusion

- LLRF Architecture is now well defined
 - White-Rabbit joint network with timing (GMT)
 - MTCA platform
 - Mixed of COTS and in-house designs, outsourcing is optimized
- Infrastructure is on track and well defined
- Challenging RF noise requirement
 - Now well defined specification available [7] → challenge for LLRF electronics
 - Crab-cavity noise feedback required
 - Hardware evaluation on going
- Strategy: priority on the development of HL-LHC LLRF hardware
 - MTCA Pre-series only with HL-LHC spec
 - Comb filter, RF conditioning on MTCA platform

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LLRF architecture - White-Rabbit network

- Joint WR network for GMT and RF validated
 - Design finalized
 - Max 6 layers of White-Rabbit Switches (WRS)
- RF-train injected on level 1
 - Available everywhere around LHC
- WR2RF module for RF users
 - SPS version qualification [19]
 - LHC Prototyping (400MHz)
 - Q1 2024

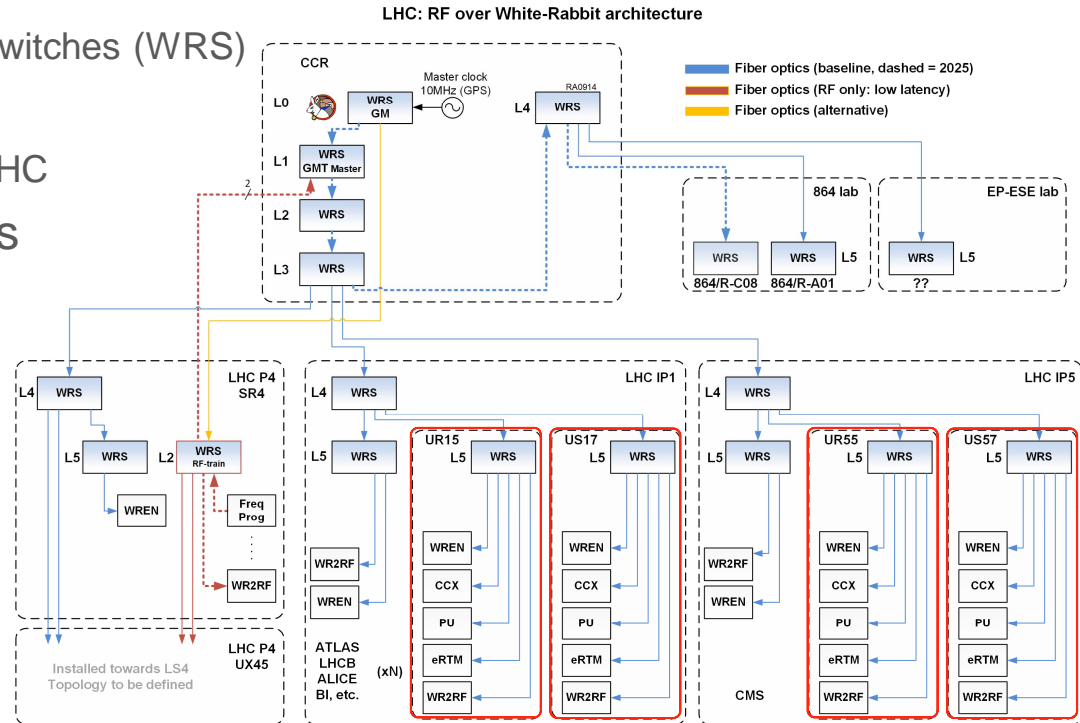


Fig – LHC RF-train White-Rabbit architecture

LLRF phase stability & Crabbing closure

- Coherent phase shift
 - Dominated by the full-detuning of ACS
 - ~ same transverse offset for both beams
 - **<100ps phase error** (14.4°) [1] (-2% peak luminosity)
- Incoherent phase shift (Static phase offset or drifts)
 - Different transverse offset for both beams
 - **<15ps phase error** (2° RF) [1] (-1% peak luminosity)
 - Beam phase tracking required (differential measurement)
- Precise crabbing-uncrabbing voltage (V_Σ)
 - Global control (slow MIMO fdbk) to be strictly limited crabbing around the IP
 - compensate a single-cavity failure) for at least 3 turns (~beam dump) in the other cavities
 - Design at the conceptual stage [12]

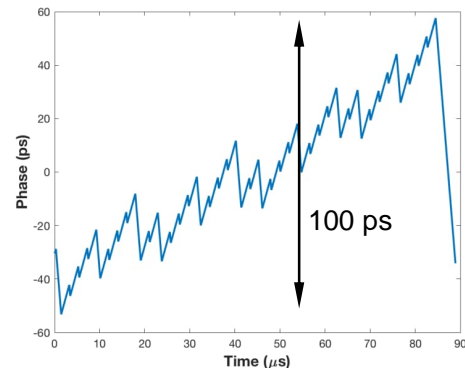


Fig – Phase modulation of LHC ACS cavities along the batch for $2.2 \cdot 10^{11}$ [9]

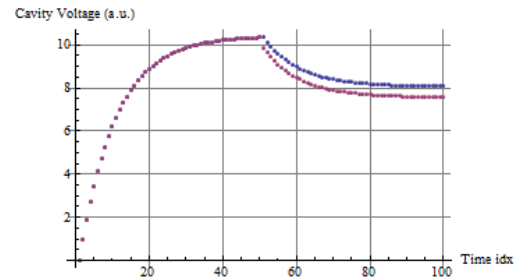


Fig – Response to a cavity 2 voltage drop, with coupling (right) [12]