

The Entanglement Fabric

Enabling distributed quantum computing with quantum networks

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Building the networking infrastructure for quantum computing scale-out





Building the networking infrastructure for quantum computing scale-out

Founding year 2018	Pre-A funding £8.5M
People	Nationalities
~60 FTE	20
Female	LGBTQ
40%	10%
Tech disciplines	PhDs
9	28
Locations Cambridge, UK Harwell, UK Los Angeles, USA	





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At QT4HEP



Simone Eizagirre Product Manager





Ed Wood **VP** Product



Romerson Oliveira Senior FPGA Engineer



Quantum computing represents an exponential increase in computing power to address some of the world's biggest challenges

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1.3 trillion in value by 2035

Reference McKinsey

Life Sciences	Transport Market	Energy Market
\$20B	\$21B	\$30B





A Scaling Challenge: the 'monolithic' approach to scaling qubit numbers is hitting a wall



IBM	Pasqal	IonQ	C	uantinuum		QuEra
OQC	Atom Computing		1	Google	F	Rigetti



Network-enabled scale-out accelerates the timeline to larger quantum computers

















Can we build the networking hardware to entangle processors together?





Can quantum error-correction codes be implemented on a distributed machine?

Can we build the networking hardware to entangle processors together?















Optical microcavity QPI

- Enhances qubit transition
- Boosts emission rates
- Emission into cavity mode







FIGURES OF MERIT

- Probability of photon extraction into cavity mode
- Probability of photon collection into fibre

TECHNICAL REQUIREMENTS

- Ultra smooth cavity mirrors (<nm)
- High-precision cavity locking
- Photon collection optics
- Ultra-high vacuum compatibility

The Qubit-Photon Interface: in-house proprietary design and fabrication



Cavity finesse 200,000

Cavity locking resonance tunability 1 MHz- 6 GHz



Cavity length locking precision 80 pm



The Qubit-Photon Interface: adaptable to different qubit types



QPI for neutral atoms In collaboration with Inflegtion Infleqtion



QPI for trapped ions

In collaboration with NQCC National Quantum Computing Centre

Can we build the networking hardware to entangle processors together?



The Quantum Networking Unit

hosts photonic technology to distribute entanglement across processors





delivering a world-first Quantum Networking Unit product prototype





Innovate UK SBRI contract

"from the lab to the data centre"

- scalable
- industrialised
- modular

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delivering a world-first Quantum Networking Unit product prototype



CISCO Prospective end-user

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delivering a world-first Quantum Networking Unit product prototype



The Quantum Networking Unit: control and orchestration

Control Module



TECHNICAL REQUIREMENTS

- Low latency
- Scalability
- Fast feedforward
- Distribution of precise timing

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CURRENT APPROACH

- ARTIQ control system
 - Developed for ion traps
 - \circ FPGA-based
 - 8 ns time resolution
 - 250 ns fast feedforward
- White Rabbit Technology
 - Ethernet for timing distribution
 - Sub-nanosecond accuracy
 - Scalable (1000+ nodes)



The Quantum Networking Unit: switching light to weave entanglement

Optical Module

QUANTUM



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The Quantum Networking Unit: switching light to weave entanglement



Optical

Module





- Gen1: commercial off-the-shelf components (COTS)
- Limited entanglement fidelity (97-8%)

The Quantum Networking Unit: switching light to weave entanglement

Optical Module



integrated photonics 99.5% E.F.

97% E.F.

The Quantum Networking Unit: entangling quantum processors





- Ion QC emulator
- Benchmarking capability
- Upgradeability
- Next: integration with real ion nodes in Nu Quantum networking testbed

Can quantum error-correction codes be implemented on a distributed machine?



Building a modular fault-tolerant quantum computer

- Computing operations are susceptible to hardware and environmental noise
- Quantum Error Correction (QEC) enables the creation of logical qubits out of multiple physical qubits
- Are QEC codes compatible with a modular architecture?







Distributed Quantum Error Correction: a pathway to modular fault-tolerant QCs



Distributed Quantum Error Correction: a pathway to modular fault-tolerant QCs



Logical qubits formed by physical qubits in different processors



Distributed Quantum Error Correction: a pathway to modular fault-tolerant QCs





Logical qubits formed by physical qubits in different processors Distributed architecture unlocks application of new families of QEC codes



What's next?





Pioneering an optically connected multi-node system at the NQCC







Integrating our solutions with real quantum computers

Thank you to everyone at Nu Quantum & shout-out to CERN!







White Rabbit Collaboration, December 2024 (image:CERN)

Thank you for your attention and #StayEntangled!

Interested in collaborating?

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We're hiring! AMO Engineer Lab Software Engineer



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