



Status of defect investigations in Si and SiC

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p-type Silicon diodes (Boron doped)

Motivation&*Aim*: Large spread in measured g_B making hard to develop reliable parametrization models to describe the radiation damage from low to high and extreme fluences - *understand the Acceptor Removal Process (Boron removal rate* g_B and change in N_{eff}) in connection with impurity content



$$N_{eff}(\Phi_{eq}) = g_{eff} \times \Phi_{eq} + N_{A,0} \times \exp(-g_B \times \Phi_{eq}/N_{A,0})$$

Possible causes:

- Different impurity content (e.g. C, O but also B if P exist in the samples)
- Metastable defect states
- New defects at high fluences (2nd order) undetected so far

Focus: on defects that can influence g_B and N_{eff}

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Experimental data compared to the parametrization model Ferrero et al -<u>https://doi.org/10.1016/j.nima.2018.11.121</u>

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p-type Silicon diodes (Boron doped)

Defects that can be charged at room temperature (RT) of the sensors

Typical TSC spectra in PiN and LGAD diodes (filling of traps at 10K)



At RT donors with levels in the upper part of the bandgap are positively charged, acceptors with energy levels in the lower part of the gap are negatively charged, *both* showing the Poole Frenkel effect (their emission rate ~ exp (const. $F^{1/2}$) and have a direct impact on the device electrical properties¹⁻⁴





Causes up to 70% variations variations in the calculated g_B in high resistivity diodes

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- 3. C. Besleaga et al, Nucl. Instrum.Methods Phys. Res. A, Accel. Spectrom. Detect. Assoc. Equip., 1017, 165809 (2021)
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p-type Silicon diodes (Boron doped)

The X defect

A hole trap detected first by TSC in low resistivity diodes (50 Ω cm)^{1,5}



Temperature dependent capture cross section for holes (defect clearly detected when $T_{fill} > 30 \text{ K}$)



10K

20K

30K

40K

50K

- 60K

65K

4







- E_a and σ_p similar to that of donor state of divacancy $V_2^{+/0}$
- Only in low resistivity diodes the defect manifest a strong field enhanced emission

Potential barrier for the emission of an electron from a deep-level defect in external electric field for: a) charged impurities and b) neutral impurities.



The electric field effect on the emission rate of the $V_2^{(+/0)}$ fitted to the model for phonon assisted tunnelling for electric fields F > 60 kV/cm 10^{3} EMISSION RATE (s⁻¹)

 $e_n = A \exp(F^2/E_c^2)$ (Phonon assisted tunneling model) 0.4 0.6 0.8

X defect can be associated with the donor state of divacancy $V_2^{(+/0)}$

ELECTRIC FIELD (V/cm)

1.0

x10⁵



p-type Silicon diodes (Boron doped)

Carbon implanted diodes from the EXFLU1 batch

| Wafer # | Thickness | C shield |
|---------|-----------|----------|
| 1 | 45 | |
| 3 | 45 | 0.6 |
| 4 | 45 | 1.0 |





PIN sensors from the EXFLU1 batch: W1, W3, W4 900 Ω cm EPI thickness is 45 um – on 525 μ m CZ substrates

Carbon spray deep implant – dose given in unit of FBK C dose

Measured before and after irradiation with $8x10^{14}$ n/cm² Available also 1.5E15 and 2.5E15 n_{eq}/cm² Annealing at 60°C for 80 minutes performed on the irradiated sensors

Carbon implanted diodes from the EXFLU1 batch



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As processed diodes

| Wafer # | Thickness | C shield |
|---------|-----------|----------|
| 1 | 45 | |
| 3 | 45 | 0.6 |
| 4 | 45 | 1.0 |

DLTS spectra

(injection successful only with Fw biasing)



- A distribution of defects rather than point defects (most likely related to surface)
- For C implanted diodes (W3 and W4) the distribution moves to deeper energies, more defect states in W4 than in W3

Strange C-Vs, I-Vs and large differences between the the as-processed samples

p-type Silicon diodes (Boron doped)

DRD3 Carbon implanted diodes from the EXFLU1 batch – 8x10¹⁴ n/cm²



DRD3 n-type Silicon Carbide diodes (Nitrogen doped)

RD50 Common Project 4H-SiC LGADs, Planar Pad Diode Run & Status of Irradiation Campaign at CERN

- CNM Run 17047 W2
- n-type epitaxial 4H-SiC pad diodes
- 50µm EPI layer
- $3x3mm^2$ area
- Outflow of Aluminium during processing →Broken guard ring structure
- More details in talk by Andreas Gsponer at 1st DRD3
- Irradiation campaign by SSD group at CERN
 - Irradiate with Protons, Neutrons and Gammas
 - Compare damage of different particle types
- Status of this project
 - Measured all diodes unirradiated (due to differences in processing)
 - Diodes send out to facilities, awaiting their return

All results shown in the following are from **unirradiated diodes from the same wafer!**











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Measurements on unirradiated diodes



²nd DRD3 Workshop, Geneve, 2-6 December 2024



DLTS measurements on unirradiated diodes



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W2 (17047, 50 um)





Z_{1,2} defects

electron traps with 2 energy levels in the bandga, exhibiting negative U behavior (capture/emission of the 2nd electron takes place faster than of the 1st electron)^{7,8}
associated initially with a donor complex with Nitrogen dopant and interstitis Carbon on (h) and (k) sites ^{8,9} and lately with single and double acceptor levels of V_C (h)&V_C (k) (Carbon vacancy on hexagonal and cubic sites)¹⁰

Peaks 5 and 6

- Related to Boron incorporation on Silicon and Carbon sites (unintentional during CVD growth)¹¹⁻¹³

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n-type Silicon Carbide diodes (Nitrogen doped)

DLTS measurements on unirradiated diodes



- Overall similar behaviour
- Same kind of defects observed in all samples with slight variations observed in defect concentration

First tests on high temperature set-up installed at NIMP



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Hall System: n-type silicon (left): resistivity = $6.33 \times 10^{-1} \Omega$ cm, free carrier concentration = 3.33×10^{15} cm⁻³, free carrier mobility = $2960 \text{ cm}^2/\text{Vs}$; WBG semiconductor, In:Ga:Zn oxide, Eg~3eV (right): resistivity = $2.39 \times 10^{-2} \Omega$ cm; free carrier concentration = 6.94×10^{19} cm⁻³; free carrier mobility = $3.77 \text{ cm}^2/\text{Vs}$.







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Future plans

Silicon – start measurements on "Defect engineered diodes mimicking the gain layer in LGADs" RD50 started project (18 differently defect engineered wafers with respect to B, O and C impurities as well as with P in compensated $n^{++}-p^+$ diodes)

P and C implantations in compensated n^{++} - p^+ diodes with 50 nm of oxide



layout design including samples for Hall measurements and diodes with fully transparent electrodes.



Expected results

- Reveal the microscopic radiation induced effects above 10^{15} n_{eq}/cm^2 and identify the reasons for losing the gain in LGADs.
- Reveal the role of O, C and P impurities in low resistivity B doped Si and of defects impacting on the gain layers in LGADs
- Detection and characterization of new defects induced by irradiation above $10^{15} n_{eq}/cm^2$ (e.g. 2^{nd} order defects)
- provide real inputs for modelling the radiation damage above 10^{16} n_{eq}/cm², allowing the development of accurate parametrization models validated on the entire range of fluences, from low to extreme;

Silicon Carbide – irradiate and start comprehensive defect investigations on the already fabricated devices

- n-type epitaxial 4H-SiC pad diodes (CNM Run 17047 W2)
- n-type 4H-SiC Schottky diodes at NIMP (< 15 µm EPI layers)

Expected results

- built up data set on defects induced by irradiation with different type of particles and fluence range from low to extreme
- establish the charge state of the detected defects and their impact on the device performance

Thank You!