



Discussion on CMOS projects

J. Bardot (IPHC – Strasbourg), H. Pernegger (CERN)

and E. Vilella (Uni. Liverpool)







Project proposals



Contact	Group	Project title	Technology	RG	Target application
S. Spannagel	DESY	Octopus - Fine-pitch CMOS pixel sensors with precision timing for vertex detectors at future Lepton-Collider experiments	TPSCo 65 nm	RG1, RG3	FCCee, vertex
D. Contardo	IP2I	IP2I-TPSCo 65nm CMOS with high precision timing	TPSCo 65 nm	RG2, RG3	FCCee, tracker, pre-shower
H. Augustin	Heidelberg	Evaluation of new process option(s) for large fll-factor HVCMOS sensors (Xfab) and Internal Gain Layer for HVCMOS sensors	Xfab/IHP SiGe	RG2	PSI, tracker
P. Schwemling	CEA-IRFU	Cactus: Large electrode designs for timing with and without intrinsic amplification	LF 150 nm	RG2	Tracker
H. Pernegger	CERN	CASSIA - CMOS Active SenSor with Internal Amplification	TJ 180 nm	RG2	Generic R&D
A. Apresyan	FermiLab	Development of Ultra Fast-Time Low Mass Tracking Detectors	US vendor	RG2, RG3	Tracker
C Vernieri	SLAC	MAPS developments at SLAC	TPSCo 65 nm	RG3	FCCee, tracker, calo
Y. Li	IHEP	Development of MAPS using 55nm HVCMOS process for future tracking detectors	SMIC 55 nm	RG3, RG4	HL-LHC, FCCee, tracker
C. Solans	CERN	Radiation hard read-out architectures	TJ 180 nm	RG3, RG4	HL-LHC, tracker
J. Baudot	IPHC - Strasbourg	Versatile CMOS pixel sensor suited for future trackers	TPSCo 65 nm	RG3 (RG4)	Tracker
A. Dierlamm	KIT	Monolithic sensors, fast electronics, and silicon photonics for future tracking detectors	IHP SiGe	RG3 (RG4)	FCCee, tracker
E. Vilella	Liverpool	Thin monolithic High Voltage CMOS sensors with excellent radiation tolerance	LF 150 nm	RG4	HL-LHC, tracker
J. Weingarten	Dortmund	Monolithic strip sensors for large area detectors	LF 150 nm	RG5	Tracker
A. Andreazza	Milano	HV-CMOS Multi-chip integration for large area silicon trackers	180 nm/150 nm	RG5	FCCee, tracker
X. Shi	CAS	CMOS Strip Chip for Future Tracking Detector	CSMC 180 nm	RG5	CEPC, tracker
A. Andreazza	Milano	Arcadia	LF 110 nm		







General observations



- Three technologies are well represented (TPSCo65, LF15A, TJ180),
 there are a few others which sound quite healthy;
 - We expect good synergy between these projects, in particular for TPSCo65, since projects in this techno will share the same submission(s) organised within DRD7;
- Most projects are about trackers (a couple deal about vertex detectors);
- All research goals defined in the roadmap document and in the large table of the proposal are covered;



General observations



- We have projects at different levels of maturity, a few can go to the MOU annexes already;
- We have not checked—and will not check, as it is beyond our available time—that any groups who expressed interest in the WG1 activities in the initial DRD3 surveys are not left behind;
 - But we encourage all those who are not yet in a project and would like to, to contact us or the project contact.





Backup slides







DRD3.1 CMOS – Research goals DRD3



Aim is to advance the performance of monolithic CMOS sensors for future tracking applications, tackling the challenges of

- very high spatial resolution;
- high data rate;
- high radiation tolerance;
- low mass;
- covering large areas;
- reducing power;
- keeping an affordable cost;
- and ultimately combining these requirements in one single sensor device.

WG1 research goals <2027				
	Description			
RG1	Spatial resolution: ≤3 μm position resolution			
RG2	Timing resolution: towards 20 ps timing precision			
RG3	Readout architectures: towards 100 MHz/cm ² , 1 GHz/cm ² with 3D stacked monolithic sensors, and on-chip reconfigurability			
RG4	Radiation tolerance: towards $10^{16} n_{eq}/cm^2 NIEL$ and 500 MRad			
RG5	Low-cost large-area CMOS sensors			

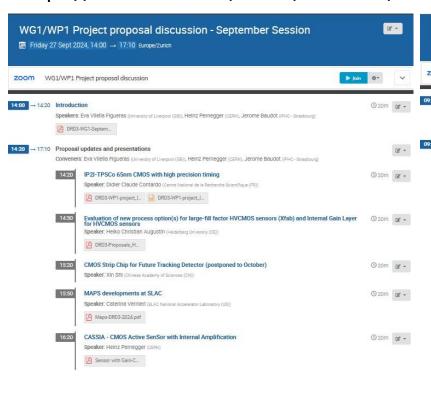




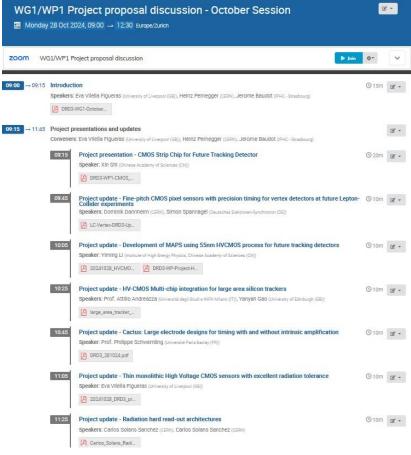
Zoom meetings



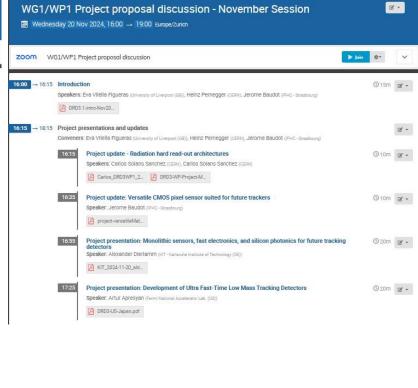
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