Session Program

2-6 Dec 2024

2nd DRD3 week on Solid State Detectors R&D WG1 - CMOS technologies

CERN, 500/1-001 - Main Auditorium

Thursday 5 December

CMOS Strip	
	Sensors - Characterisation, Simulation and Test Beam Results
Speaker Iveta Zatocilov	3
15:20-15:40	
	Active Strip Sensors for Applications in future Tracking detectors &
medical Ima	nging
Speakers	
	el, Ulrich Parzefall
15:40-16:00	The RD50-MPW4 CMOS Pixel Sensor: Performance Post-Irradiation
Speaker	
Bernhard Pilsl	
16:00-16:20	Performance studies of the CE-65v2 MAPS prototype structure
Speaker	
Alessandra Lor	enzetti
16:20-16:50	Coffee Break
16:50-17:10	
	and characterisation of the first monolithic CMOS LGAD implemente
in 110nm	
Speaker	
Mr Umberto Fo	lo
17:10-17:30	H2M - Characterization of a MAPS in a 65 nm CMOS Imaging Proces
Speaker	
Finn Feindt	
Finn Feindt	TPA Laser Experiment on the MPW4 HVCMOS MAPS chip

Friday 6 December

09:00	WG1 - CMOS technologies: WG1 - CMOS technologies II Session Location: CERN, 500/1-001 - Main Auditorium Convener: Heinz Pernegger
	09:00-09:20 CEPC vertex detector R & D status and plan
	Speaker Zhijun Liang
	09:20-09:40 Development of MAPS using 55nm HVCMOS process for future tracking detectors
	Speaker Dr Zhiyu Xiang
	09:40-09:50 Evaluation of Open PDKs and Open Source Design Tools for DMAPS
	Speaker Daniel Muenstermann
	09:50-11:00 Discussion on CMOS projects
12:40	11:00-11:30 Coffee Break