

## **Tuesday 3 December**

09:00

## WG7 - Interconnect: WG7 - Interconnect technologies

Session | Location: CERN, 500/1-001 - Main Auditorium | Conveners: Giovanni Calderini, Fabian Huegging, Dominik Dannheim, Giovanni Calderini

09:00-09:20

SMALLGAD: Capacitively-coupled AC-LGAD pixel detector for particle detection

**Speaker** 

Mathieu Benoit

09:20-09:40

Development of in-house plating and hybridisation technologies for pixel detectors

Speaker

Dr Ahmet Lale

09:40-10:00 Development of a Novel Low-Mass Flip-Chip-Capable Module Flex PCB

Speaker

Julian Weick

10:00-10:20 DRD7.6b - Status and Plans

Speaker

Hoang Vu Nguyen

10:20-10:30 Flip-chip bonding results

Speaker

Mateus Vicente Barreto Pinto

10:30-10:50 WG7 - Discussion on Interconnect Technologies

**Speakers** 

Dominik Dannheim, Fabian Huegging, Fabian Huegging, Giovanni Calderini, Giovanni Calderini

10:50-11:20 Coffee Break

11:20