

Wednesday 5 March

10:45

	Pata Processing for Level 1 Trigger Scouting at CMS using a CXL
-	ce Architecture
Speaker Thomas Owen James	
11:00-11:30	Micron CXL and SSD Solutions
Speaker Luis Ancajas	
11:30-11:45	The Openlab Heterogeneous Architectures Testbed (HAT) Project
Speaker Luca Atzori	
11:45-12:05	Intel Solutions on AI and HPC Convergence
Speaker Jean Laurent Ph	nilinne
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12:05-12:50 12:50-13:10 Evaluating	Lunch break HPC Architectures for Scientific Computing: Performance, Scalabilit
12:05-12:50 12:50-13:10	Lunch break HPC Architectures for Scientific Computing: Performance, Scalabilit Efficiency
12:05-12:50 12:50-13:10 Evaluating and Energy Speaker	Lunch break HPC Architectures for Scientific Computing: Performance, Scalabilit Efficiency
12:05-12:50 12:50-13:10 Evaluating and Energy Speaker Elisabetta Boel	Lunch break HPC Architectures for Scientific Computing: Performance, Scalabilit Efficiency la Benchmarking and Energy Studies with HEPScore
12:05-12:50 12:50-13:10 Evaluating and Energy Speaker Elisabetta Boel 13:10-13:25	Lunch break HPC Architectures for Scientific Computing: Performance, Scalabilit Efficiency la Benchmarking and Energy Studies with HEPScore
12:05-12:50 12:50-13:10 Evaluating and Energy Speaker Elisabetta Boel 13:10-13:25 13:25-13:40 Speaker	Lunch break HPC Architectures for Scientific Computing: Performance, Scalabilit Efficiency la Benchmarking and Energy Studies with HEPScore
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14:25-14:35	Cost Optimization and Sustainability for Public Cloud Provider
Speaker Ricardo Rocha	
14:35-15:05	Coffee Break and Poster Session
15:05-15:20	Kubernetes and Oracle: What Happened in 2024 and What's Next?
Speakers Antonio Nappi,	Miroslav Potocky
15:20-15:35	ORACLE Technologies
Speaker Dr Cris Pedrega	I-Martin

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