Session Program

1–13 Sept 2025

INFIERI 2025

POST MOORE, POST EXASCALE HIGH PERFORMANCE COMPUTING ERA

University of Pisa and INFN Sezione di Pisa

Tuesday 9 September

08:30

POST MOORE, POST EXASCALE HIGH PERFORMANCE COMPUTING ERA
Session | **Location:** University of Pisa and INFN Sezione di Pisa

08:30–09:40

NEW CHALLENGES AND DEVELOPMENTS ON HPC, THE EUROHPC-JU ENTERPRISE

09:40–10:30

DISAGGREGATING SYSTEM ARCHITECTURES (e.g. MEMORY SYSTEMS) FOR FUTURE HPC AND ARTIFICIAL INTELLIGENCE WORKLOAD

Speaker
Robert PATTI

10:30–11:00   **COFFEE/TEA BREAK**

11:00–12:30

INNOVATIVE SUPERCOMPUTING, by Integrations of Simulations/Data/Learning , ON LARGE-SCALE HETEROGENEOUS SYSTEMS

Speaker
Kengo NAKAJIMA

12:30