

## **Session Program**

**2-4 Oct 2024**



## **2nd Computing Challenges workshop (COMCHA), A Coruña**

### ***Session I***

# Wednesday 2 October

17:00

## Session I

Session | Location:

17:20–17:45

### Sustainability of real-time analysis at 5 TB/s data rate

**Speaker**

Volodymyr Svintozelskyi

17:45–18:10

### Allen optimozation

**Speaker**

Sergio Andres Estrada

18:10–18:35

### Porting MADGRAPH to FPGA using High-Level Synthesis (HLS)

**Speaker**

Hector Gutierrez Arance

18:35