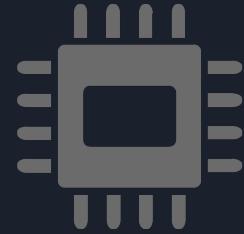




FDF



Seminar Series

News & Introduction

Rui Zou (Cornell University)

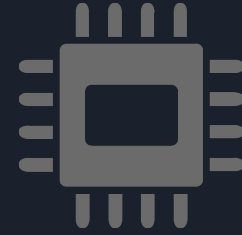
On behalf of FDF Organizing Committee

FPGA Developers' Forum

- Goal: an open community for FPGA developers
- FDF2024:
 - Great participations & feedbacks!
 - Lots of requests for more discussions!
- Discourse Forum:
<https://fpga-developers-forum.web.cern.ch/>
- Newsletter: <https://cern.ch/fdf-news>



FDF 2025 : **May 28-30 2025** @ CERN



FDF 25 will take place in the CERN Main Auditorium on May 28-30 2025

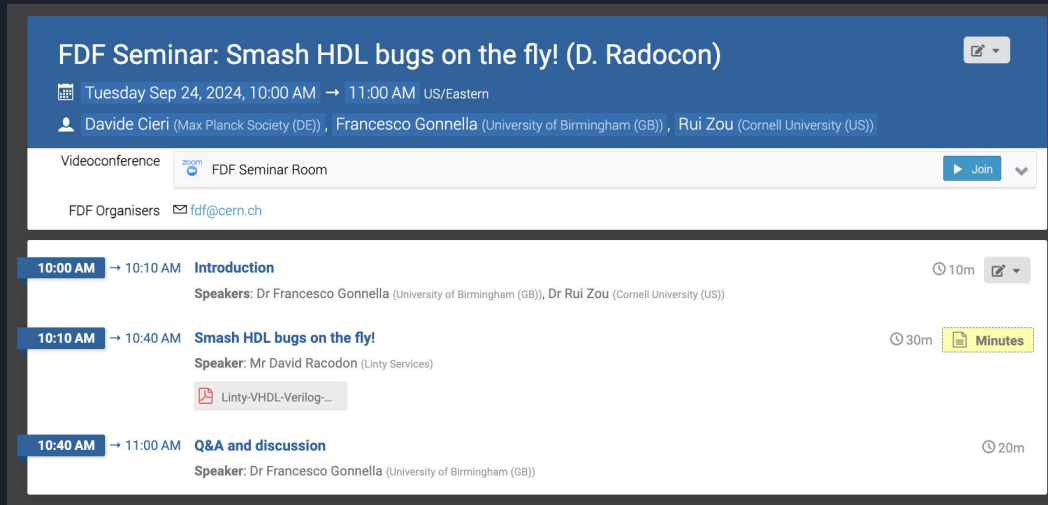
Mark Your Calendar!



Today: First Seminar!

Seminars are of similar spirit as the workshop, but with more details

Friendly discussions highly encouraged!



The screenshot shows a Zoom meeting agenda for 'FDF Seminar: Smash HDL bugs on the fly! (D. Radocon)'. The meeting is scheduled for Tuesday, September 24, 2024, from 10:00 AM to 11:00 AM US/Eastern. The organizers are Davide Cieri (Max Planck Society, DE), Francesco Gonnella (University of Birmingham, GB), and Rui Zou (Cornell University, US). The meeting room is 'FDF Seminar Room'. The agenda includes three items: 'Introduction' (10:00 AM - 10:10 AM, 10m) by Dr Francesco Gonnella and Dr Rui Zou; 'Smash HDL bugs on the fly!' (10:10 AM - 10:40 AM, 30m) by Mr David Racodon, with a 'Minutes' document icon; and 'Q&A and discussion' (10:40 AM - 11:00 AM, 20m) by Dr Francesco Gonnella.

Next seminar: Mattia Barbanera (INFN Perugia). Date TBA. Stay tuned!

The FPGA design for the LO trigger of the RICH detector of the NA62 Experiment at CERN SPS