

# Job TTL Optimization

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# Job distribution in a Grid slot

Single-core analysis Aulti-core simulation jobs



Marta Bertran Ferrer, Whole-node scheduling in the ALICE Grid





# What we have seen

• In certain conditions, jobs finish much faster than the time they asked



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• Jobs will wait for newly created slots to be scheduled on.



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- Heterogenous resources and configurations
  - 55 sites
  - 11000 hosts
  - 15 operating systems
  - 131 CPU Model Names
  - 143 (CPU Model Name, hyperthreading) combinations
  - Different CPU flags
  - 1680 unique combinations of (CPU Model Name, microcode, CPU flags, hyperthreading, OS, RAM, SWAP)
  - + virtualization, security patches, batch queue solution, containerization





# Find the common factors

#### • How to

- Split datasets based on different keys
- Check if the data distribution is normal (Gaussian model)



■ AMD EPYC 7302 16-Core Processor/H-ON ■ Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz/H-ON ■ Intel(R) Xeon(R) CPU E5-2630 v4 @ 2.20GHz/H-ON ■ Intel(R) Xeon(R) CPU E5-2680 v4 @ 2.40GHz/H-ON ■ Intel(R) Xeon(R) CPU E5-2650 v4 @ 2.20GHz/H-ON ■ Intel(R) Xeon(R) Gold 5218 CPU @ 2.30GHz/H-ON ■ Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz/H-OFF ■ Intel(R) Xeon(R) CPU E5-2630 v3 @ 2.40GHz/H-OFF ■ Intel(R) Xeon(R) CPU E5-2640 v4 @ 2.40GHz/H-OFF













### Find the common factors

### • How to

- Split datasets based on different keys
- Check if the data distribution is normal (Gaussian model)
- Check if the values are not spread in a wide interval



AMD EPYC 7282 16-Core Processor
AMD EPYC 7343 32-Core Processor
Intel(R) Xeon(R) CPU E5-2630 v4 @ 2.20GHz
Intel(R) Xeon(R) CPU E5-2660 v3 @ 2.20GHz

Intel(R) Xeon(R) CPU E5-2650 v2 @ 2.60GHz
Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.50GHz
Intel(R) Xeon(R) CPU E5-2650 v4 @ 2.40GHz
Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.50GHz
Intel(R) Xeon(R) CPU E5-2650 v4 @ 2.40GHz
Intel(R) Xeon(R) CPU E5-2680 v4 @ 2.40GHz
Intel(R) Xeon(R) CPU @ 2.40GHz
Intel(R) Xeon(R) CPU





### Find the common factors

### • How to

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AMD EPYC 7282 16-Core Processor
AMD EPYC 7302 16-Core Processor
AMD EPYC 7543 32-Core Processor
Intel(R) Xeon(R) CPU E5-2630 v4 @ 2.20GHz
Intel(R) Xeon(R) CPU E5-2650 v2 @ 2.60GHz
Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.30GHz
Intel(R) Xeon(R) CPU E5-2650 v4 @ 2.20GHz
Intel(R) Xeon(R) CPU E5-2680 v3 @ 2.50GHz

Intel(R) Xeon(R) CPU E5-2680 v4 @ 2.40GHz
Intel(R) Xeon(R) CPU E5-2680



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# Job TTL Optimization

- Predict TTL based on the history of the job duration for the same:
  - Production Id
  - CPU Model Name
  - Site
- Keep history metadata in database TTLPredictionHistoryTable





# Job TTL Optimization in action

- Step 1: Collect history
  - Each job that finishes its execution will update the job history table
  - Compute the predicted TTL in advance
- Step 2: Modify the Job Scheduling process
  - Try default matching option if this doesn't work then
  - Try to alter the TTL based on the job history table
- Step 3: Alter Job's JDL file and send it to the Grid site





# Results

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# Results

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