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Get the right FPGA quality through efficient Requirements Coverage (aka Specification Coverage)

Thursday 22 May 2025 15:10 (40 minutes)

Requirements Tracking is getting more and more attention, and is critical for safety (e.g. DO-254) and mission critical (e.g. ESA space and Avionics) applications.

Unfortunately, this is often handled manually, which is very time-consuming and error-prone. The open Source UVVM's Specification Coverage allows a really efficient collection of predefined requirements, and it generates the reports you need for both mission-critical and safety projects, and in fact for any Project where quality is important.

This presentation gives a brief overview of Specification Coverage before going into more details of proper Requirements Tracking. It also shows what is provided with UVVM and how this could be applied. **UVVM is free and Open Source, and so are all the interface models, randomisation, functional coverage and specification coverage. UVVM has been the fastest growing FPGA verification methdology and Library over the last 4 and 8 years** (cf Wilson Research)

Talk's Q&A

During the talk

Talk duration

25'+12'

Will you be able to present in person?

Yes

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Session Classification: Verification

Track Classification: HDL verification and simulation tools