

Session Program

20-23 May 2025



2nd FPGA Developers' Forum (FDF) meeting

Introduction

CERN, 500/1-001 - Main Auditorium

Tuesday 20 May

14:00

Introduction

Session | **Location:** CERN, 500/1-001 - Main Auditorium | **Convener:** Dr Francesco Gonnella

14:00–14:20

Welcome to the 2nd FPGA Developers' Forum

Speaker

Davide Cieri

14:20–14:40

Welcome to CERN

Speaker

Evangelia Gousiou

14:40–15:10

The future of FPGAs in HEP detectors for FCC and beyond

Speaker

Sophie Baron

15:10