

Session Program

25-28 Nov 2024

Verification with UVM for HEP Workshop
SystemVerilog accelerated verification using
UVM

CERN, 593/R-013

Monday 25 November

09:20

SystemVerilog accelerated verification using UVM

Session | Location: CERN, 593/R-013 | Convener: Luca Brambilla

10:30

10:45

SystemVerilog accelerated verification using UVM

Session | Location: CERN, 593/R-013 | Convener: Luca Brambilla

12:00

13:00

SystemVerilog accelerated verification using UVM

Session | Location: CERN, 593/R-013 | Convener: Luca Brambilla

15:15

15:30

SystemVerilog accelerated verification using UVM

Session | Location: CERN, 593/R-013 | Convener: Luca Brambilla

17:30

Tuesday 26 November

09:00

SystemVerilog accelerated verification using UVM

Session | **Location:** CERN, 593/R-013 | **Convener:** Luca Brambilla

10:30

10:45

SystemVerilog accelerated verification using UVM

Session | **Location:** CERN, 593/R-013 | **Convener:** Luca Brambilla

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SystemVerilog accelerated verification using UVM

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17:30

Wednesday 27 November

09:00

SystemVerilog accelerated verification using UVM

Session | Location: CERN, 593/R-013 | Convener: Luca Brambilla

10:30

10:45

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