



Caribou 2.0 Hardware Meeting

Jan 7th

Things I've done since last meeting

- Met with Eric prior to the break, with Thomas Koffas. Discussed plans for the break and into January. Decided on Ryan to support layout of test board, provide I2C Bus review and future planning for full board and for architecture development of full board
- I2C Bus review for conflicts. A schematic wide review was done and is on page 1 of the schematic, but it needs future planning. The architecture of the final version of the board should be done (not the schematic, just think about the requirements for Masters and slaves of the I2C bus) to avoid any i2C address clashes. (60% done)
- General block diagram update. Should have a block diagram for what is already done on the schematic. (done)

Things to do:

- Layout support for test board
- Architecture of Full board. Start and send to Eric/BNL for review
- I2C Bus review (in progress and 60%) complete
- Review footprints/cells. **(Eric B. sent me source files but I'm missing the footprints, so I can't review yet.)**
- Schematic for full board
- Layout for full board