19th RD50 Workshop Wednesday 23rd November 2011 CERN

ATLAS Semi-Conductor Tracker (Strips)

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The SCT Modules (The basic detector unit)

• Sensor Parameters

- Stand by Voltage = 50Volts
- Operational Voltage =150Volts
- Leakage current measured to 10nA
- Temperature
 - NTCs on Hybrid (2 BAR, 1 EC)
 - Difference Hybrid Si from FEA and measurement
 - Barrel Temp (Si) =-3°C End Caps =-7°C
 - Cooling by evaporation of C₃F₈
 - Barrel 6 operates at Thermal Shield (10°C warmer)

Environment

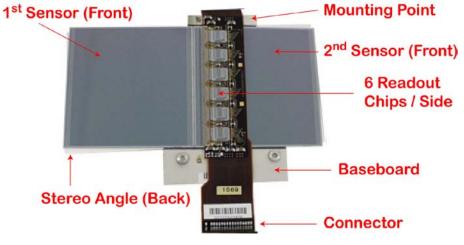
Volume is flushed with dry Nitrogen

• Sensor Dimensions

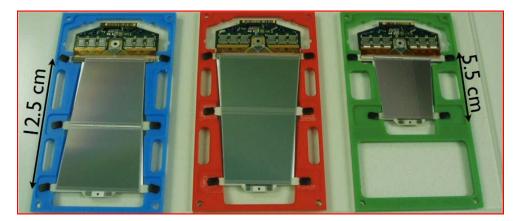
- 12 (2x6) cm (B), 6-12 (1or2 x 6cm) cm (ECs)
- See next slide for sensor details.

Baseboard

- Thermal Pyrolitic Graphite
- Mechanical & thermal structure
- Readout
 - Rad-hard front-end readout chips (ABCD)
 - 6 chips/side, 128 channels/chip
 - 48 modules (96 optical links) served by 1 ROD
 - Mechanical & thermal structure
- Resolutions
 - ~17 μm(rφ, bending plane), ~580 μm (z)



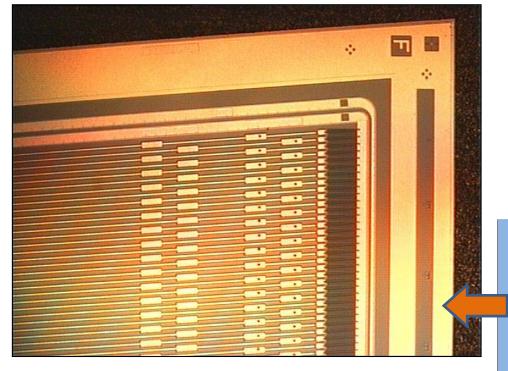
Barrel Module

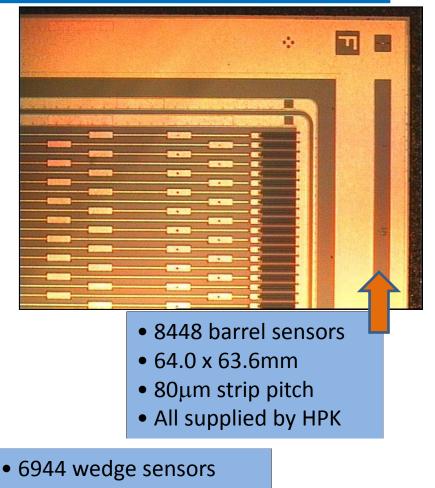


End Cap Modules

The SCT Sensors

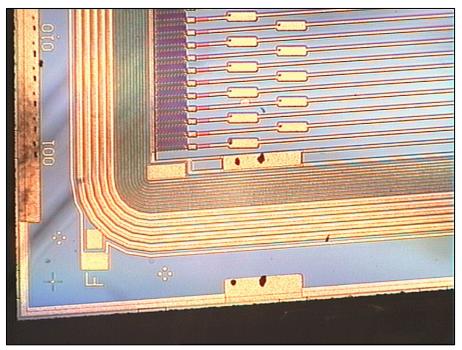
- Single sided p-on-n
- <111> substrate, 285µm thick
- 768+2 AC-coupled strips
- Polysilicon (1.5M Ω) Bias
- Strips reach-through protection 5-10µm
- Strip metal/implant widths 20/16μm





- 56.9-90.4µm strip pitch
- 5 flavours
- 82.8% HPK

The SCT CiS Sensors – "Same spec, different species"



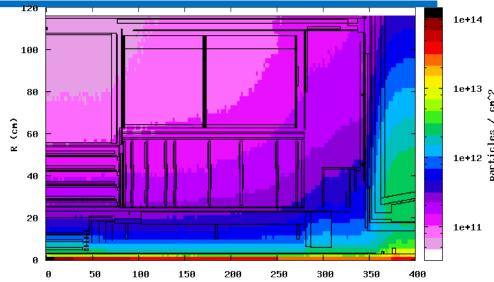
	Hamamatsu	CiS	Sintef
Sensor shapes	All	Wedges only	Barrels only
Orientation	$\langle 111 \rangle$		$\langle 100 \rangle$
Oxygenation	None	W12 only	None
Biasing resistor	Polysilicon	Implant	Polysilicon
Edge design	Single guard	14-multiguard	11-multiguard
Strip dielectric	Composite structure, depending on manufacturer		

	Hamamatsu	CiS	
Bias Resistors (1.5M Ω)	Polysilicon	Implant	
Strip metal/implant widths (μ m)	20/16	16/20	
Guard design	Single floating	Multi-guard	
Barrels supplied	8448	0	
Wedges supplied	6944	1196	

Fluence Expectations

The silicon detectors SCT & PIX will be exposed to high a fluence of particles which will affect their performance.

The PIXEL and SCT groups will monitor the affects of radiation on the leakage currents and depletion voltages

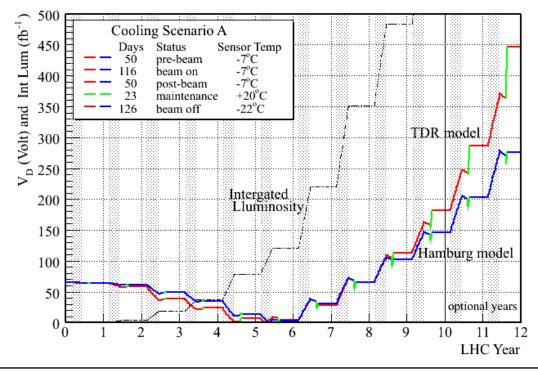


z (cn)

Silicon 1 MeV neutron equivalent fluence, 14 TeV

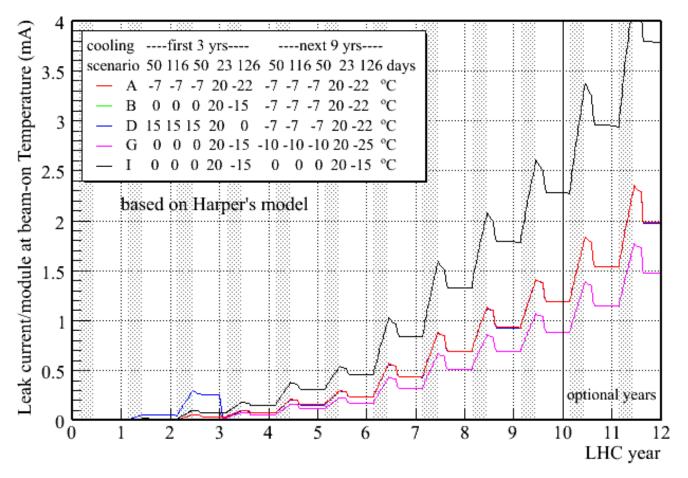
layer	R (cm)	Z(cm)	n _{eq} fluences (cm⁻ ²)/100fb⁻¹	n _{eq} fluences (cm ⁻²) /730fb ⁻¹
Pixel B-layer	4.2	0-40.7	267*10 ¹²	1922*10 ¹²
Pixel B2	12.7	0-40.7	46*10 ¹²	335*10 ¹²
SCT B3	30	0-75	16*10 ¹²	130*10 ¹²
SCT B6	52	0-75	8.9*10 ¹²	65*10 ¹²
SCT D9	44-56	272	14*10 ¹²	102*10 ¹²

Predictions (Depletion Voltage)



Prediction of LHC Luminosity profile (one particular profile with high(ish) integrated values)							es)						
Vear	1	2	3	4	5	6	7	8	9	10	11	12	Voar
year	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	year
IL/year	0.5	3.3	15	19	41	42	99	132	132	145	193	242	fb⁻¹
Integ. L	0.5	3.8	19	38	79	121	220	352	484	629	822	1064	fb⁻¹

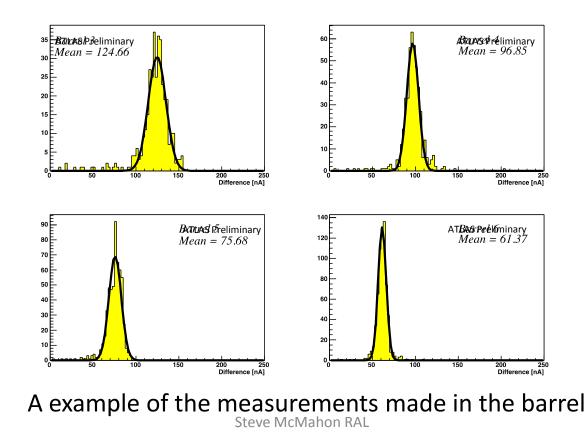
Predictions (Leakage current)



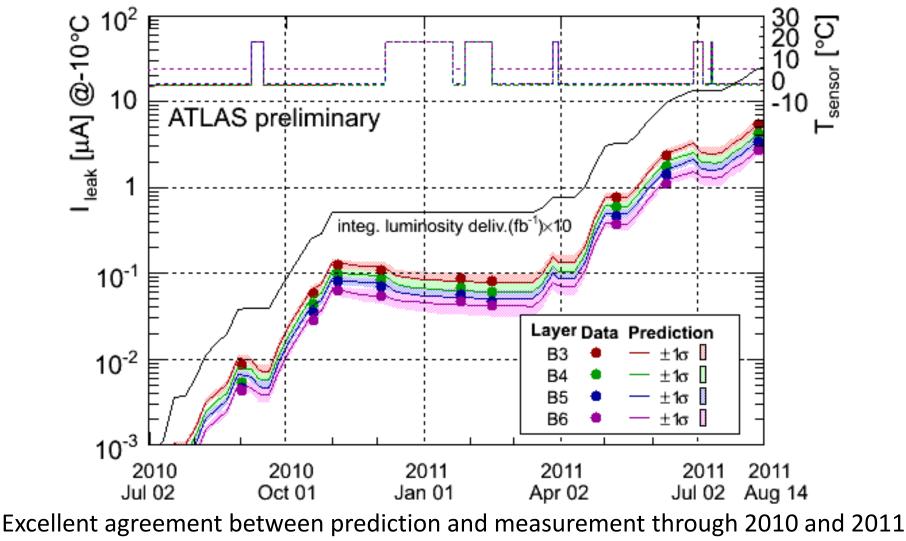
Barrel-3 for various cooling scenarios based on Harper's model (PhD Thesis, Sheffield). The integrated luminosity uses the same model as the previous slide.

Leakage Current Measurements

- Measure current during Machine Development (FSI off)
 - Current monitored constantly but make these special measurements during TS
- Average over a period of time
- Correct current to -10°C (temperature QA irradiations were performed at)
- Since first workshop now use 0°C (but older results shown here)



Comparison of SCT measured leakage current and prediction



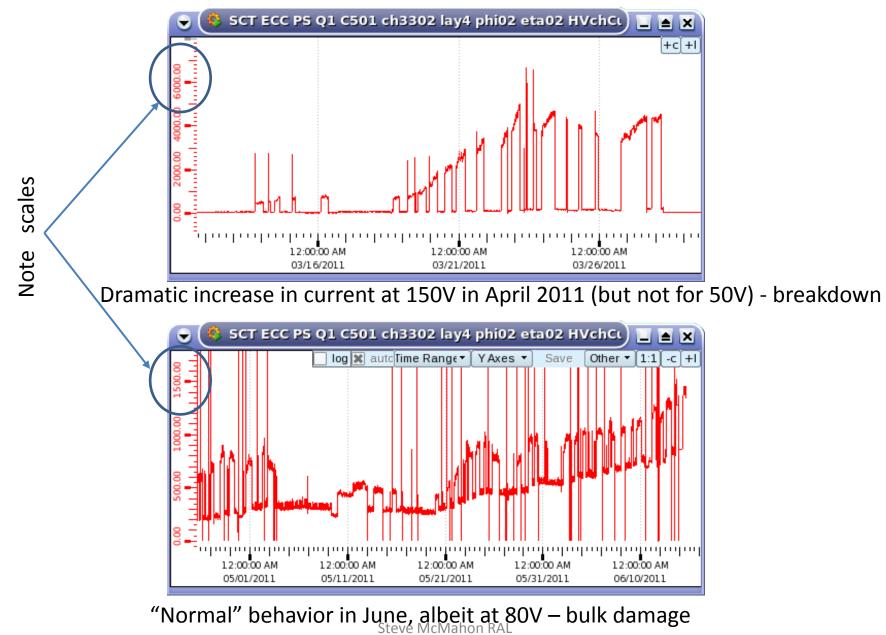
The CIS Leakage Current Problem

- During production become clear that the CIS SCT sensors were very sensitive to humidity
 - A significant subset displayed poor IV and early (<150V) breakdown in dry conditions
 - Need humidity to maintain a 'healthy looking' IV
 - Became more apparent during module tests which (unlike sensor QA) were typically conducted in nitrogen environment
 - Problem identified as micro-discharge from strips, due to lack of field plate (strip metal narrower than implant)
- As this became an issue rather late in the delivery program, SCT adopted a pragmatic strategy:
 - Only accept sensors with no sign of breakdown below 150V in dry air
 - OK for the short term, and then strip micro-discharge becomes less relevant after type inversion

CIS Leakage Current Problem

- We have had a small but significant (~30/4088) number of modules which have developed anomalously high leakage currents this year (2011)
- Almost all were constructed with CIS sensors, and all showed IV breakdown above 150V during production QA tests
- We believe that oxide charge buildup from ionising radiation is shifting the breakdown voltage downwards
 - Decreasing HV and increasing current limits means we do keep operating these devices with full efficiency so far

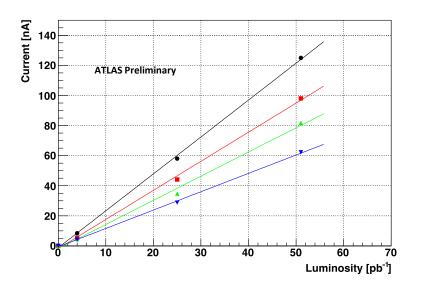
Example of leakage current deterioration from anomalous CIS-equipped modules



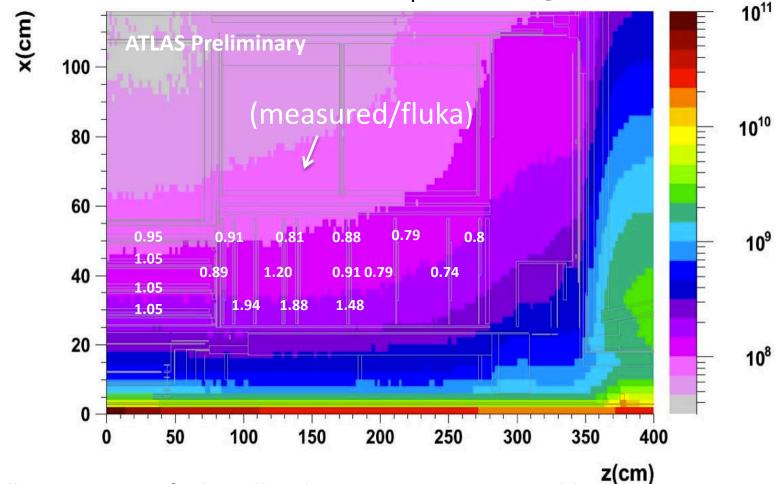
Fluence extraction

1 MeV neutron equivalent fluences obtained from increased leakage currents

$\Delta I = I_{Measured} -$	$I_{Initial} = \alpha \phi V$		
I: leakage current	(measured)		
lpha: damage parameter	(known)		
V: active volume of detector	(known)		
Φ : fluence	(what we want)		

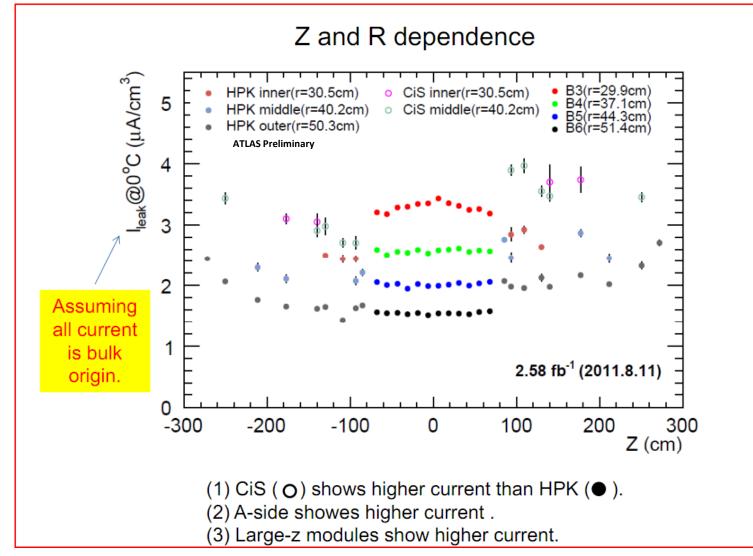


Linear increase in leakage currents with luminosity suggests fluences dominated by pp collisions (cf machine background.) Comparison of 1MeV n-eq fluences determined from SCT leakage current measurements with simulated FLUKA predictions @ 7 TeV.



- Excellent agreement for barrel! EndCap comparisons reasonable too.
 - (Indicated are differences for EC-C.)
- For inner rings we measure consistently higher than predictions why?

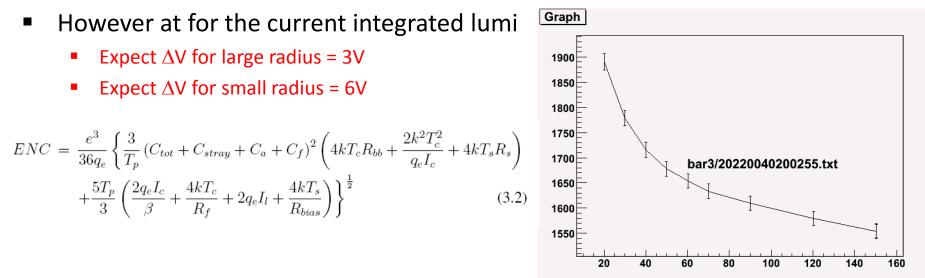
SCT Leakage Current



Shifting the "measured" temperature in the +Z end cap (2°C) removes discontinuity between Steve McMahon RAL the end of the barrel and end-caps (but right now we have no reason to do this)

Depletion Voltage

- Capacitance contributes to the noise
- When bias voltage reaches full depletion voltage in silicon sensors the strip to backplane capacitance becomes minimal as well as the noise due to this capacitance.
- Measurements were done in <u>December 2008</u> at the start of LHC running, and were repeated on 8th <u>November 2011</u>.
- Analysis to track the changes since 2008 is now in progress



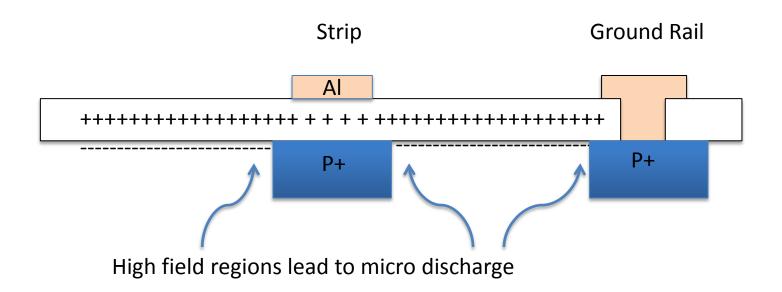
Summary (ATLAS-SCT)

- Predictions have been made for the evolution of the depletion voltage and leakage current
- Measurements of the leakage currents have been performed
 - Excellent agreement with Hamburg model predictions
- Fluence measurements have excellent agreement with FLUKA simulations
- Depletion voltage measurements (noise scans) will be performed at the end of the year and compared to predictions
- The effects of radiation will continue to be monitored

Bulk leakage current

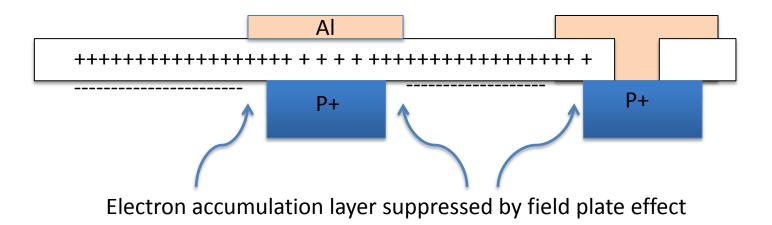
references	Robert Harper's Thesis (2001, University of Sheffield)						
$I = g(\Theta(T_A)t_{ir}, \Theta(T_A)t')\alpha\phi V$							
$g(\Theta(T_A)t_{ir})$	$g(\Theta(T_A)t_{ir},\Theta(T_A)t') = \sum_{i=1}^n \left\{ A_i \frac{\tau_i}{\Theta(T_A)t_{ir}} \left[1 - \exp\left(-\frac{\Theta(T_A)t_{ir}}{\tau_i}\right) \right] \exp\left(-\frac{\Theta(T_A)t'}{\tau_i}\right) \right\}$						
	$\Theta(T_A) = \exp\left(\frac{E_I}{k_B}\left[\frac{1}{T_R} - \frac{1}{T_A}\right]\right)$						
	$\alpha_{eq}(-7^{\circ}C) = (6.90 \pm 0.20) \times 10^{-18} \text{ A} \cdot \text{cm}^{-1}$						
	i	$ au_i(\min)$	A_i				
	1 $(1.2\pm0.2)\times10^6$ 0.42 ± 0.11						
	2	$(4.1\pm0.6)\times10^4$	0.10 ± 0.01				
	3 $(3.7\pm0.3)\times10^3$ 0.23 ± 0.02						
	4	124 ± 25	0.21 ± 0.02				
	5	8 ± 5	0.04 ± 0.03				

CiS Sensor strip schematic



Build up of positive charge in oxide (which starts from a non-zero offset and steadily increases and eventually saturates with ionising radiation) leads to increasing electron accumulation layer at the Si-SiO interface, giving high field region at the edge of the implant. CiS sensors were known to have improved breakdown at high humidity because surface charge is always negative which suppresses the electron accumulation layer.

Hamamatsu Sensor strip schematic



Field plate effect suppresses electron accumulation layer (because strip metal is at negative potential wrt implant underneath it), lower field strength at edge of P+ implant, much less prone to micro discharge and less sensitive to humidity.