



### Measurements of highly irradiated ATLAS planar pixel sensors with unirradiated readout electronics

### André Rummler

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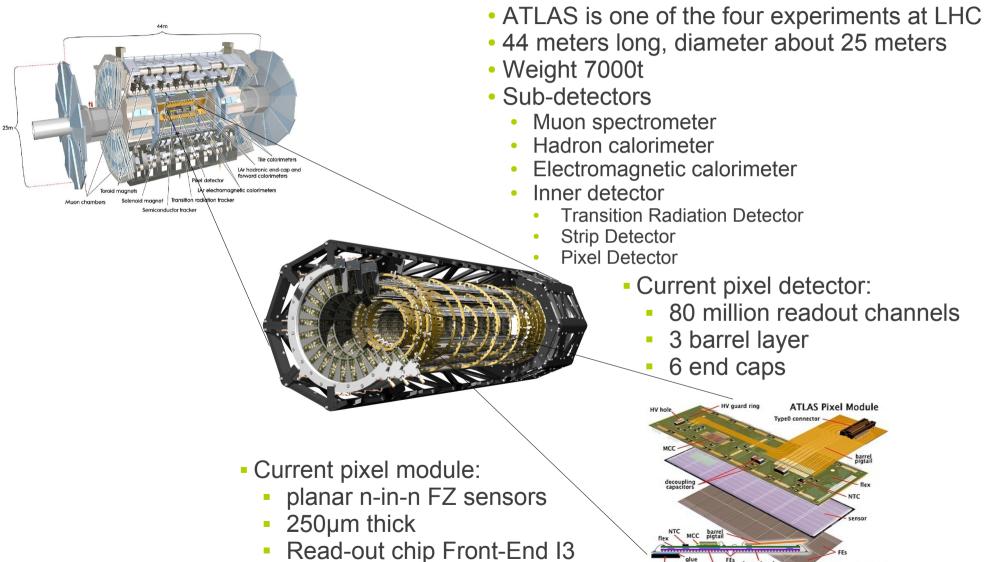
GEFÖRDERT VOM

Bu für

Bundesministerium für Bildung und Forschung



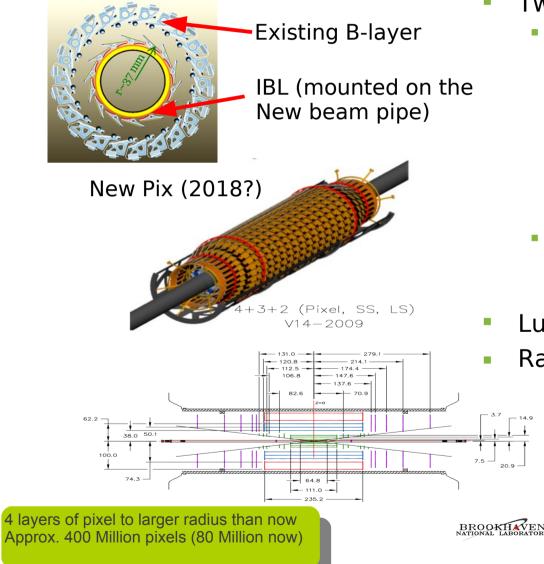
### The pixel detector of the ATLAS experiment



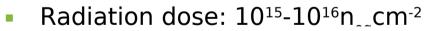
dimensions:  $\sim 2 \times 6.3$  cm<sup>2</sup> weight:  $\sim 2.2$  o

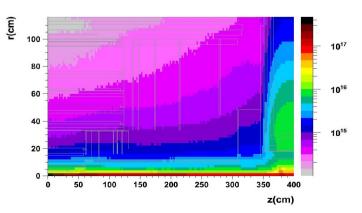


## Future upgrade of the ATLAS pixel detector

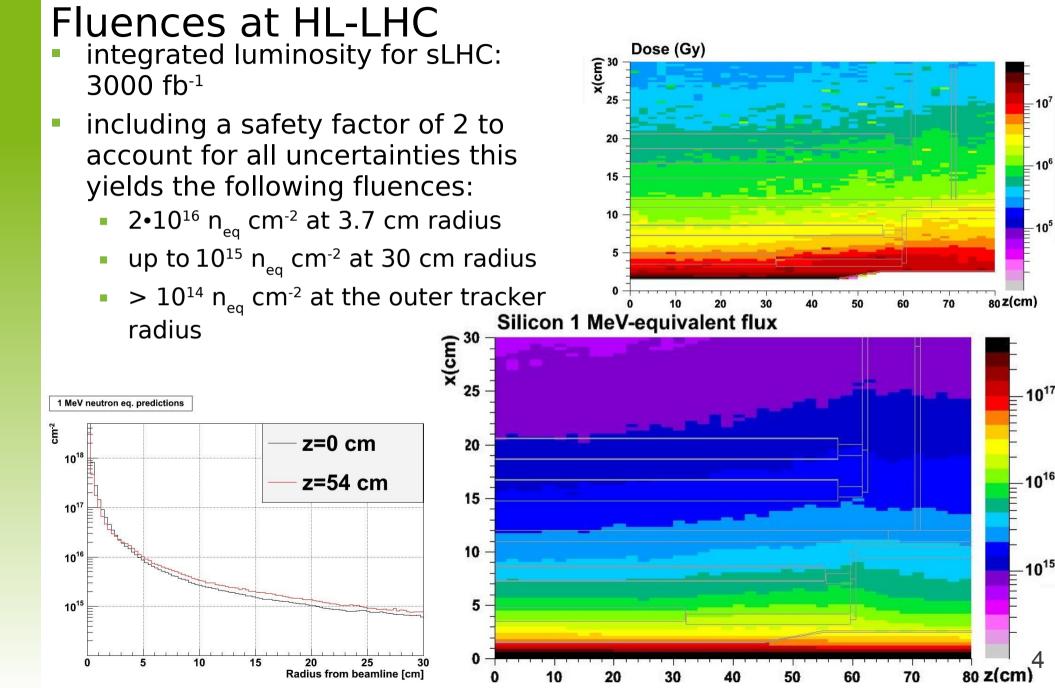


- Twofold upgrade
  - Insertable b-layer (IBL)
    - IBL conditions:
      - Maximum bias voltage: 1000V
      - Cooling -15°C
      - Radiation hard up to 5.10<sup>15</sup>n<sub>eq</sub>cm<sup>-2</sup>
  - Full replacement in 2018 or 2021-2022 depending on the performance
- Luminosity: (2-3) · 10<sup>34</sup>-10<sup>35</sup>cm<sup>-2</sup>s<sup>-1</sup>











200

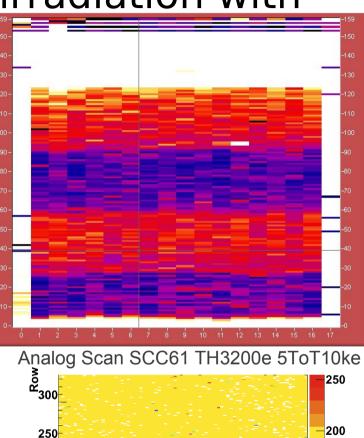
100

50

# FE damage – FE-I3 after severe irradiation with neutrons

- Experience after very high fluences with neutron-irradiated assemblies:
- threshold tuning and digital communication always work
  - **but**: erratic behaviour observed, even with assemblies from the same irradiation batch:
    - on some days, source scans only yield noise/empty events/stripes
    - on some days, chip works fine
    - some chips never work
    - some chips always work
- Low energy proton irradiation
   → no longer working beyond 1•10<sup>15</sup> n<sub>eq</sub> cm<sup>-2</sup>
- High ionising dose means trouble also for FE-I4 150
- See Claus Goessling's talk at last AUW

André Rummler | Measurements of highly irradiated sensors with unirradiated readout electronics | CleRN,104.120.20301



150

100

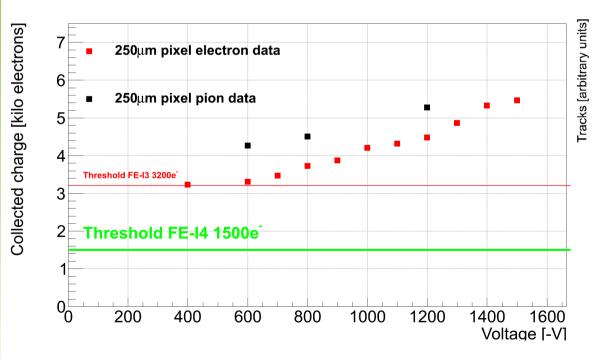
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705 Column

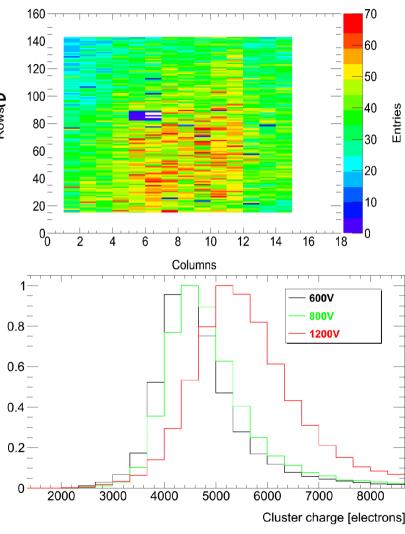


# It is still possible: first results FE-I3 assemblies were irradiated in Ljubljana with reactor neutrons up to 2•10<sup>16</sup> n<sub>en</sub> cm<sup>-2</sup>

- Collected charge measured with a Sr-90 source (electrons) and with pions at CERN-SPS
  - beamspot
  - Iandaus
  - collected charge vs. bias voltage



#### 2•10<sup>16</sup> n<sub>eq</sub> cm<sup>-2</sup> 1200V



doi:10.1016/j.nima.2011.05.074 doi:10.1016/j.nima.2010.11.186 more forthcoming

## Deconvolution of the effect of radiation on sensor and chip

### FE-I3 chip

- Present ATLAS front-end
- 250nm IBM CMOS technology
- Radiation hard up to ~100Mrad
- Still widely used for the R&D thanks to its larger availability and better understood TOT-Charge conversion

### FE-I4 chip

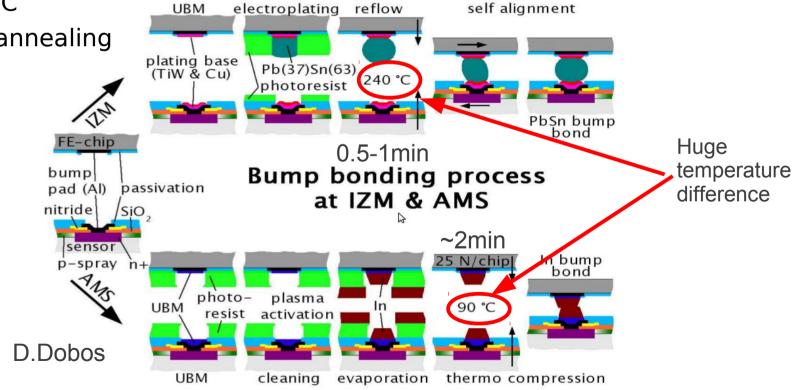
- New front-end for IBL upgrades
- 130 nm IBM CMOS technology
- Radiation hard up to ~250Mrad
- Will be used in the outer layers of upgraded pixel detector
- A more radiation resistant chip will be needed for the pixel inner layer in 2022

Solution to investigate sensor radiation hardness in the full HL-LHC range: Irradiate sensor before connection to the front-end chip and afterwards interconnect the chip with indium bump-bonding, a technology requiring a low temperature budget



## Flip chip procedure

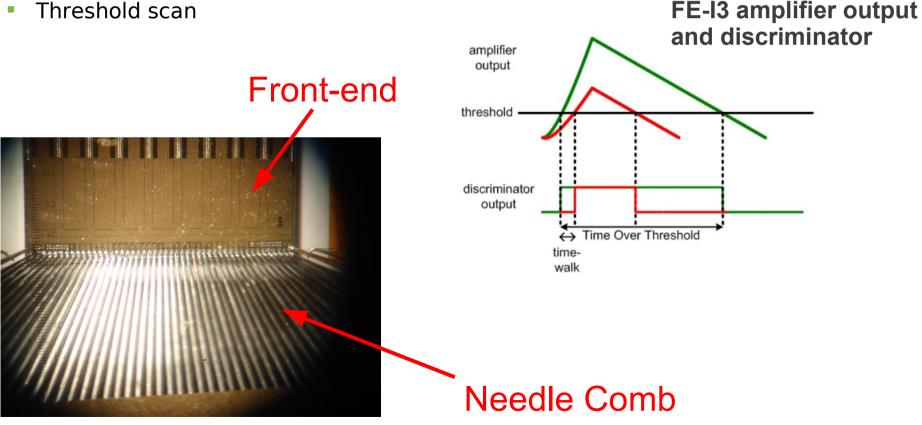
- Samples were flip chipped successfully at Selex (AMS) in Italy
- Thermo compression bonding with indium bumps
- Flip chip procedure is much colder (80°C 90°C, 2min) than the IZM (SnAg-3-5, 250°C-260°C, 0.5 – 1min) procedure.
- This roughly equals the annealing time 2h @ 60°C
- No additional annealing needed





### Pre-testing the unirradiated front-ends

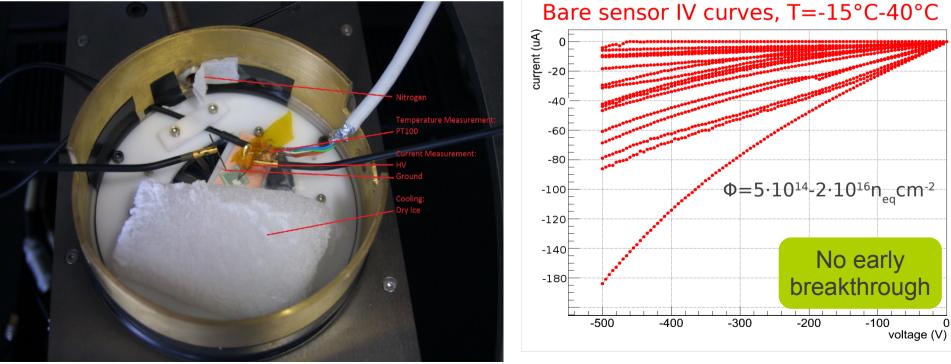
- unirradiated FE-I3s were tested before flip chipping with a testcard (needle comb) in order to assert their working condition
- Successfully passed tests:
  - Digital test
  - Analog test
  - Threshold scan





### Testing the irradiated sensors

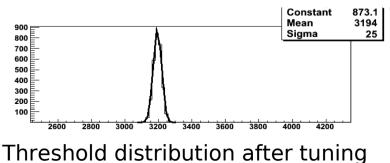
- Sensors in n<sup>+</sup>-in-n design on 250 $\mu$ m thick Fz-bulk from the original ATLAS production
- tested before and after irradiation by IV measurements (ATLAS QA criteria applied, best samples selected)
- Irradiated with 26 MeV protons in Karlsruhe  $(5 \cdot 10^{14} n_{ea}^{2} \text{ cm}^{-2} \text{ to } 2 \cdot 10^{16} n_{ea}^{2} \text{ cm}^{-2})$
- After irradiation the sensors were cooled with dry ice (temperature between -15°C and -40°C, all currents corrected to -15°C)

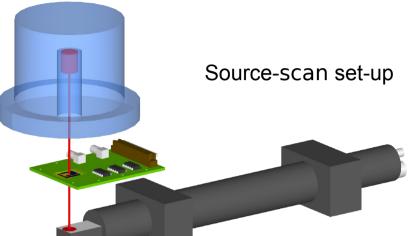


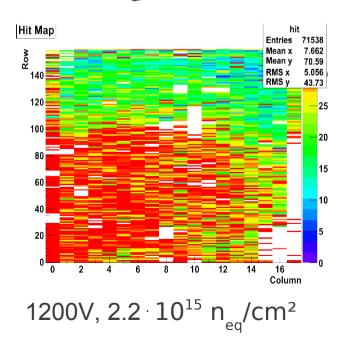


### Lab Measurements

- Source scans with <sup>90</sup>Sr (~1MeV electrons)
- External trigger: scintillator coupled to a photomultiplier
- Sensor bias voltage up to 2000V
- Cooling with dry ice (about -45°C on at the aluminum carrier plate)
- Copper tape was used to realize the heat conductive connection between dry ice and the backside of the aluminum
- All temperatures were measured with a PT1000 on the aluminum carrier
- Read-out realized with new ATLAS common read-out system (USBPix)



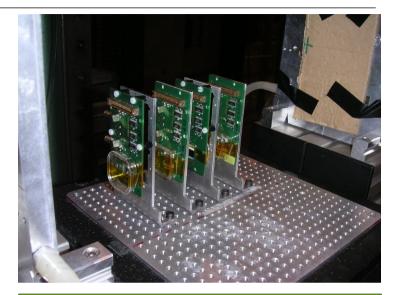






### Test beam measurements

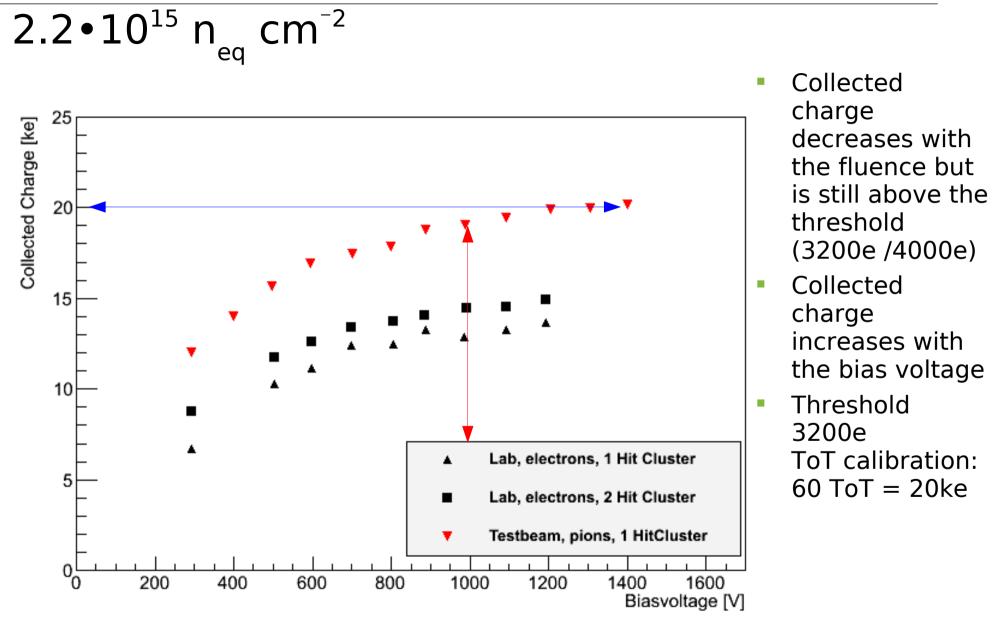
- Beam from SPS with 120GeV pions
- cooling with dry ice (about -45°C on the aluminum plate) measured with a PT1000
- fast bias scan → just enough statistics to measure the correlation between the collected charge and bias voltage
- 5 voltage steps up to 1800V with sufficient statistics for space resolved analysis (e.g. efficiency)



<u>Many thanks to the PPS collaboration</u> <u>and in particular to the PPS Testbeam</u> <u>group and all people involved in the</u> <u>data taking and analysis</u>

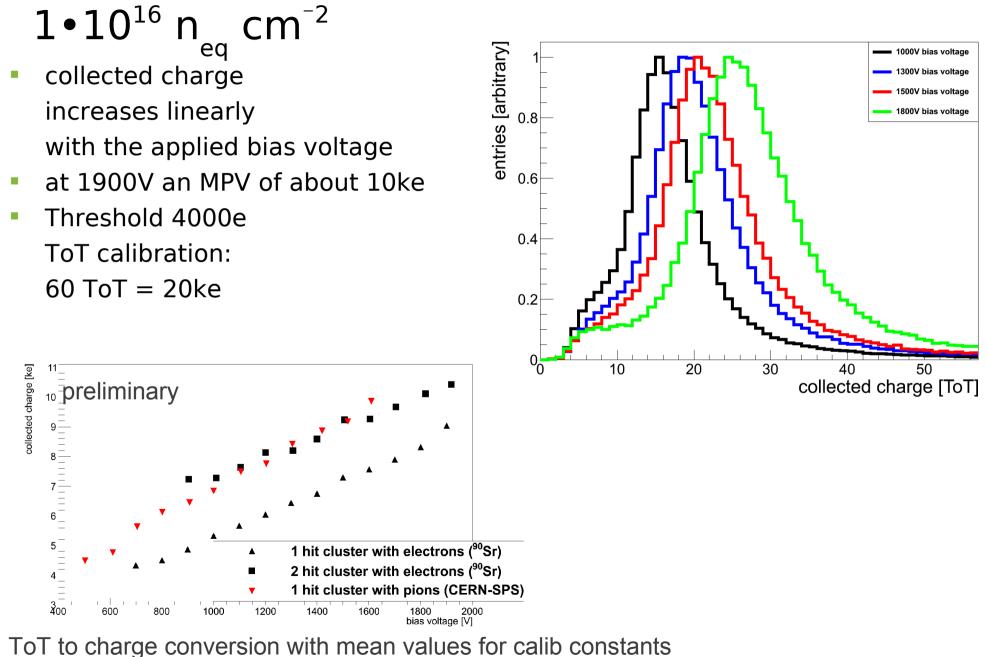
Name	Fluence	Voltage	Voltage	Voltage	Voltage	Voltage	"fast bias scan"
DO-I-7	2.2 <sup>.</sup> 10 <sup>15</sup> n <sub>eq</sub> cm <sup>-2</sup>	500	600	1000	800	1200	300-1500
DO-I-11	1 <sup>.</sup> 10 <sup>16</sup> n <sub>eq</sub> cm <sup>-2</sup>	600	1000	1500	1300	1800	500-1700
DO-I-5	1.4 <sup>.</sup> 10 <sup>16</sup> n <sub>eq</sub> cm <sup>-2</sup>	600	1000	1500	1300	1800	500-1700
DO-I-12	1.4 <sup>.</sup> 10 <sup>16</sup> n <sub>eq</sub> cm <sup>-2</sup>	600	1000	1500	1300	1800	500-1700





ToT to charge conversion with mean values for calib constants

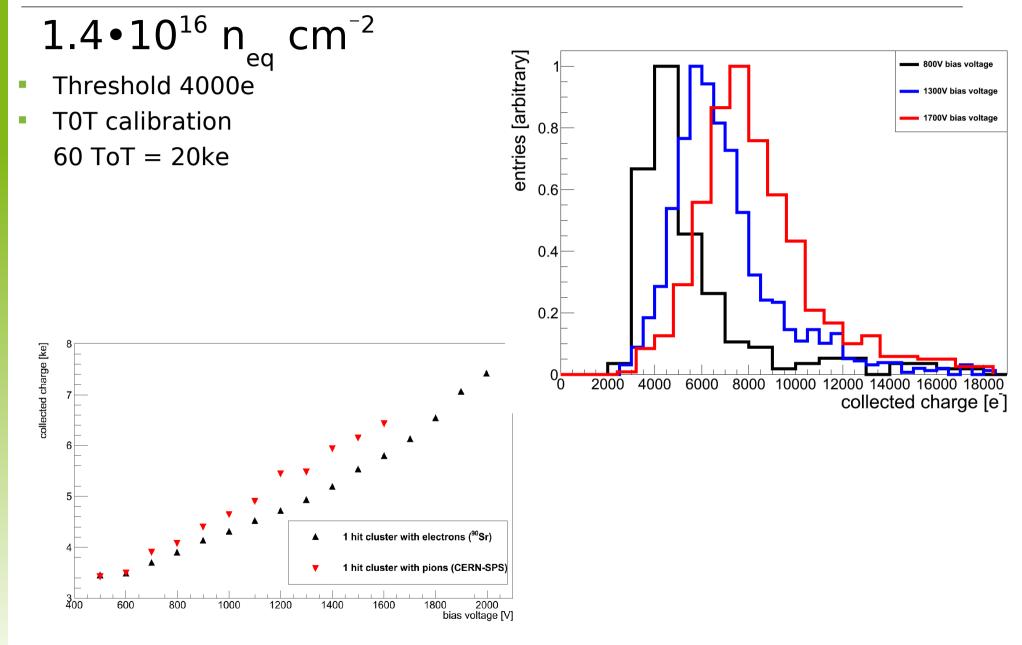




André Rummler | Measurements of highly irradiated sensors with unirradiated readout electronics | CERN, 14.11.2011

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Fakultät Physik Experimentelle Physik 4

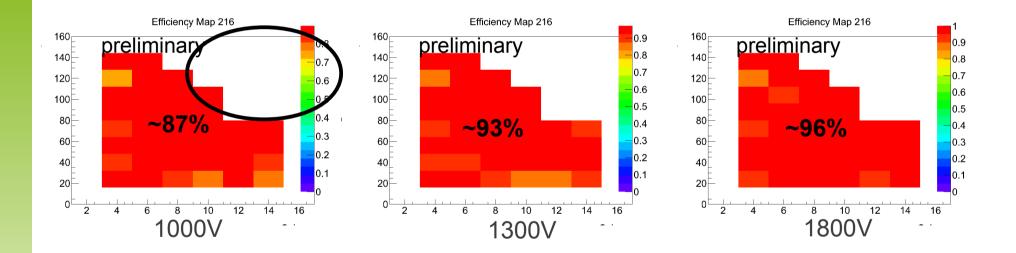


ToT to charge conversion with mean values for calib constants André Rummler | Measurements of highly irradiated sensors with unirradiated readout electronics | CERN, 14.11.2011



## Tracking efficiency maps 1.4•10<sup>16</sup> n<sub>eq</sub> cm<sup>-2</sup>

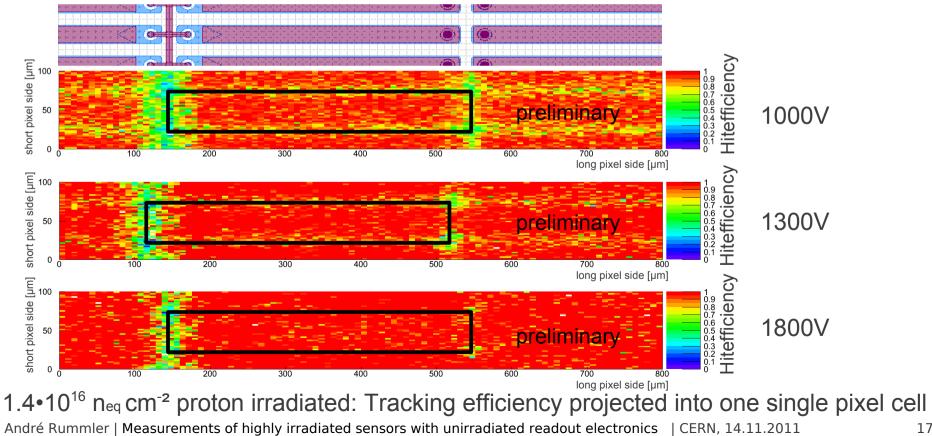
#### Some disconnected bumps due to aging

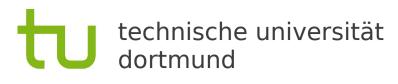




### Charge loss and possible improvements

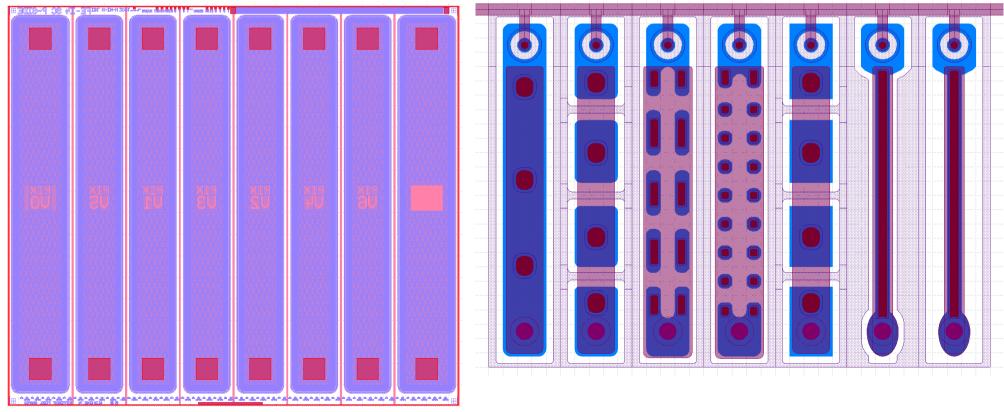
- Most of the pixel has much higher efficiency than 97% charge loss seems to occur mainly under the bias grid
- Ideas:
  - No bias grid?  $\rightarrow$  No testing before flip chipping
  - Bias grid running over pixels instead of between them  $\rightarrow$  currently being produced, first results soon





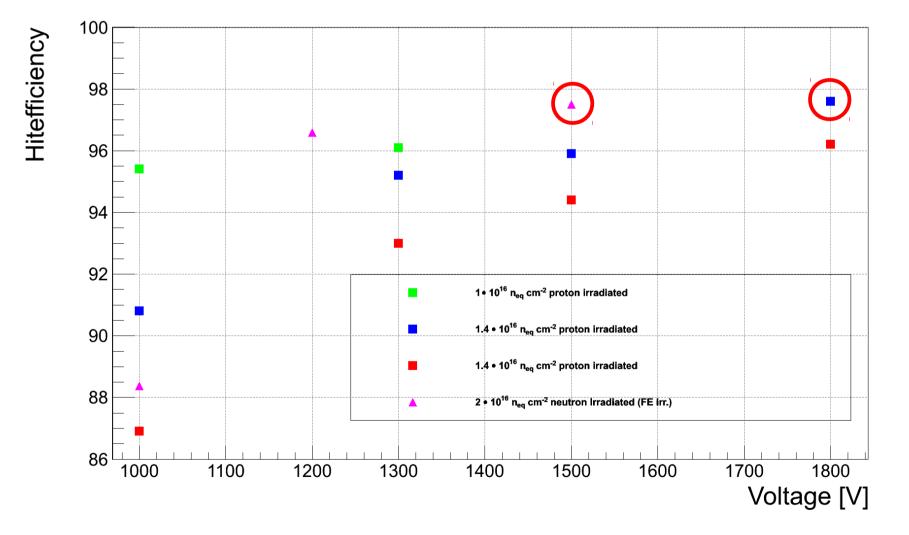
### Charge loss and possible improvements

- In addition: improve collected charge
  - reduce thickness to increase field: 150 μm thick planar sensors available from several vendors, 75 μm has also been produced
  - try to improve the charge multiplication effect by employing special field forming implant geometries
  - special structures dedicated to this task were already produced
- New materials like n-type MCz are being evaluated





### Tracking efficiencies at HL-LHC fluences





### Conclusion

- Strong indication that the planar sensor are sufficiently radiation hard for the inner most layer under HL-LHC conditions
- Sensors irradiated to a fluence about 1.4•10<sup>16</sup> n<sub>eq</sub> cm<sup>-2</sup> yield 4-5ke at 1000V and about 7-8ke at 2000V
- Good tracking efficiencies measured with beam-test data: 97% at 1800V
- Results are comparable between samples with and without irradiated front-end
- Bias Voltages and environmental temperature not far away from IBL conditions: max. bias voltage of 1000V, T=-25°C-30°C on the modules
- Successful and stable operation up to 2 kV at -45°C
   Outlook
- More detailed analysis of the data taken / Annealing
- More studies will be carried out with new Indium samples irradiated with reactor neutrons
- Would also be possible in the future with FE-I4 as soon as Indium-bumps with Selex are qualified
- Even at moderate (IBL like) bias voltages the highest irradiated assembly shows charge collection fully compatible with the FE-I4 threshold (1500e)



## Backup

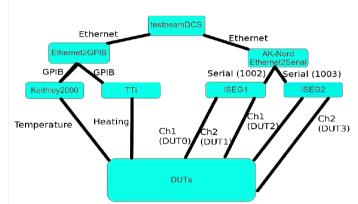


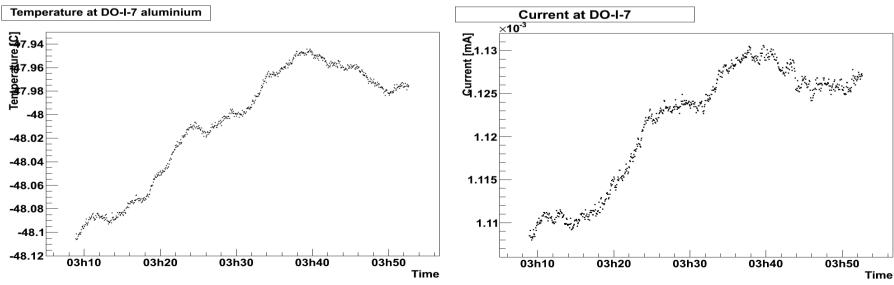
# TestbeamDCS -an environment monitoring program

- Monitoring temperature, currents and high voltage
- Control temperature with heating resistor
- Synchronize information with the run control
- Possibility to see for

example the strong correlations

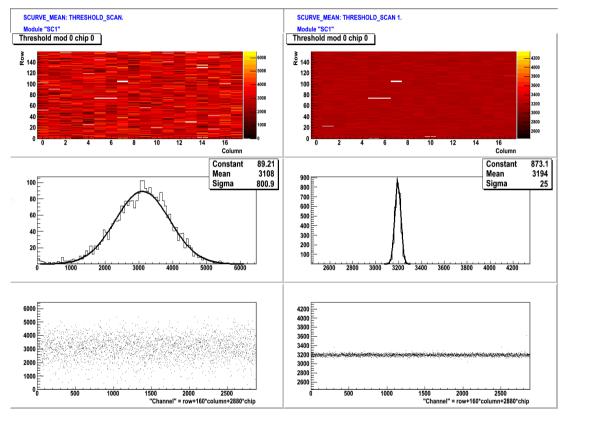
between temperature and leakage current





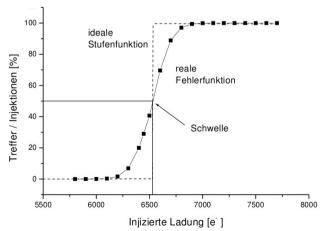


### Tunings



#### Fakultät Physik Experimentelle Physik 4

- Each pixel has its own threshold
- Before testing the samples with a source they have to be tuned to a specific threshold
- After tuning the pixel have a similar threshold



technische universität Fakultät Physik **Experimentelle Physik 4** dortmund  $5 \cdot 10^{14}$ n\_/cm<sup>2</sup>, measured with Sr90 ea DO-I-1, 5E14 Collected charge 25 Collected Charge [ke] increases with the bias voltage 20 

15

10

5

00

200

400

600

- Maximum MPV is about 17ke
- Threshold 3200e
- **TOT** calibration 60TOT=20ke

800

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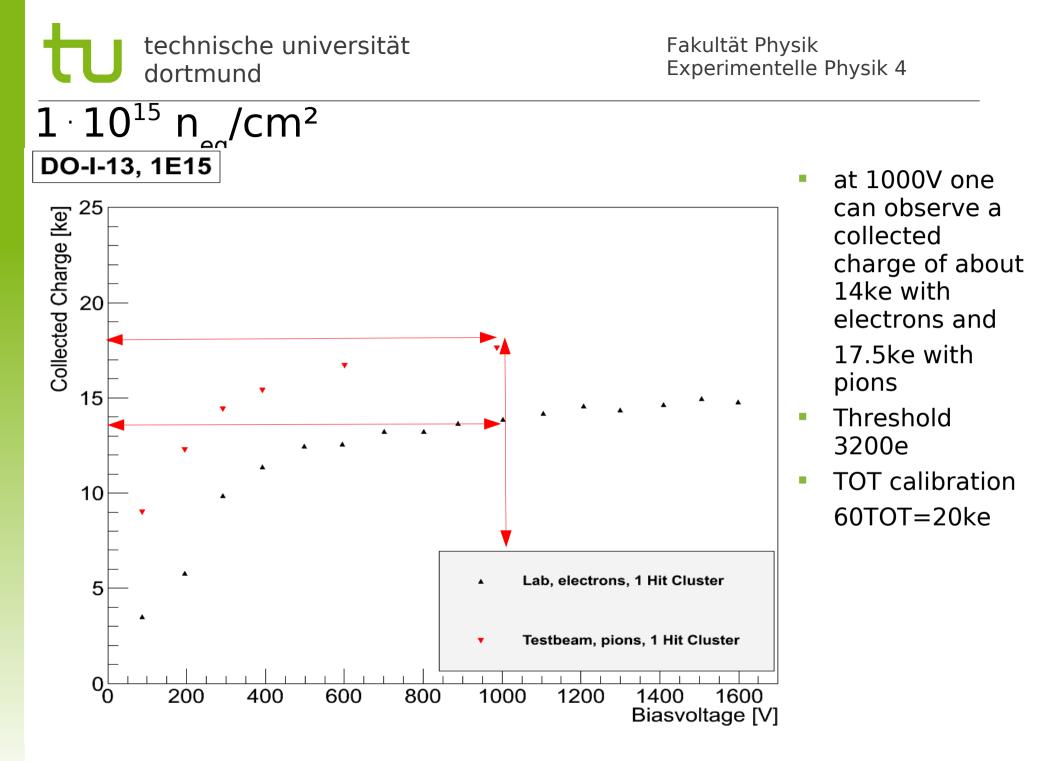
1 Hit Cluster

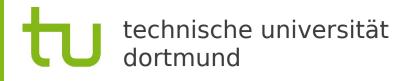
2 Hit Cluster

1200

1400 Biasvoltage [V]

1000





### Results of lab measurements

- Collected charge decreases with the fluence but is still above the threshold (3200e /4000e)
- Collected charge increases with the bias voltage
- Above 500V the noise contribution grows with bias voltage at low charge values

