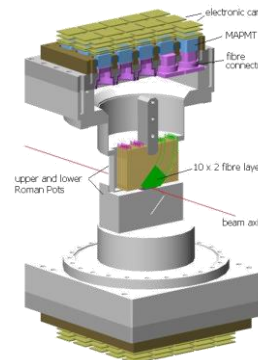
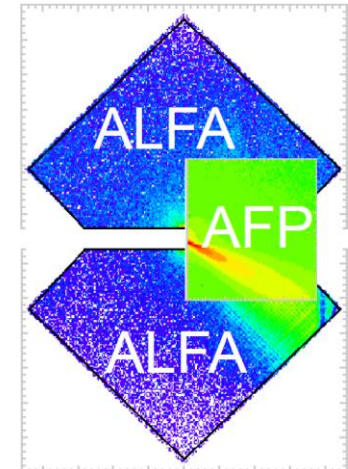
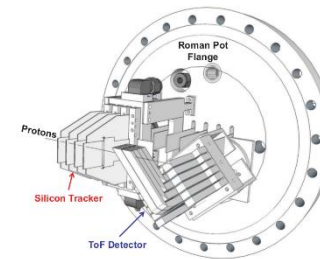
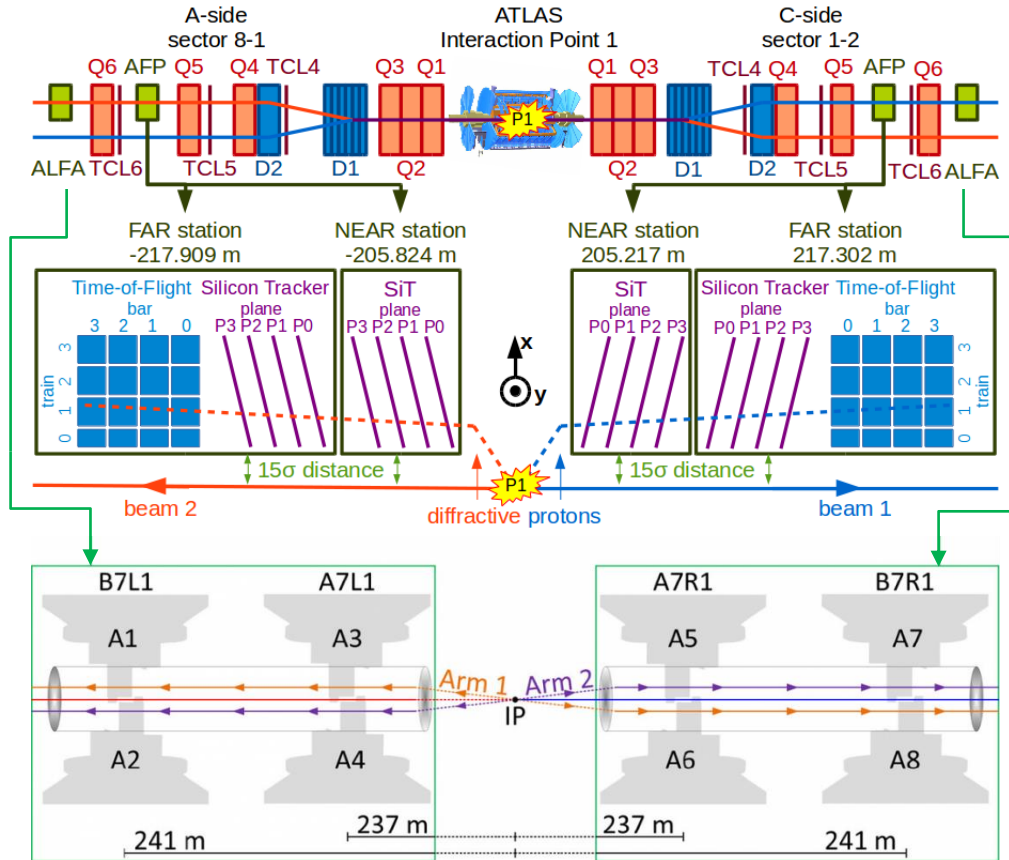


# ECR/Plans for AFP shielding installation during YETS 24/25

## LHC Tunnel Region Experiments Working Group (TREX)

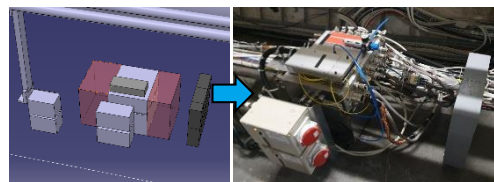
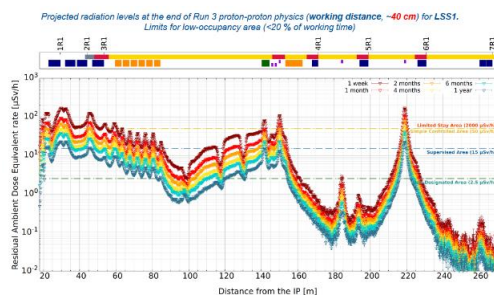
Marko Milovanovic

31.01.2025.



# Background

- Thanks to its location, i.e. close proximity to TCL6 (~1m) AFP keeps suffering from tremendous radiation damage throughout Run3, owing to increased luminosity, which particularly in 2024 produced a lot of very good data for physics.
  - This implies increased equipment activation, due to which even 2 months after LHC shutdown, 30uSv/h are measured at the location of FAR stations.
  - High losses in TCL6 were also identified during loss maps, which made it also the hottest spot in the LHC ring in terms of residual dose rate during YETS.
- These issues were first identified in 2022, when AFP was not able to calibrate equipment anymore during interfills, nor have enough time to repair/replace failing components in the tunnel. Two solutions:
  - Relax TCL6 settings to mimic favorable conditions in Run2 (~1.9mm).
  - Install possible shielding walls between AFP and TCL6 collimators.
- 2023 was a lot better year for AFP in terms of radiation damage.
- In 2024 however, with changed optics (TCL6 settings were again very tight, ~1.6mm) AFP started experiencing the same issues.
  - As relaxing TCL6 was no longer an option (due to negative radiation damage effects to other equipment in the tunnel), the only remaining choice was upgrading the shielding walls between AFP & TCL6 as much as possible.
  - ECR: <https://edms.cern.ch/document/2914323/0.1> (approval accepted)
  - However, this did not improve situation much, and apart from weekly failures in FAR station electronics, the PP equipment (VREG CC & VB) also started dying at an alarming rate.

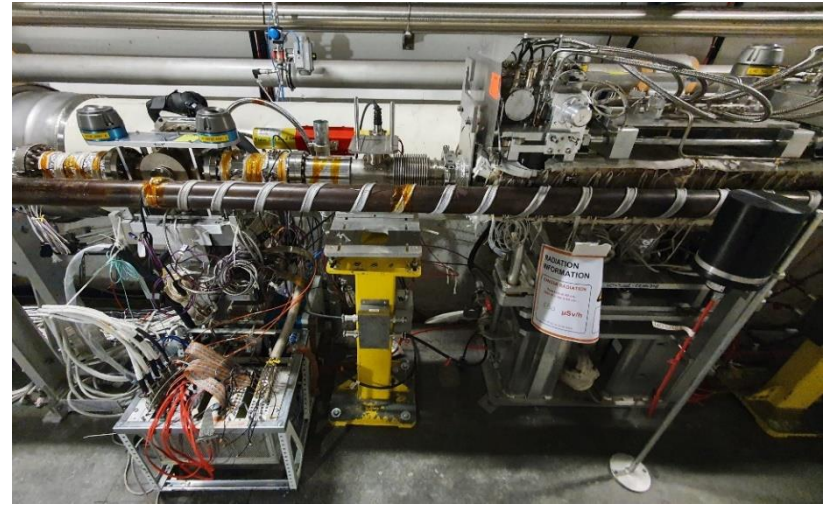
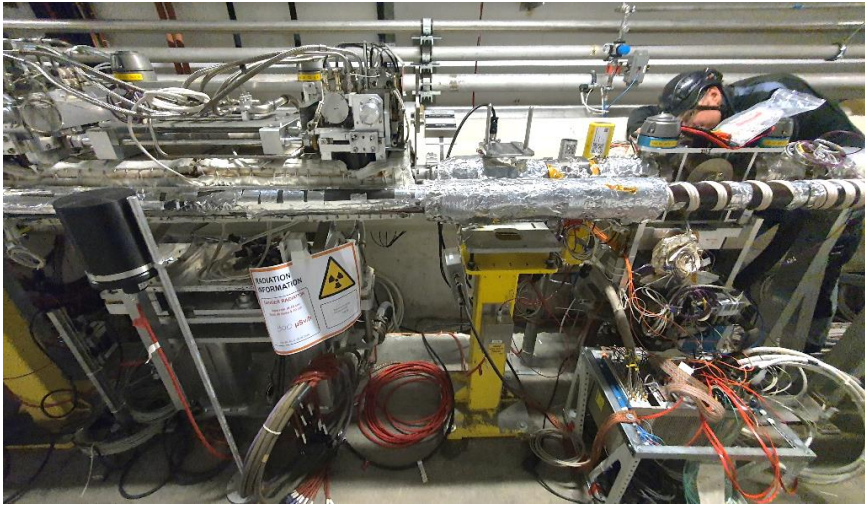


## 2024 BatMon deployment in AFP: context

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- The closed **TCL6** settings in IR1, as well as the large amount of **integrated luminosity** being delivered to ATLAS, is causing **issues** to AFP equipment, including (among others):
  - Electronics crate (roughly 1m upstream of the TCL6, at floor level)
  - Patch panel (roughly 7m upstream of the rack)
- The option of relaxing the TCL6 setting has been considered, upon request by AFP, to reduce the radiation levels on the equipment and the local activation (see [CoIWG#281](#)).
- However, **TCL6 could not be relaxed** due to conflicting constraints (R2E in the DS, FASER/SND backgrounds). Moreover, RP measurements after a dedicated test run have shown that small changes in the half-gap don't yield significant improvements on the activation ([EDMS 3096203](#)).
- The following actions were undertaken during TS1 (see next slides for details and layout):
  - **Implementation of shielding** using iron bricks
  - **Deployment of BatMons** to measure the radiation levels on the racks and patch panels, on the left and right side of P1

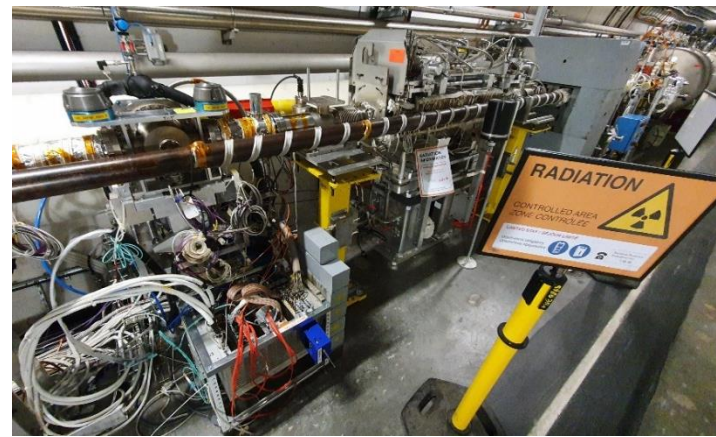
# Background – BatMon deployment + shielding upgrade



> LSS1L: **900  $\mu\text{Sv/h}$**  (dose rate at 40cm)

LSS1R: **650  $\mu\text{Sv/h}$**  (dose rate at 40cm)

> Average dose at AFP location during short access: 400  $\mu\text{Sv/h}$  (max. allowed dose per day/person: 50/100  $\mu\text{Sv}$  according to ALARA lvl.1 – now increased to ALARA lvl.2)



Thanks to prompt reaction by JPC & M.Lazzaroni!

## Conclusions/2

- The measurements (summarized on slide 19) indicate that neither the AFP crate **nor** the patch panel are **safe for electronics** (as known for the IR1-IR5 tunnel areas)
- In particular, considering the target luminosity of  $120 \text{ fb}^{-1}$  for the 2024 run, the crate and patch panel are **expected to receive TID levels between ~100 Gy and ~200 Gy, and HEH fluences varying between around  $10^{11}$  and  $10^{12}$  HEH/cm<sup>2</sup>** (see the table on slide 20 for the full numbers, including the thermal neutron fluences)
- For the above values, the occurrence of radiation-induced failures may be **critical**, both in terms of lifetime degradation and (very high) probability of Single Event Effects (SEEs)
- The BatMons could not test the efficiency of the shielding deployed in TS1 **with the current deployment**. However, the installed bricks are not large enough (and hermetic enough) to make a substantial impact.
- The above radiation levels can serve as a starting point to determine an appropriate mitigation strategy in view of 2025, which also needs to take into account:
  - The **level of criticality of the electronics**, and any information about their lifetime and/or SEE sensitivity
  - **Practical constraints** for the possible installation of **larger** local shielding (ideally, an iron bunker)

- The small shielding walls were to remain until end of AFP program (at the time seemed most likely until end of 2024) and it was just struggling to survive until this point.

# Background – decision that keeps AFP until end of Run3!

## Configuration proposal

**RP-V in IP5**  
seems to be mandatory to avoid major risks on the inner triplet region magnets

**CONSEQUENCES:**

- TOTEM pots rotation
- Modification of powering scheme (IT+Q4)

LHC-BOC final decision:

~~**RP-H in IP1**  
best configuration for magnet protection~~

~~**CONSEQUENCES:**~~

- ~~• Increased background for FASER & SND~~
- ~~• AFP is lost~~

**NOM-H in IP1**  
increased risk for D1 & IT toward the limit at the end of Run3

**CONSEQUENCES:**

- Reduced background for FASER&SND
- Preserve AFP experiment

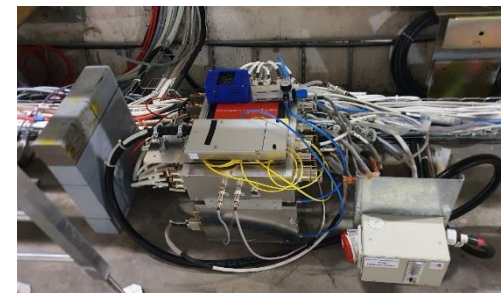
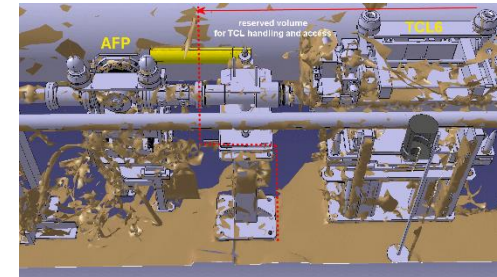
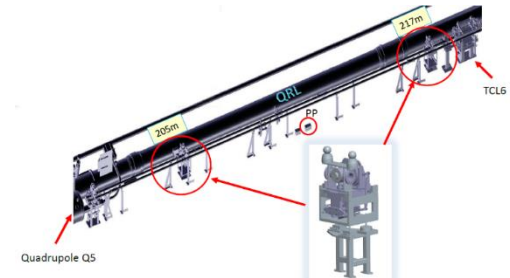


### Swap magnet with spare possible:

- Operation estimated to last 6-7 weeks – not optimized!
- Partial reconditioning to be done

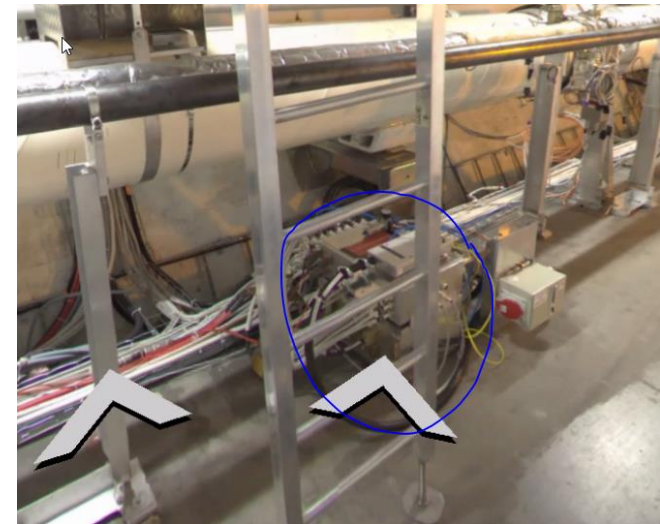
# Current situation

- During 2024, AFP had to borrow the last (two) remaining spare(s) of VREG Controller Cards from Pixels, in order to complete the final/p-p reference run program.
  - In total, AFP needs 1 functioning VREG Controller Card (located in PP) per arm. In total, we had 5 broken cards just before p-p reference run.
  - An enormous effort was invested to find a way to repair these cards finally during YETS, as new production was not possible.
  - Still struggling to repair all the VREG Voltage Boards (located in PP) to be able to operate this year.
- Since it is NOT possible to make any proper shielding near TCL6 (without modifying the vacuum layout), an IB decision was taken NOT to run w/ToF detector during nominal LHC operation until end of Run3.
  - ToF crates would be installed only during ramp-up or special runs in order to avoid so many issues/equipment failures experienced during 2024 and reduce costs
  - In 2024 AFP overspent the foreseen budget in order to produce enough spares to be able to operate until end of the year.
- While it is possible to operate without ToF, it is not possible to do so without the PP electronics (VLDBs, VREG Controller Cards and Voltage Boards).
- This is why we are in desperate need of a better shielding wall for our Patch Panel at least in order to reduce the unsustainable equipment failure and operation downtime in 2025/26.



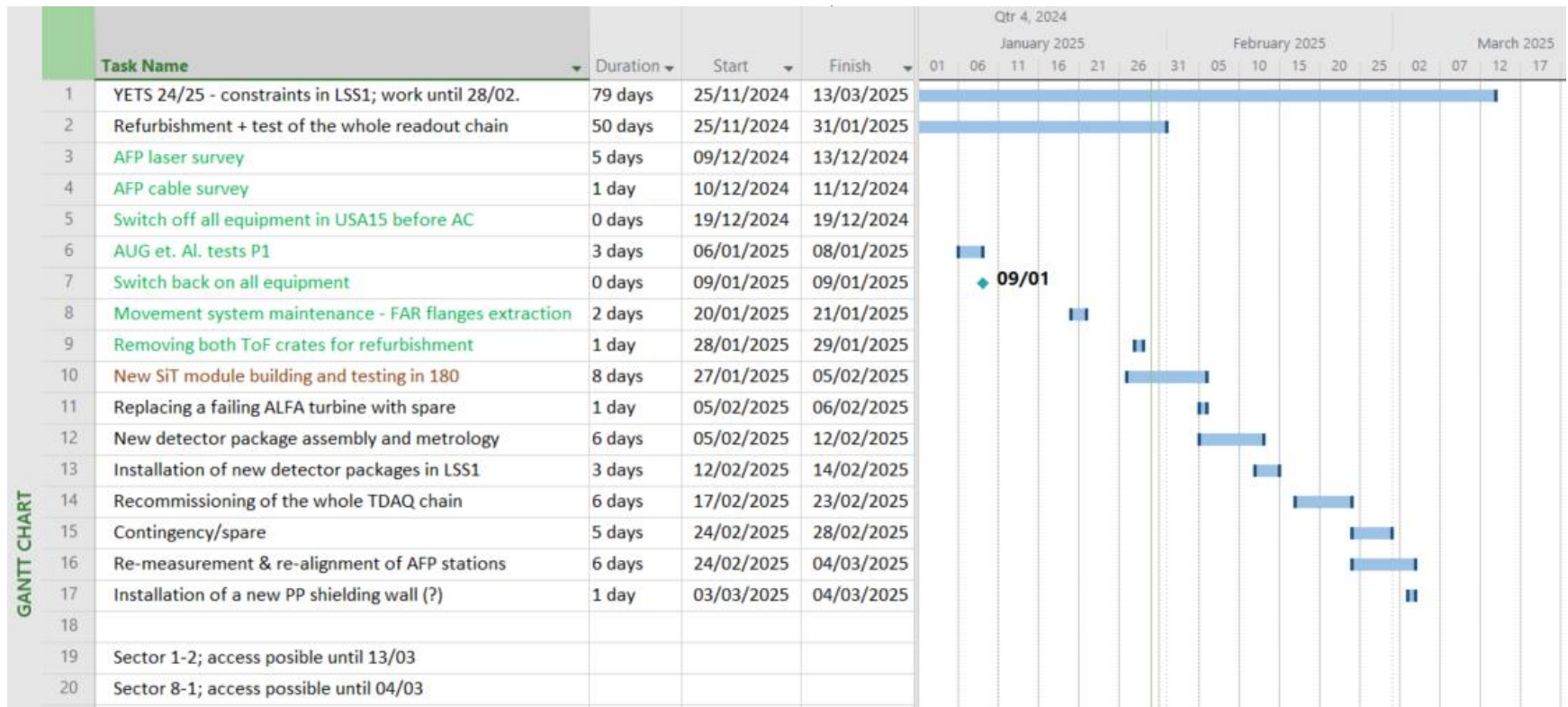
# Proposal by R2E (PP upgraded shielding wall) – GL, JPC & ML

- For FLUKA/R2E, in an ideal world, the patch panel should be in a bunker-like shielding, covered both on the sides and on the top
  - The BSRT in P4 is a good example, or similarly, the bunkers in the TD62/68 tunnels upstream of the beam dumps.
  - From R2E experience with similar bunker studies (both with FLUKA + measurements) one would need at least 10cm of iron shielding on all sides; but the more, the better.
- The main constraints in P1 are related to integration:
  - A full bunker like the one of P4 or TD62/68 is impossible to implement, but in order to achieve a non-negligible reduction of the dose, one would need to implement at least something that “encapsulates” the full racks (i.e., with walls on the two sides, and a “ceiling” made of iron bars).
- Before running FLUKA studies, integration study needs to be done to determine the max. amount of shielding to be realistically added in YETS
  - Once a realistic shielding layout is available, we could then launch a quick FLUKA study to compute the expected reduction of the dose and include it in an ECR to justify the intervention.
  - 1<sup>st</sup> step: understand if a shielding of this sort can be put in place during this YETS.
- Integration team might need some assistance to do it in time, which AFP is more than willing to provide.





# YETS 2024/25 plan



- Still on track with the YETS schedule. Hopefully the shielding wall will be approved before end of YETS.
- ECR is kept up-to-date with the progress with the help of M.Majstorovic (BE-EA-EC) in order to circulate it in time (2 weeks before the LMC approval).

Backup slides.

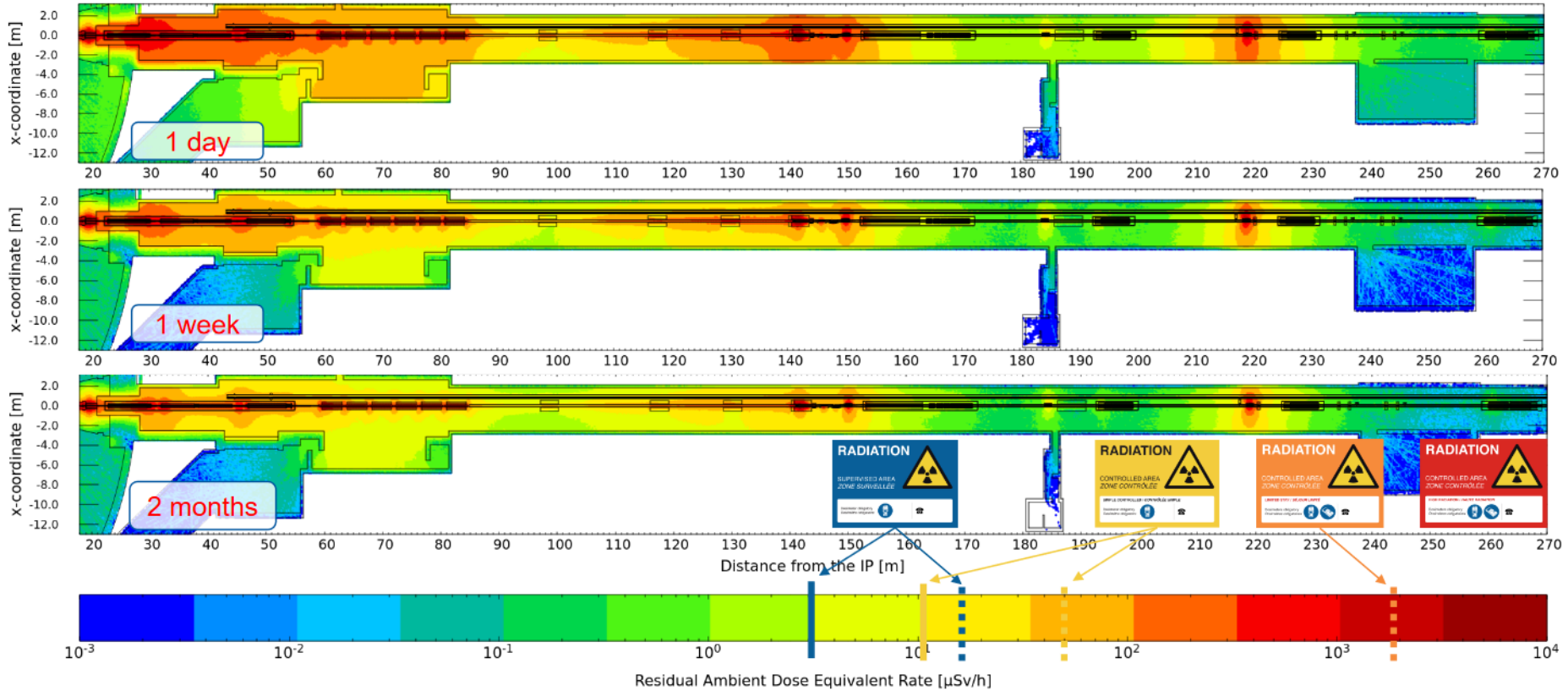
# Reminders, updates & constraints

- > Replacing Stainless Steel ALFA blind flanges in S81 with Al ones:
  - Scheduled for 28-29/11. Sune will use his own impact.
- > ARP cable survey
  - Scheduled for 10/12 – Giulio, Elzbieta, Luis, Maciej, Marko.
- > 1<sup>st</sup> AFP laser survey
  - Scheduled for W50 (ca.12-13/12).
- > 2<sup>nd</sup> AFP laser survey
  - New fiducialisation (if necessary) and new LVDT calibration (~2 days): for these. Precise slot tbd; aiming for W9. A short slot can be locked in order to optimize our presence
  - Smoothing alignment: the regular LSS1 smoothing will be done between 2025-02-10 and 2025-02-21 (no help required).
- > New SiT modules production at IFAE:
  - Bump-bonding machine broken at IFAE & wire-bonding technician over-committed. Might send the modules to CERN for wire-bonding.
  - Ideally to be ready for pick up by 12/01 (W2) or deliver by 27/01 (W5) to CERN for wire-bonding and testing.
- > Extraction and re-insertion of flanges w/refurbished detector packages:
  - Aiming for removal of flanges in W4 (+1 week)
  - Assembly and metrology of detector packages in W6-7
  - Re-installation of flanges w/refurbished packages: W7-8.
  - Recommissioning, 2<sup>nd</sup> survey & smoothing alignment: W9
- > Sector 8-1 access possible only until 04/03!
  - Sector 1-2: access possible until 13/03.



# Projected radiation levels in LSS1

## LS3: LSS1 Residual $H^*(10)$

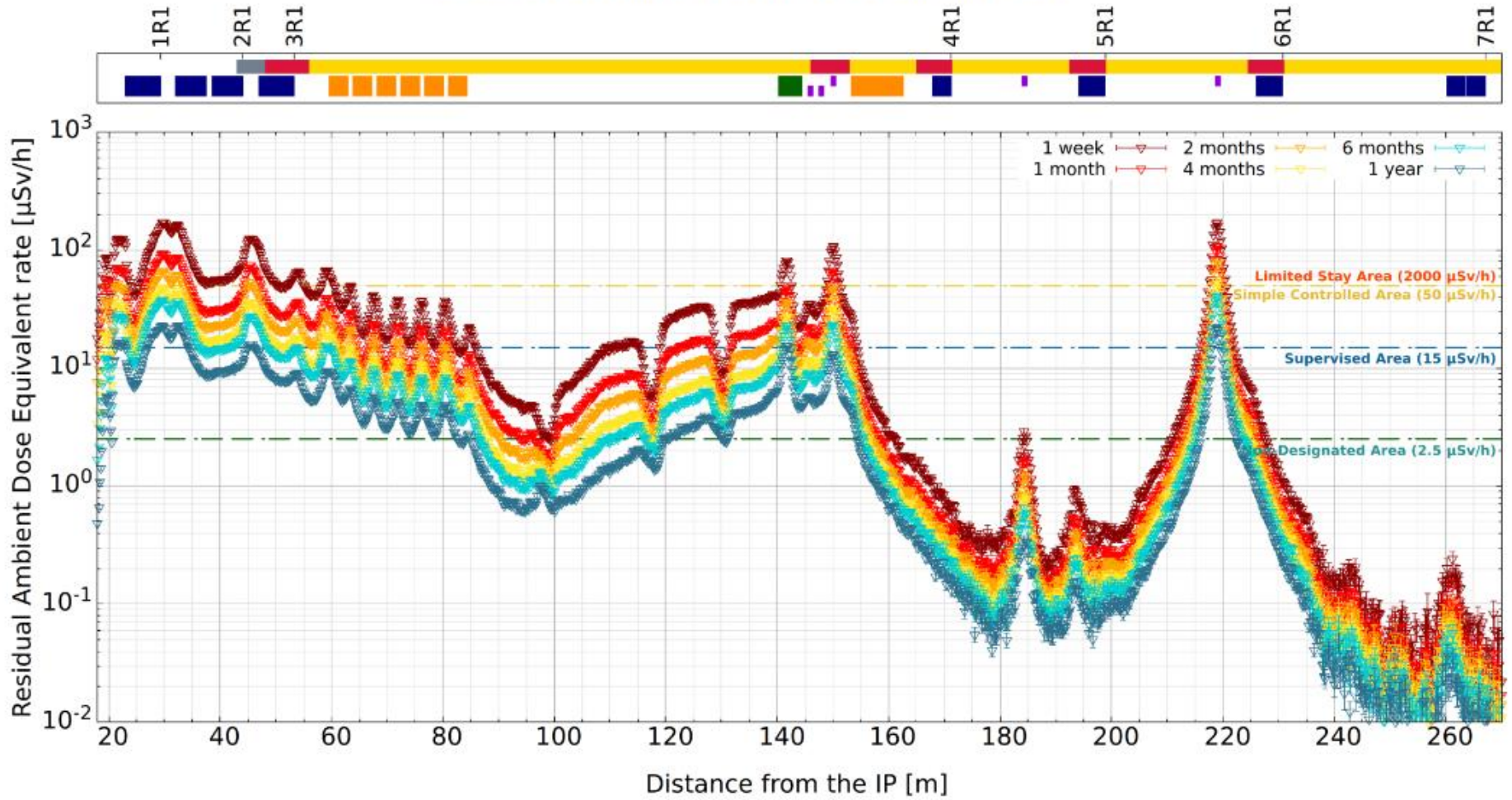


P. Dyrz, A. Infantino  
HL-LHC Integration meeting #210  
21.10.2022  
EDMS 2790070

7

# Projected radiation levels in LSS1

Projected radiation levels at the end of Run 3 proton-proton physics (working distance, ~40 cm) for LSS1.  
Limits for low-occupancy area (<20 % of working time)



# Update from R2E-MCWG on BatMon measurements

## Summary and Normalized Measurements for IR1-AFP racks

		P1 Left Side			P1 Right Side		
Position	Magnitude	**5.7 fb-1	1 fb-1	120 fb-1	**5.7 fb-1	1 fb-1	120 fb-1
CRATE	TID	8.27	1.45	174	5.6*	0.98*	117.6*
	$\phi_{pp/cm^2}^{HEH}$	$3.99 \times 10^{10}$	$7 \times 10^9$	$8.4 \times 10^{11}$	$3.12 \times 10^{10*}$	$5.4 \times 10^9*$	$6.5 \times 10^{11*}$
	$\phi_{pp/cm^2}^{TbN}$	$1.96 \times 10^{11}$	$3.44 \times 10^{10}$	$4.13 \times 10^{12}$	$1.80 \times 10^{11*}$	$3.16 \times 10^{10*}$	$3.8 \times 10^{12*}$
PATCH PANEL	TID	9.03	1.58	190	6.12	1.07	128.4
	$\phi_{pp/cm^2}^{HEH}$	$5.91 \times 10^9$	$1.03 \times 10^9$	$1.24 \times 10^{11}$	$4.62 \times 10^9$	$8.11 \times 10^8$	$9.73 \times 10^{10}$
	$\phi_{pp/cm^2}^{TbN}$	$3.30 \times 10^{10}$	$5.79 \times 10^9$	$6.95 \times 10^{11}$	$3.05 \times 10^{10}$	$5.35 \times 10^9$	$6.42 \times 10^{11}$

\*\*Atlas Luminosity from 2024-06-14 to 2024-07-01

\*Expected values considering the measurements of the three devices and a similar geometry for the two areas

# Conclusions, (still) open questions and ECR status

- > Suffering from enormous amounts of radiation damage, **AFP 'upgraded' its small shielding wall(s)** to try to compensate as much as possible for the increased radiation doses coming (mostly) from its vicinity to TCL6 collimators (since larger opening is no longer an option as last year).
- > BatMons (online radiation monitoring) installed in 2 crucial locations per side to determine precise radiation doses which, after the 1st measurements, are **confirmed not safe for the electronics** in that area.
- > These values will serve as a starting point to determine appropriate mitigation strategy:
  - Radiation testing of modules to get a better estimate at which doses our equipment fails and determine the exact cause (TID, SEE, DD)
  - Re-investigate practical/layout constraints for possible installation of larger local shielding (ideally, an iron bunker).
- > AFP still does not know if it will be able to run next year due to triplet polarity inversion/change of crossing angle (from vertical to horizontal) in P1.
- > ECRs still pending in anticipation of more information.

## 6. High Precision Time Digitizer

The High Precision Time Digitizer board, HPTDC, was developed by the University of Alberta. The 12-channel board uses 4 HPTDC ASICs developed and produced by CERN in 0.25  $\mu\text{m}$  CMOS technology (HPTDC, J. Christiansen *et al.*, <http://tdc.web.cern.ch/tdc/hptdc/hptdc.htm>). The four ASICs are controlled by an on-board FPGA which also handles the flow of data and controls. This and previous versions of the HPTDC board have been used successfully at various beam tests. The HPTDC, and new developments were presented by Pinfold at this workshop [6].

The intrinsic resolution of the current HPTDC is 16 ps, which is a significant contributor to the per-channel resolution. However, new HPTDC ASIC development with smaller feature size are ongoing at CERN and may lead to significant improvements in the near future. Note that the 16 ps resolution of the HPTDC is per channel and that the contribution for a system of four quartz bars in sequence would only be 8 ps.

The radiation tolerance of the HPTDC is not guaranteed. The HPTDC ASIC is expected to be radiation tolerant to a degree sufficient for it to be located on the tunnel floor, near the detectors. The FPGA firmware must be re-designed to provide the appropriate checking of HPTDC registers for

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*ATLAS Forward Proton Detectors: Time-of-Flight Electronics* 757

upsets. Moreover, the FPGA itself has to be radiation tolerant, which can be done by choosing a radiation-hard part (expensive!) or going to a fuse-programmable part. Alternatively, the FPGA can be programmed to do self-checking and organized with majority decisions in critical paths. It is the latter choice that will be pursued.