

Session Program

2-6 Jun 2025



3rd DRD3 week on Solid State Detectors R&D

WG/WP1 - CMOS technologies

Nikhef, Amsterdam

Monday 2 June

14:20

WG/WP1 - CMOS technologies: Scientific Presentations

Session | **Location:** Nikhef, Amsterdam

14:25–14:30 Introduction to WG1/WP1 session

Speakers

Eva Vilella Figueras, Heinz Pernegger, Ingrid-Maria Gregor, Jerome Baudot

14:30–14:50

Conceptual Basis for Canadian Participation in the MALTA Project: toward a Design and Verification Strategy

Speakers

Leonard McEchern, Manoj Sachdev, Thomas Koffas

15:00–15:20

The first prototype Monolithic Active Pixel Sensor for The OCTOPUS Project

Speaker

Fadoua Guezzi Messaoud

15:35–15:55

The Versatile MAPS project

Speaker

Michael Deveau

16:00–16:30

Coffee/Tea break

16:30–16:50

1 ns to 100 ps timing precision architecture (PMaTT) in TPSCo 65nm with Update on NAPA and TDC @ SLAC

Speakers

Caterina Vernieri, Didier Claude Contardo

17:00–17:20

Status of the non amplified MiniCactus V2 chip in LF15A technology and first steps towards sensors with intrinsic amplification.

Speakers

Prof. Philippe Schwemling, Yavuz Degerli

17:30–17:50

Monolithic strip sensors

Speaker

Ingrid-Maria Gregor

18:00

Tuesday 3 June

08:30

WG/WP1 - CMOS technologies: WP1 project proposals

Session | **Location:** Nikhef, Amsterdam

09:00–09:20

HV-CMOS Pixel Detector Demonstrator with Serial Powering and Innovative Interconnections

Speaker

Prof. Attilio Andreazza

09:30–09:50

All-silicon ladder concept for CMOS monolithic pixel detectors

Speaker

Nana Chychkalo

10:00–10:20

Design and characterization of CASSIA monolithic pixel sensors with gain

Speakers

Ivan Berdalovic, Tomislav Suligoj

10:30–11:00

Coffee/Tea break

11:00–11:20

Design of COFFEE3, a small prototype for 55nm HVCMOS validation

Speaker

Dr Yang Zhou

11:30–11:50

Towards an OpenSource DMAPS project proposal

Speaker

Daniel Muenstermann

12:00