

## Lab session (Hierarchical implementation)

*Friday 28 March 2025 10:30 (1h 30m)*

**Authors:** NAEIM, Mohamed (Cadence Design Systems); PORTE, sebastien

**Presenters:** CARATELLI, Alessandro (CERN); ANDORNO, Marco (CERN); NAEIM, Mohamed (Cadence Design Systems); PORTE, sebastien

**Session Classification:** Digital design