

Session Program

2-3 Sept 2011



8th RD51 Collaboration Meeting

WG5 - Electronics

Maiko, Kobe, Japan

Friday 2 September

10:30

WG5 - Electronics

Session | **Location:** Maiko, Kobe, Japan | **Conveners:** Hans Muller, Dr Jochen Kaminski

10:30-10:35 **Welcome**

Speaker

Dr Jochen Kaminski

10:35-10:45 **Status of Industrial Production of SRS**

Speaker

Hans Taureg

10:45-10:55 **SRS user experience from the MAMMA tests**

Speaker

Marcin Z Drozdowa Byszewski

10:55-11:15 **Crosstalk studies between signal lines in PCBs**

Speaker

Atsushi Taketani

11:15-11:35

CrossTalkless Detector - An attempt to understand the VFAT2 noise source

Speaker

Francisco Ignacio Garcia Fuentes

11:35-11:55

Status report of the Brookhaven front end IC for the ATLAS Muon upgrade

Speaker

Venetios Polychronakos

11:55-12:15 **Report on a AFTER-based RO for the FOPI TPC**

Speaker

Sverre Dorheim

12:15-12:20 **Progress on Timepix readout**

Speaker

Dr Jochen Kaminski

12:45