

Session Program

31 March 2025

**Eols for the Development of Sub-Detectors
and Detector Concepts for the FCC**

Trigger/DAQ/Electronics

Monday 31 March

17:00

Trigger/DAQ/Electronics: Trigger/DAQ/Electronics Session

17:00-17:01

TDAQ Architecture design and R&D

17:02-17:03

Wireless communications within the vertex detector

17:03-17:04

Embedded FPGAs for ML-based Readout

18:00