## NGT - Openlab "Optimising Floating Point Precision" Workshop

## Report of Contributions

Contribution ID: 1 Type: not specified

### Problem statement from experiments and SFT

June 13, 2025

Contribution ID: 2 Type: not specified

### **Problem statement from Theory Department**

Tuesday 1 July 2025 14:40 (10 minutes)

Presenter: FINKENRATH, Jacob

June 13, 2025

Contribution ID: 3 Type: not specified

### **Problem statement from Beams**

Tuesday 1 July 2025 14:50 (10 minutes)

**Presenter:** DI MARIA, Riccardo

Contribution ID: 4 Type: **not specified** 

### NVidia - Floating point for future GPU hardware

Tuesday 1 July 2025 15:30 (45 minutes)

**Presenter:** RODRIGUEZ, Samuel

Contribution ID: 5 Type: **not specified** 

### AMD - Floating point for future GPU hardware

Tuesday 1 July 2025 16:15 (30 minutes)

Contribution ID: 6 Type: **not specified** 

#### **Extended Precision in Convex Optimisation**

Tuesday 1 July 2025 16:45 (30 minutes)

Semidefinite Programming is a matrix-form generalisation of linear programming, and is typically tackled using Interior Point Methods. These methods are of iterative nature and at each step, a matrix inversion needs to be performed. For small or sparse matrices, direct methods like sparse Cholesky factorisation are used. For dense matrices of larger size, like the ones that arise in convex relaxations of combinatorial problems, Krylov methods like Conjugate Gradient seem a better approach. We show how, as the dual-primal central trajectory approaches the feasible set and the tentative solution becomes rank-deficient, increasing the precision accelerates the convergence (in terms of number of CG iterations).

Presenters: HERRERA-MARTI, David; GUTHMULLER, Eric; FEREYRE, Jerome

Contribution ID: 7 Type: **not specified** 

### **Discussion - Questions**

Tuesday 1 July 2025 17:15 (30 minutes)

Contribution ID: 8 Type: **not specified** 

## Using physics knowledge to improve numerical stability

Wednesday 2 July 2025 09:00 (30 minutes)

The numerically stable evaluation of scattering matrix elements near the infrared limit of gauge theories is of great importance for the success of collider physics experiments. We present a novel algorithm that utilizes double precision arithmetic and reaches higher precision than a naive quadruple precision implementation at smaller computational cost. The method is based on physics-driven modifications to propagators, vertices and external polarizations. [https://arxiv.org/abs/2406.07671]

Authors: E. Bothmann (speaker), J. M. Campbell, S. Höche, M. Knobbe

**Presenter:** BOTHMANN, Enrico

June 13, 2025

Contribution ID: 9 Type: not specified

### KIT - Double-double for pySecDec on GPU

Wednesday 2 July 2025 09:30 (30 minutes)

**Presenter:** MAGERY, Vitaly

June 13, 2025

Contribution ID: 10 Type: not specified

## An overview of mixed precision strategies for scientific computing

Wednesday 2 July 2025 10:00 (30 minutes)

The increasing support of lower precision arithmetics in hardware provides new opportunities for high performance scientific computing. However, even though low precision arithmetics can provide significant speed, communication, and energy benefits, their use in scientific computing poses the challenge of preserving the accuracy and stability of the computation. To address this issue, a variety of mixed precision algorithms that combine low and high precisions have emerged. In this talk I will give an overview of mixed precision algorithms in numerical linear algebra, with a focus on recent advances to accelerate the solution of linear systems.

**Presenter:** MARY, Theo

Contribution ID: 11 Type: not specified

### TNL: Numerical Library for Modern Parallel Architectures

Wednesday 2 July 2025 12:05 (15 minutes)

TNL (www.tnl-project.org) is a collection of building blocks that facilitate the development of efficient numerical solvers and HPC algorithms. It is implemented in C++ using modern programming paradigms in order to provide a flexible and user-friendly interface similar to, for example, the C++ Standard Template Library. TNL provides native support for modern hardware architectures such as multicore CPUs, GPUs, and distributed systems, which can be managed via a unified interface. In our presentation, we will demonstrate the main features of the library together with efficiency of the implemented algorithms and data structures.

**Presenter:** OBERHUBER, Thomas

Contribution ID: 12 Type: not specified

### **University of Prague - TNL library, high precision**

Contribution ID: 13 Type: not specified

### **Discussion - Questions**

Wednesday 2 July 2025 12:35 (30 minutes)

Contribution ID: 14 Type: not specified

## LBNL - Differential programming / algo need for precision

Wednesday 2 July 2025 14:20 (30 minutes)

Presenter: SINGH, Garima

Contribution ID: 15 Type: not specified

## Precision auto-tuning and control of accuracy in high performance simulations

Wednesday 2 July 2025 14:50 (30 minutes)

In the context of high performance computing, new architectures, becoming more and more parallel, offer higher floating-point computing power. Thus, the size of the problems considered (and with it, the number of operations) increases, becoming a possible cause for increased uncertainty. As such, estimating the reliability of a result at a reasonable cost is of major importance for numerical software. In this talk we present an overview of different approaches for accuracy analysis (guaranteed or probabilistic ones) and the related software. We also describe methods to improve the results accuracy. We present the principles of Discrete Stochastic Arithmetic (DSA) that enables one to estimate rounding errors in simulation codes. DSA can be used to control the accuracy of programs in half, single, or double precision via the CADNA library, and also in arbitrary precision via the SAM library. Thanks to DSA, the accuracy estimation and the detection of numerical instabilities can be performed in parallel codes on CPU and on GPU. Most numerical simulations are performed in double precision, and this can be costly in terms of computing time, memory transfer and energy consumption. We present tools for floating-point auto-tuning that aim at reducing the numerical formats used in simulation programs.

Presenter: JÉZÉQUEL, Fabienne (LIP6, Sorbonne Université)

Contribution ID: 16 Type: not specified

## **Emulating Matrix Multiplication Using Mixed-Precision Computation**

Wednesday 2 July 2025 16:20 (30 minutes)

This talk introduces a method for emulating matrix multiplication through mixed-precision computation. As exemplified by the Matrix Engine on GPUs, low-precision arithmetic can be performed significantly faster than conventional FP32 or FP64 operations. We present Ozaki Scheme I and II, which leverage low-precision arithmetic to achieve accuracy comparable to standard FP64, and discuss their numerical performance.

**Presenter:** OZAKI, Katsuhisa

Contribution ID: 17 Type: not specified

### **Discussion - Questions**

Wednesday 2 July 2025 10:50 (15 minutes)

Contribution ID: 18 Type: not specified

## University of Budapest - Tensor networks, FP64 emulation

Wednesday 2 July 2025 11:35 (30 minutes)

Contribution ID: 19 Type: not specified

### **Experiences with CADNA and the Madgraph5 Event Generator**

Wednesday 2 July 2025 15:20 (30 minutes)

This talk presents a summer student project that explored the numerical stability of MadGraph5 using CADNA. It focuses on how CADNA's warning system and its ability to quantify floating-point precision were used to assess whether MadGraph5 can operate reliably with single-precision floating-point numbers.

Presenter: HAGEBOECK, Stephan

Contribution ID: 20 Type: not specified

### **Discussion - Questions**

Wednesday 2 July 2025 16:50 (20 minutes)

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Closing Session

Contribution ID: 21 Type: not specified

### **Closing Session**

Contribution ID: 22 Type: not specified

### **Closing Session**

Wednesday 2 July 2025 17:10 (10 minutes)

Contribution ID: 23

Type: not specified

# Float32 Expansions –A Possible Answer for Scientific Computing in the Era of Al-Driven GPU Development

Wednesday 2 July 2025 12:20 (15 minutes)

In recent years, the emergence of large language models has led GPU vendors to prioritize performance improvements for lower-precision arithmetic, often at the expense of continued development for Float64. Meanwhile, scientific computing has increasingly relied on GPGPU acceleration, where double precision is still essential. Multi-word expansions for single-precision floating point numbers may offer a viable alternative—providing comparable or even superior precision while achieving better performance than native double precision. In this talk, we will present results using a CUDA-enabled, templated, and ported version of the QD library within the TNL framework, applied to existing numerical algorithms.

Presenter: STLOUKAL, František

Contribution ID: 24 Type: not specified

### Problem statement from experiments and SFT

Tuesday 1 July 2025 14:10 (30 minutes)

Contribution ID: 25 Type: not specified

### **Welcome Session**

Tuesday 1 July 2025 14:00 (10 minutes)

**Presenter:** ROISER, Stefan (CERN)

Nicolo's talk

Contribution ID: 26 Type: not specified

### Nicolo's talk

Wednesday 2 July 2025 10:30 (20 minutes)

**Presenter:** GHIELMETTI, Nicolo (CERN)