## WIT2012 Workshop on Intelligent Trackers



Contribution ID: 46 Type: not specified

## A Self Seeded First Level Track Trigger for ATLAS

Saturday, 5 May 2012 09:00 (30 minutes)

For the planned high luminosity upgrade of the Large Hadron Collider, aiming to increase the instantaneous luminosity to 5-7 x  $10^34/\text{cm}^2/\text{s}$ , the implementation of a first level track trigger has been proposed, which could be installed in the year 2020/21 along with the complete renewal of the ATLAS Inner Detector.

The fast readout of the hit information from the Inner Detector is considered as main challenge of such a track trigger. Different concepts for the implementation of a first level trigger are currently studied within the ATLAS collaboration.

The so called "Self Seeded" track trigger concept exploits fast front-end filtering algorithms based on cluster size reconstruction and fast vector tracking to select hits associated to high momentum tracks. Simulation studies have been performed and results on efficiencies and expected bandwidth reductions are presented for different layouts.

Possible hardware implementations of the first level track processor able to reconstruct all high momentum tracks in every collision within a latency of 1 mus will be discussed.

Primary author: SCHOENING, Andre (Physikalisches Institut-Ruprecht-Karls-Universitaet Heidelberg-U)

**Co-authors:** JOHN, Arno (Ruprecht-Karls-Universitaet Heidelberg (DE)); SCHMITT, Sebastian (Ruprecht-Karls-Universitaet Heidelberg (DE))

**Presenters:** SCHOENING, Andre (Physikalisches Institut-Ruprecht-Karls-Universitaet Heidelberg-U); SCHMITT, Sebastian (Ruprecht-Karls-Universitaet Heidelberg (DE))

Session Classification: Real time pattern-recognition and advanced algorithms

Track Classification: Real time pattern-recognition and advanced algorithms